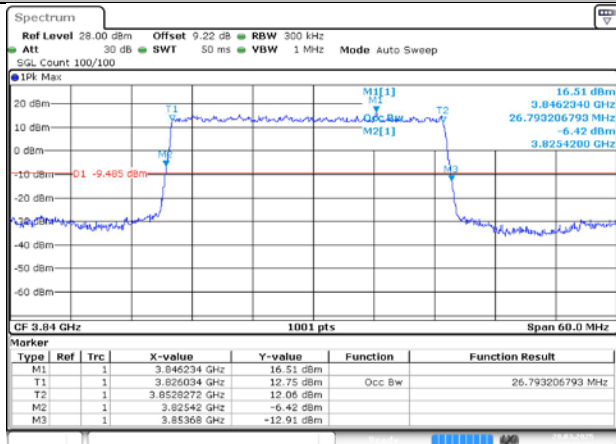


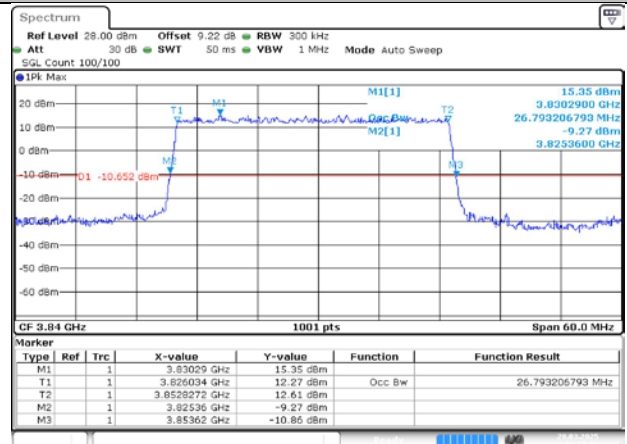
n77A / 30KHz / 30MHz

MCH / DFT-Pi2BPSK / Outer_Full



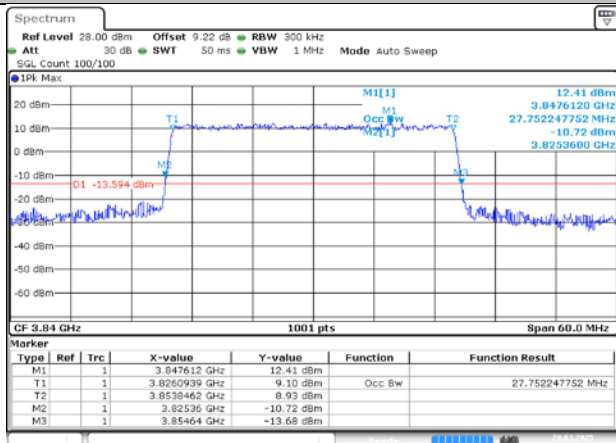
Date: 28.MAR.2025 00:16:43

MCH / DFT-QPSK / Outer_Full



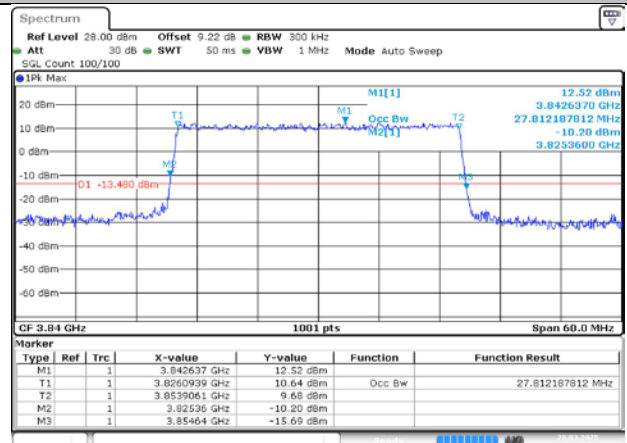
Date: 28.MAR.2025 00:17:02

MCH / CP-QPSK / Outer_Full



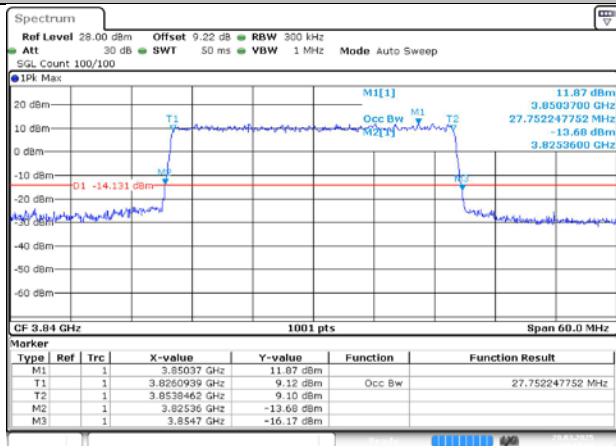
Date: 28.MAR.2025 00:17:39

MCH / CP-16QAM / Outer_Full



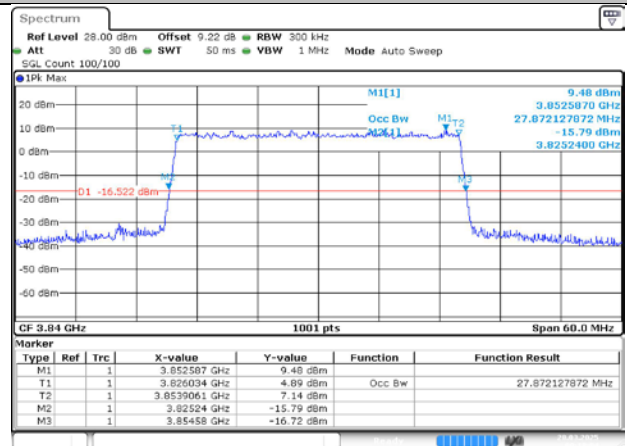
Date: 28.MAR.2025 00:17:47

MCH / CP-64QAM / Outer_Full



Date: 28.MAR.2025 00:17:54

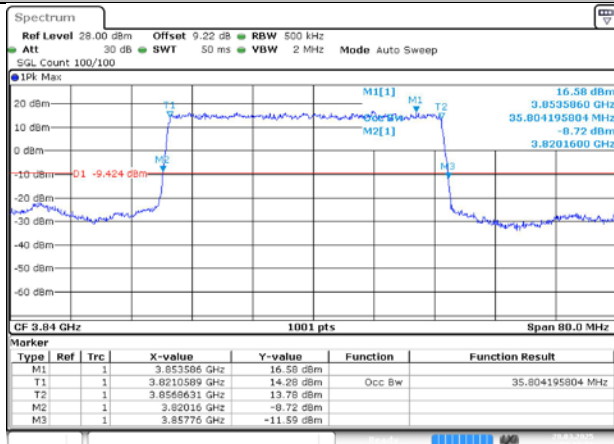
MCH / CP-256QAM / Outer_Full



Date: 28.MAR.2025 00:18:04

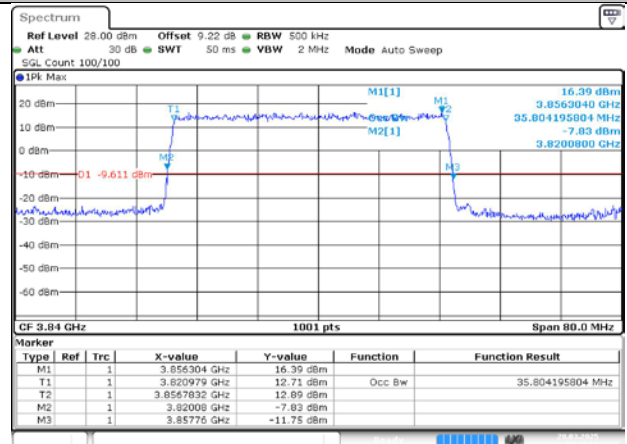
n77A / 30KHz / 40MHz

MCH / DFT-Pi2BPSK / Outer_Full



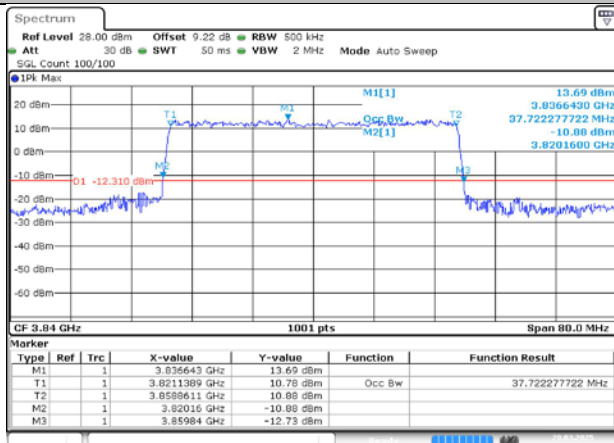
Date: 28.MAR.2025 00:18:32

MCH / DFT-QPSK / Outer_Full



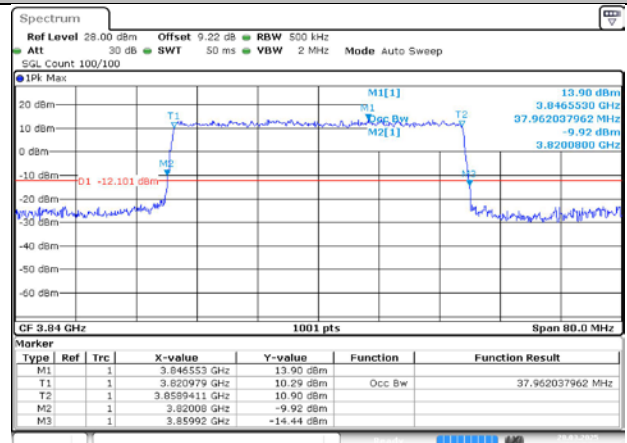
Date: 28.MAR.2025 00:18:50

MCH / CP-QPSK / Outer_Full



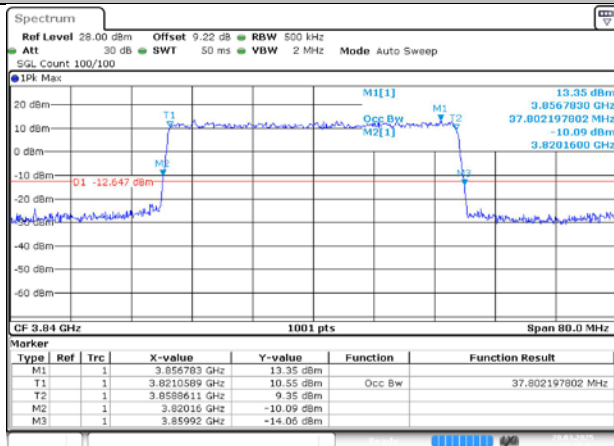
Date: 28.MAR.2025 00:19:27

MCH / CP-16QAM / Outer_Full



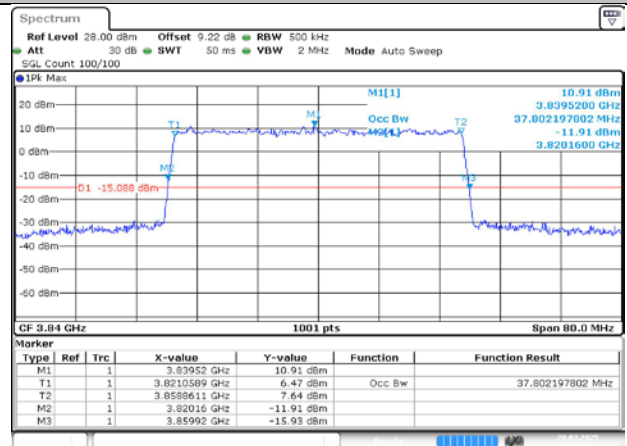
Date: 28.MAR.2025 00:19:35

MCH / CP-64QAM / Outer_Full



Date: 28.MAR.2025 00:19:43

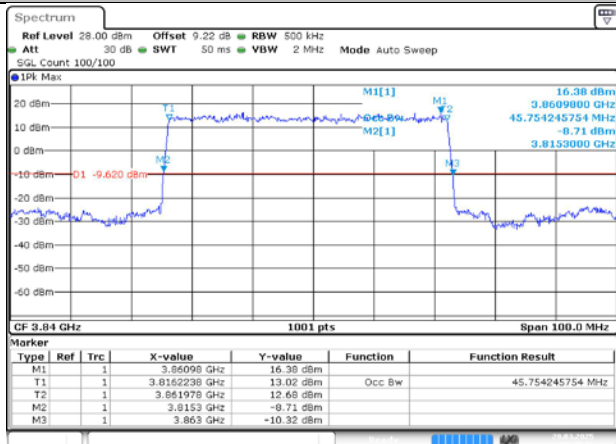
MCH / CP-256QAM / Outer_Full



Date: 28.MAR.2025 00:19:53

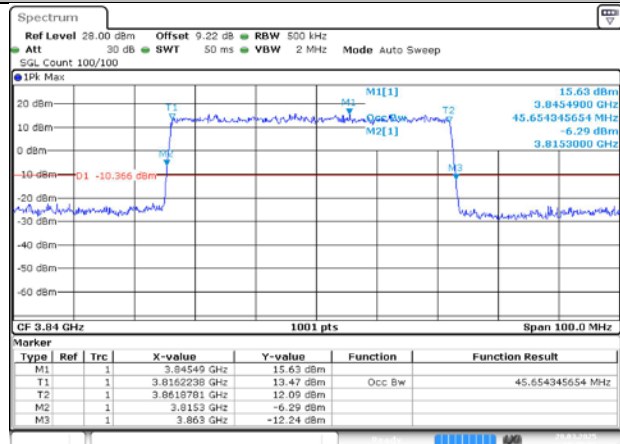
n77A / 30KHz / 50MHz

MCH / DFT-Pi2BPSK / Outer_Full



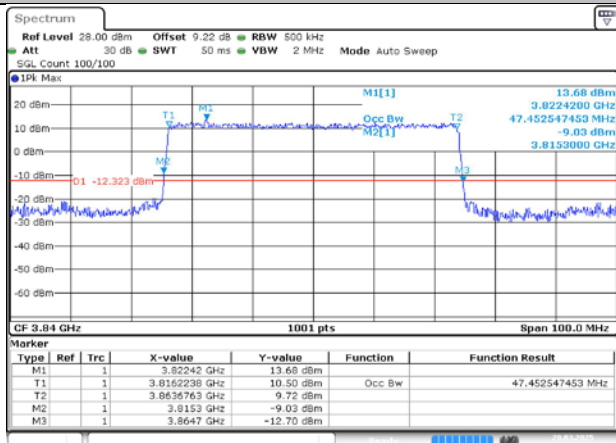
Date: 28.MAR.2025 00:20:15

MCH / DFT-QPSK / Outer_Full



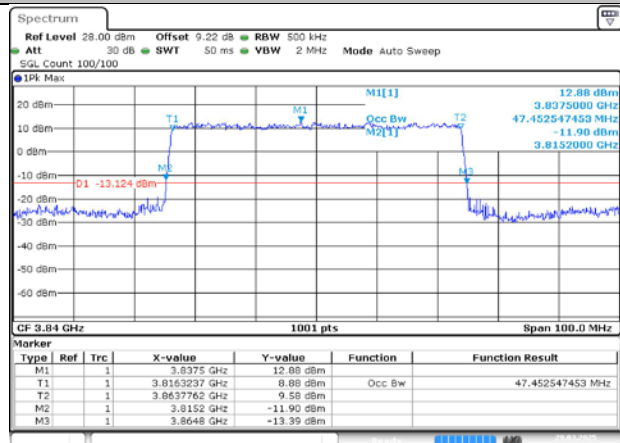
Date: 28.MAR.2025 00:20:33

MCH / CP-QPSK / Outer_Full



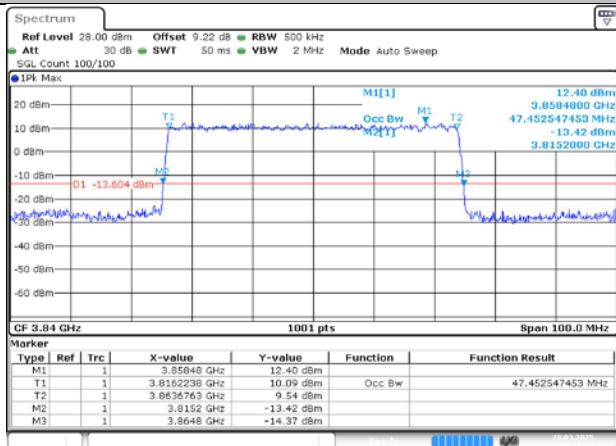
Date: 28.MAR.2025 00:21:09

MCH / CP-16QAM / Outer_Full



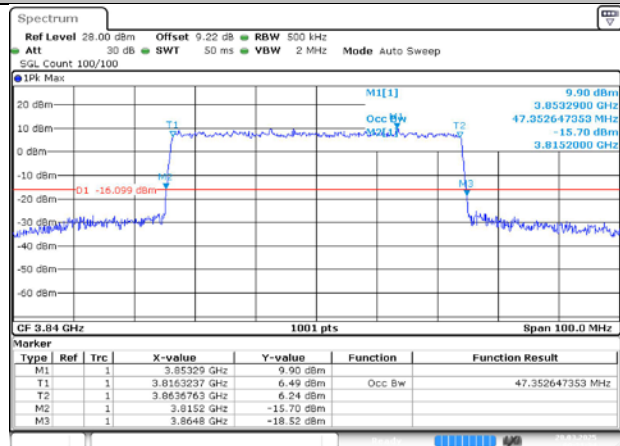
Date: 28.MAR.2025 00:21:17

MCH / CP-64QAM / Outer_Full



Date: 28.MAR.2025 00:21:25

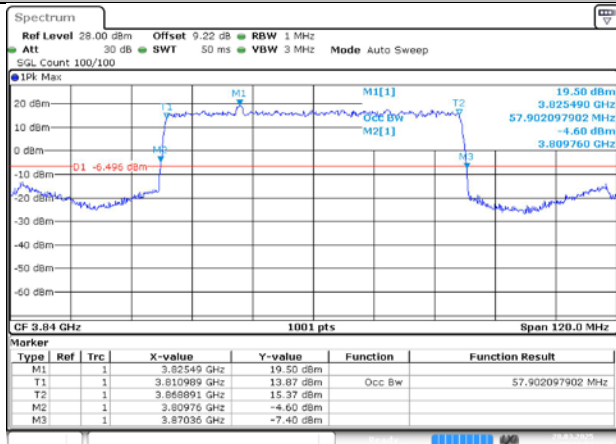
MCH / CP-256QAM / Outer_Full



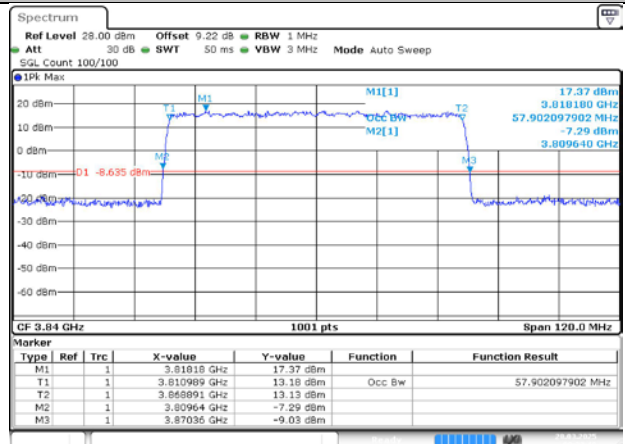
Date: 28.MAR.2025 00:21:34

n77A / 30KHz / 60MHz

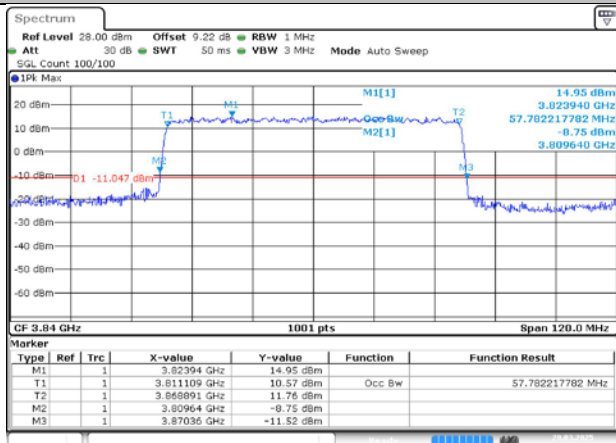
MCH / DFT-Pi2BPSK / Outer_Full



MCH / DFT-QPSK / Outer_Full



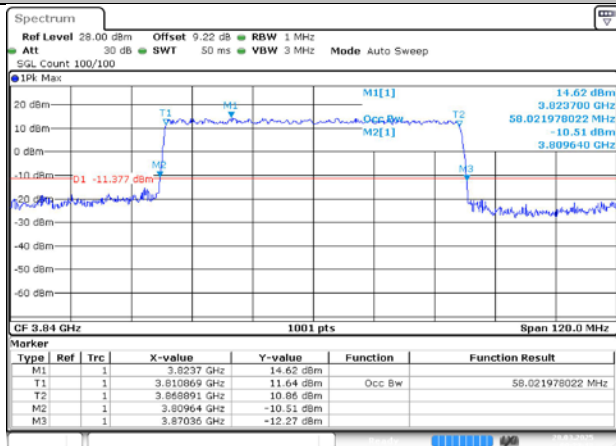
MCH / CP-QPSK / Outer_Full



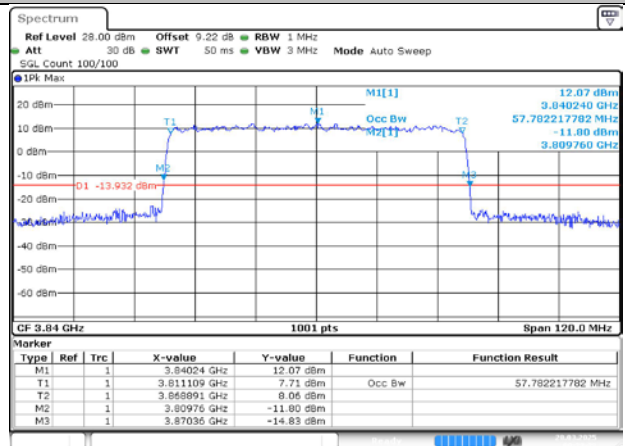
MCH / CP-16QAM / Outer_Full



MCH / CP-64QAM / Outer_Full

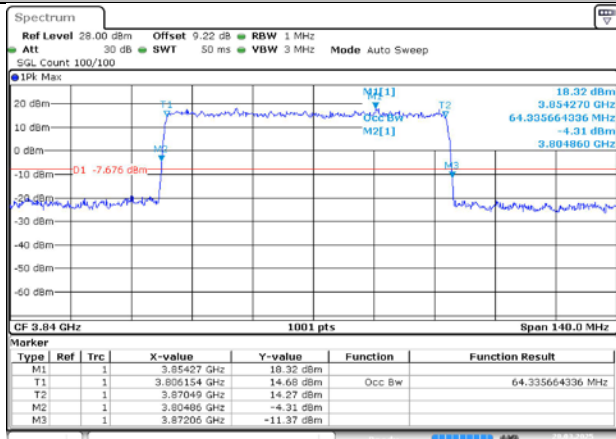


MCH / CP-256QAM / Outer_Full



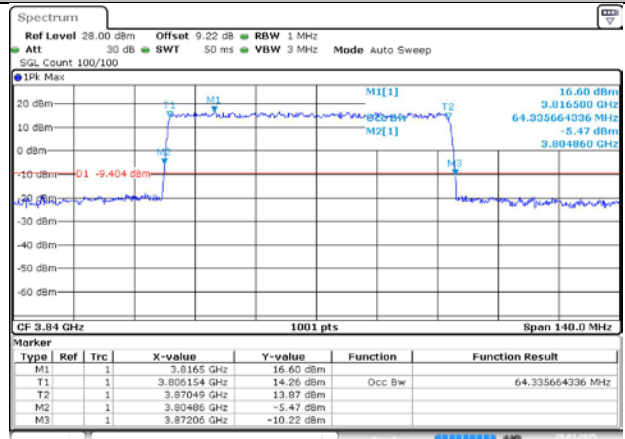
n77A / 30KHz / 70MHz

MCH / DFT-Pi2BPSK / Outer_Full



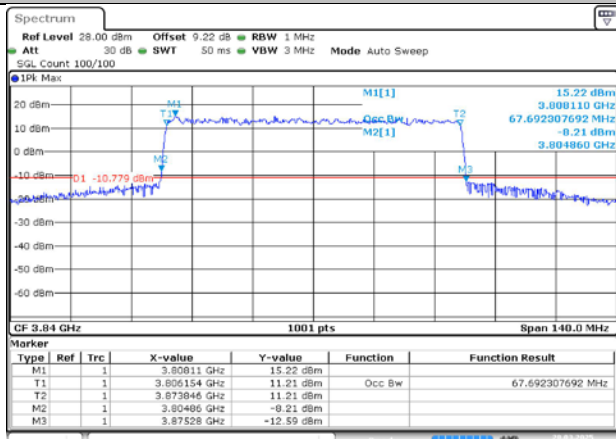
Date: 28.MAR.2025 00:23:43

MCH / DFT-QPSK / Outer_Full



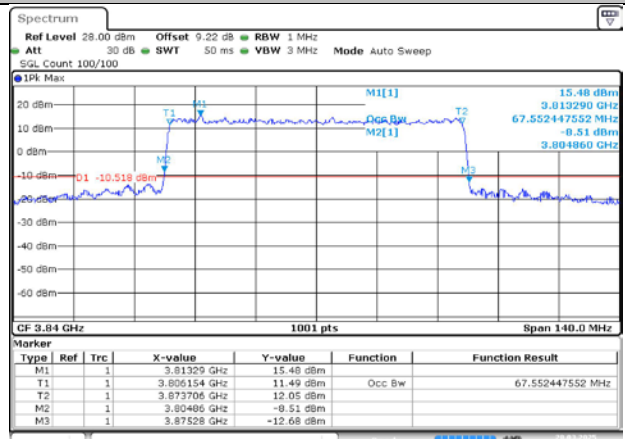
Date: 28.MAR.2025 00:24:02

MCH / CP-QPSK / Outer_Full



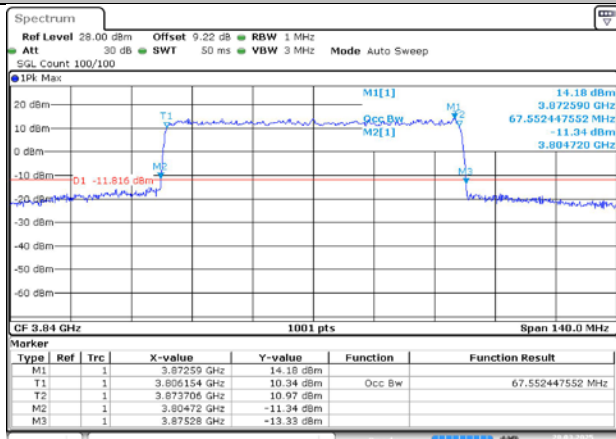
Date: 28.MAR.2025 00:24:39

MCH / CP-16QAM / Outer_Full



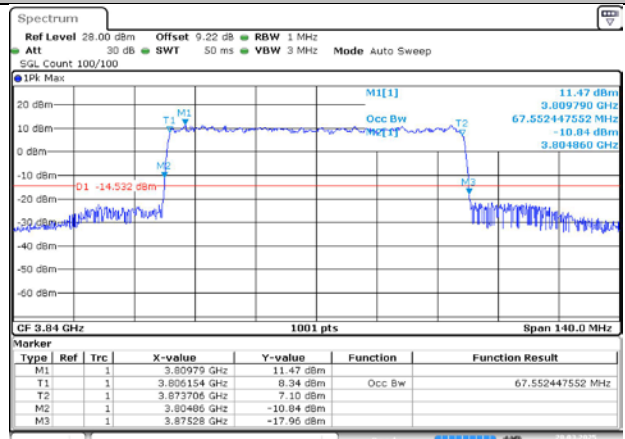
Date: 28.MAR.2025 00:24:47

MCH / CP-64QAM / Outer_Full



Date: 28.MAR.2025 00:24:55

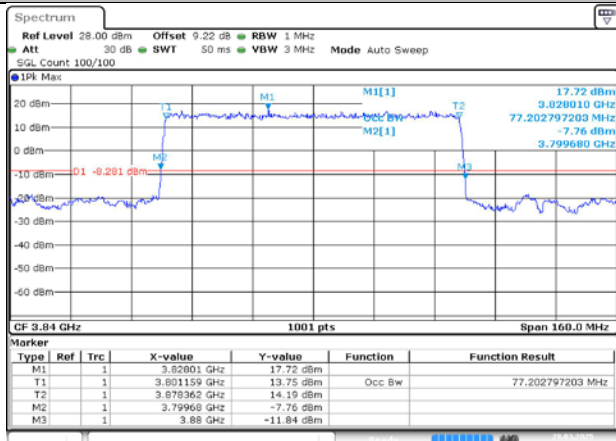
MCH / CP-256QAM / Outer_Full



Date: 28.MAR.2025 00:25:05

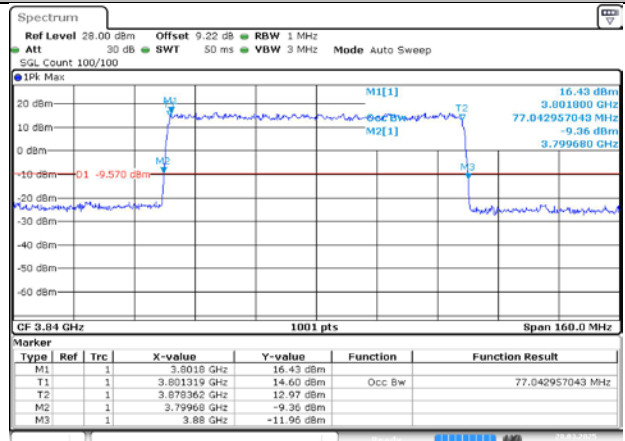
n77A / 30KHz / 80MHz

MCH / DFT-Pi2BPSK / Outer_Full



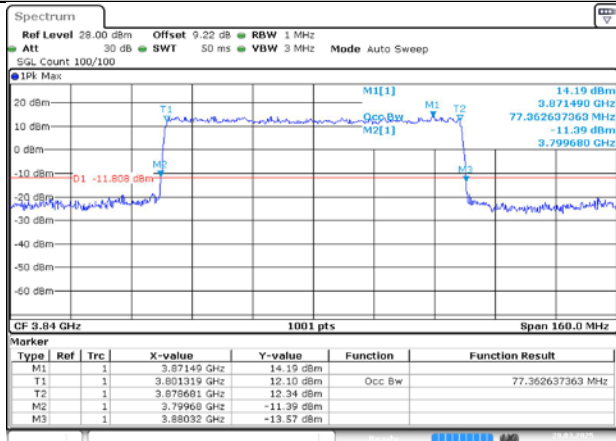
Date: 28.MAR.2025 00:25:26

MCH / DFT-QPSK / Outer_Full



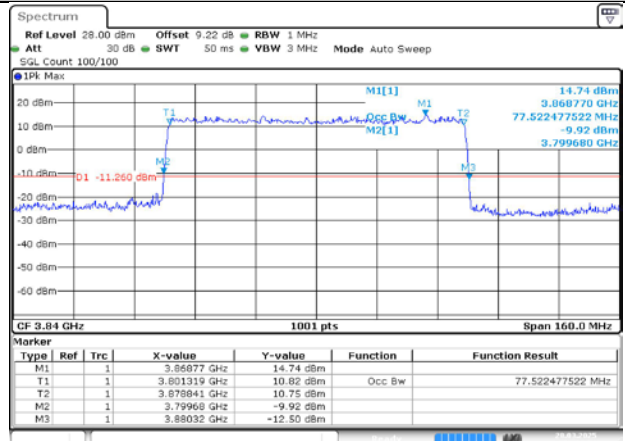
Date: 28.MAR.2025 00:25:45

MCH / CP-QPSK / Outer_Full



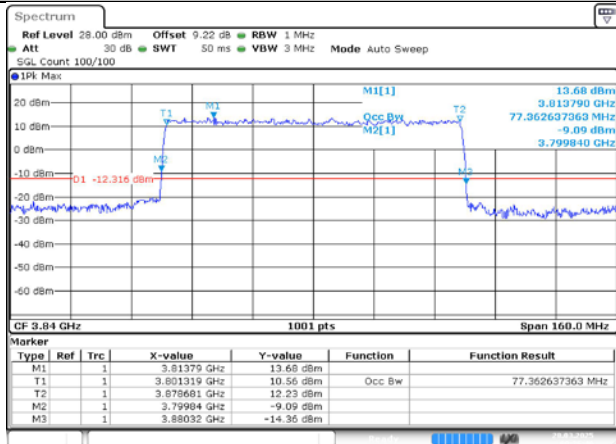
Date: 28.MAR.2025 00:26:23

MCH / CP-16QAM / Outer_Full



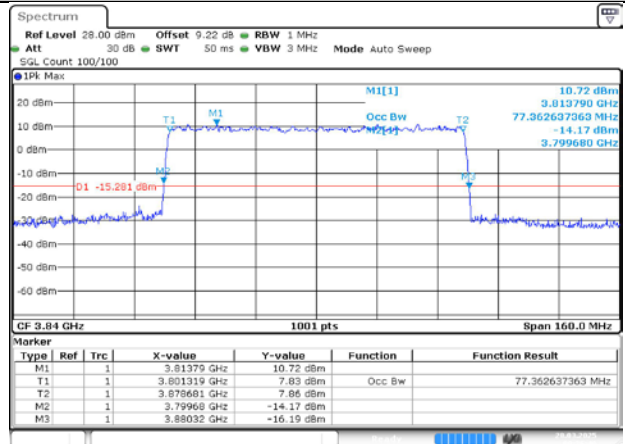
Date: 28.MAR.2025 00:26:32

MCH / CP-64QAM / Outer_Full



Date: 28.MAR.2025 00:26:40

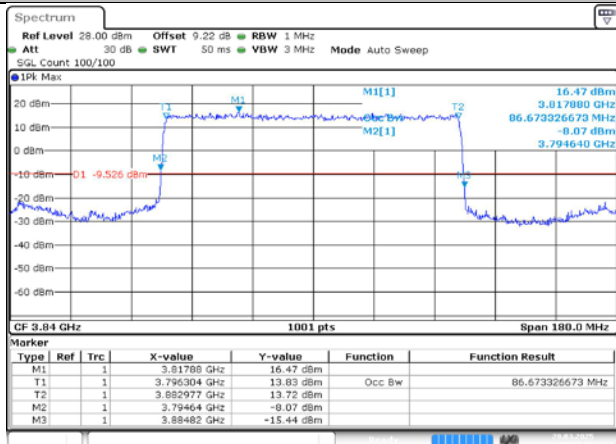
MCH / CP-256QAM / Outer_Full



Date: 28.MAR.2025 00:26:49

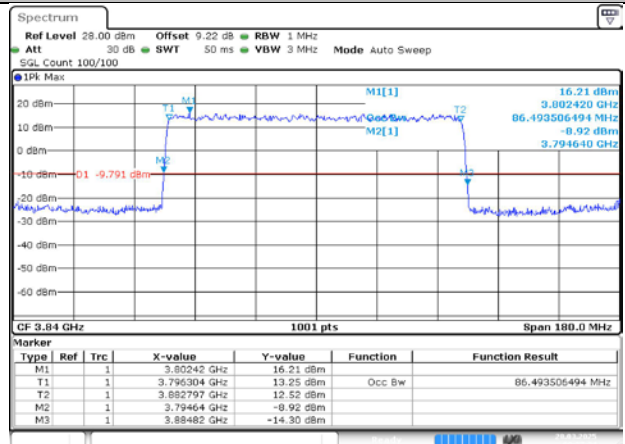
n77A / 30KHz / 90MHz

MCH / DFT-Pi2BPSK / Outer_Full



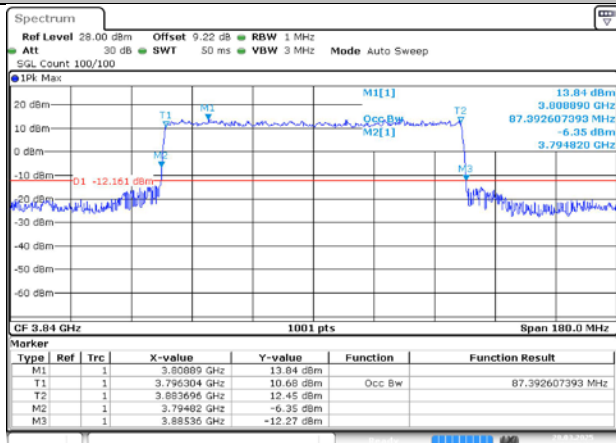
Date: 28.MAR.2025 00:27:16

MCH / DFT-QPSK / Outer_Full



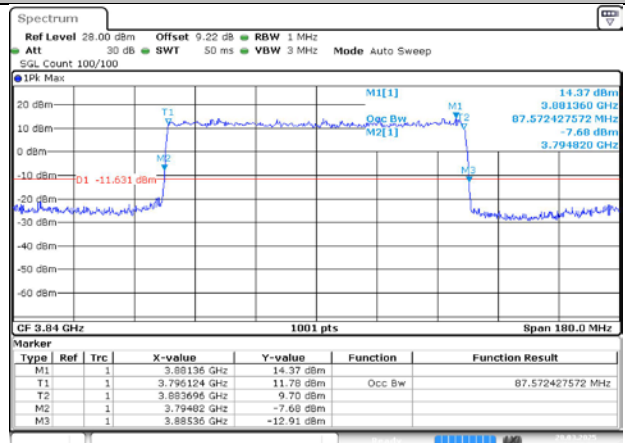
Date: 28.MAR.2025 00:27:34

MCH / CP-QPSK / Outer_Full



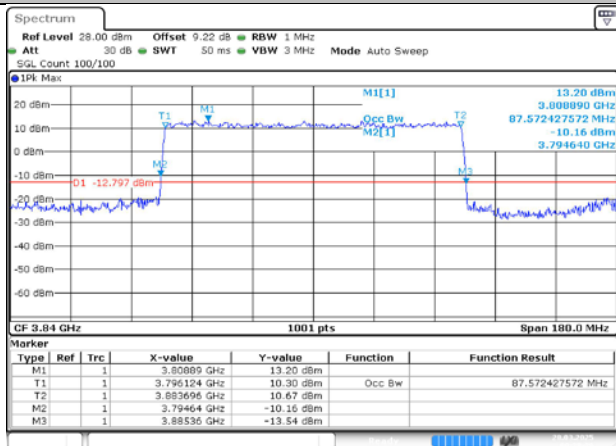
Date: 28.MAR.2025 00:28:11

MCH / CP-16QAM / Outer_Full



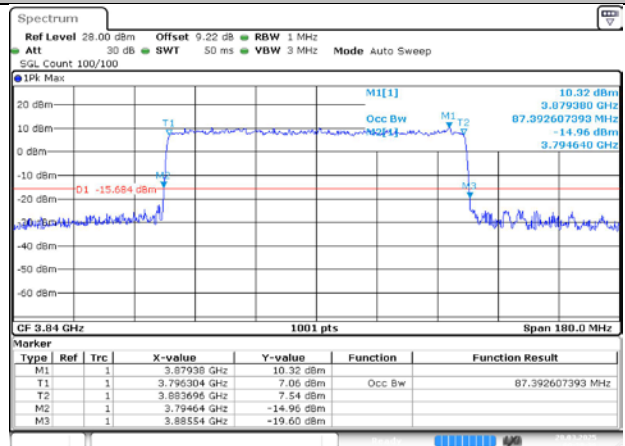
Date: 28.MAR.2025 00:28:20

MCH / CP-64QAM / Outer_Full



Date: 28.MAR.2025 00:28:29

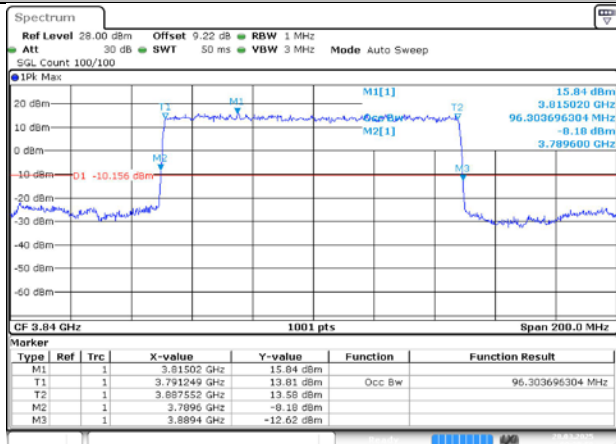
MCH / CP-256QAM / Outer_Full



Date: 28.MAR.2025 00:28:38

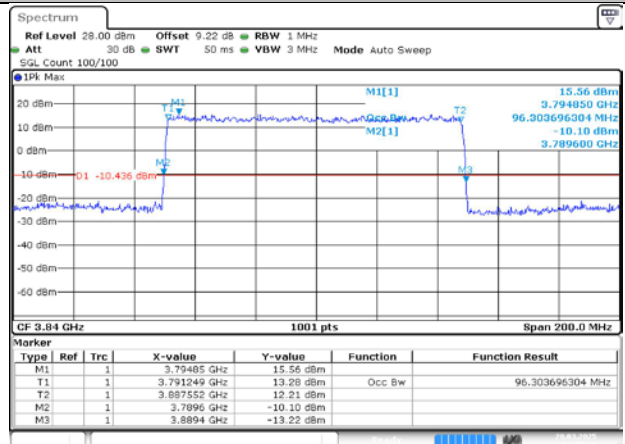
n77A / 30KHz / 100MHz

MCH / DFT-Pi2BPSK / Outer_Full



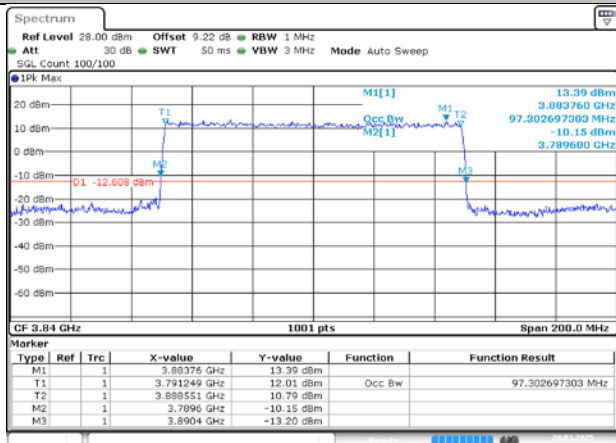
Date: 28.MAR.2025 00:29:00

MCH / DFT-QPSK / Outer_Full



Date: 28.MAR.2025 00:29:18

MCH / CP-QPSK / Outer_Full



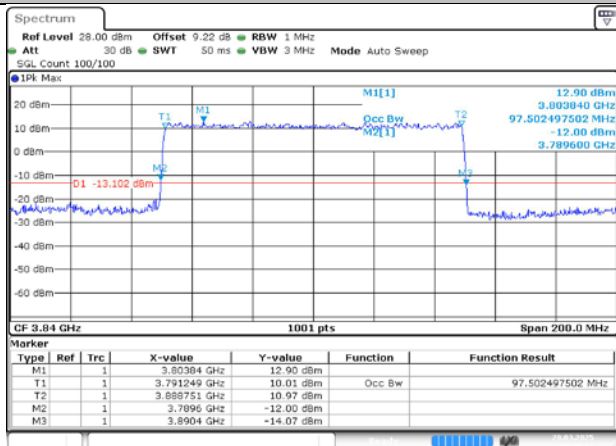
Date: 28.MAR.2025 00:29:56

MCH / CP-16QAM / Outer_Full



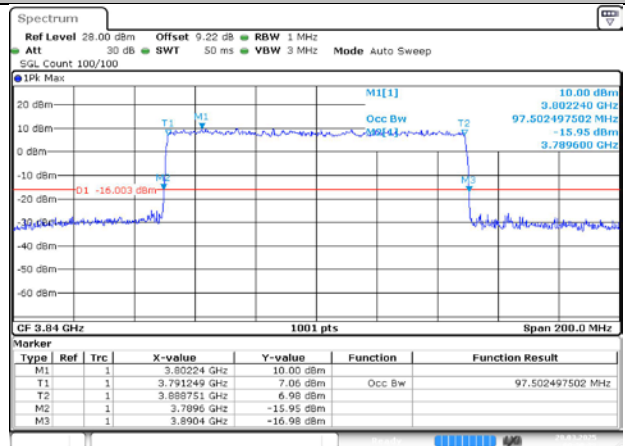
Date: 28.MAR.2025 00:30:05

MCH / CP-64QAM / Outer_Full



Date: 28.MAR.2025 00:30:13

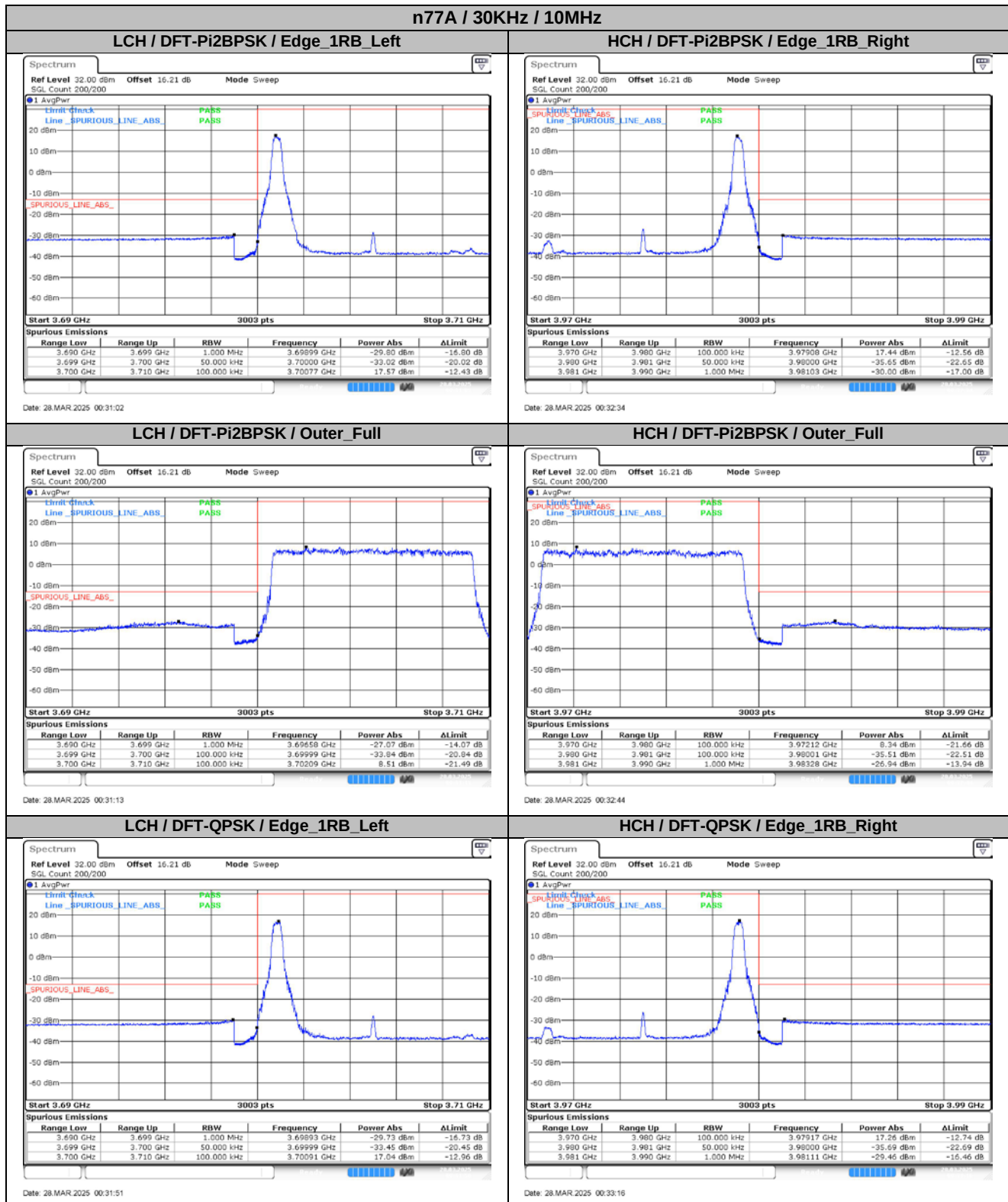
MCH / CP-256QAM / Outer_Full

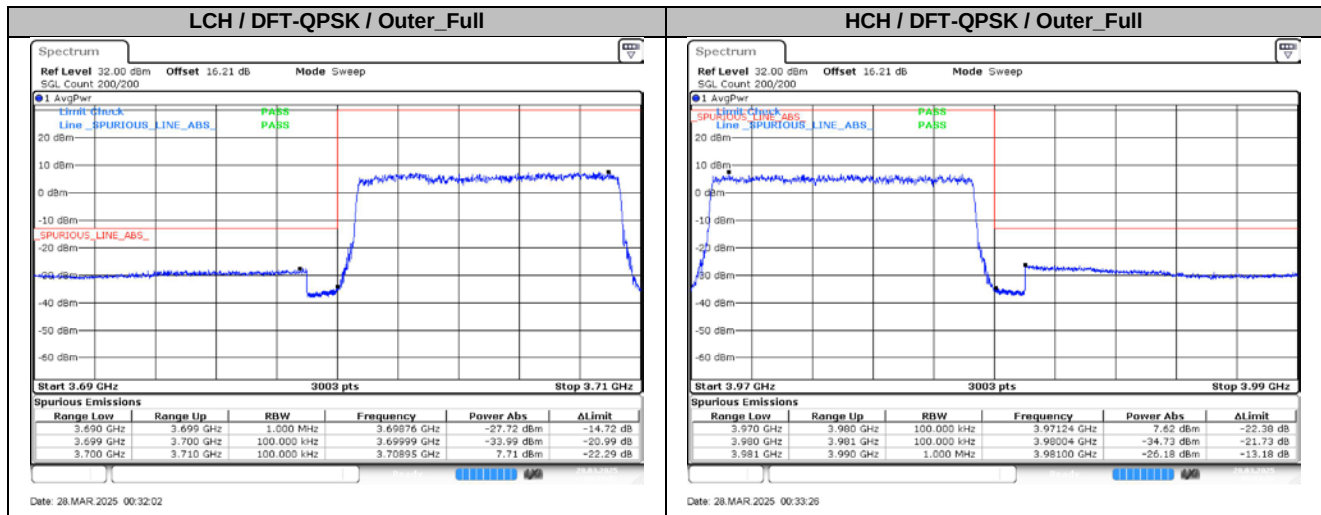


Date: 28.MAR.2025 00:30:23

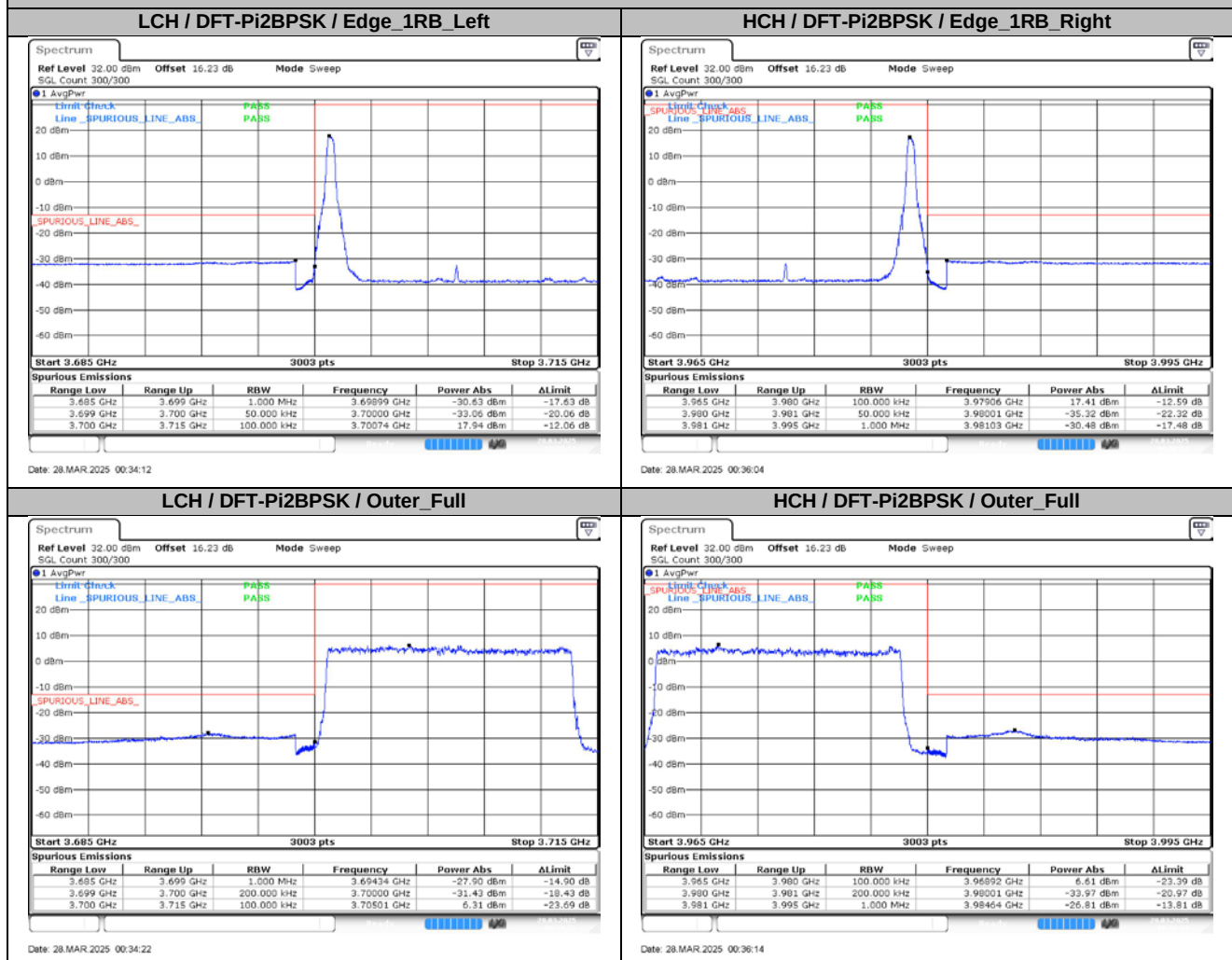
5. Conducted Band Edges

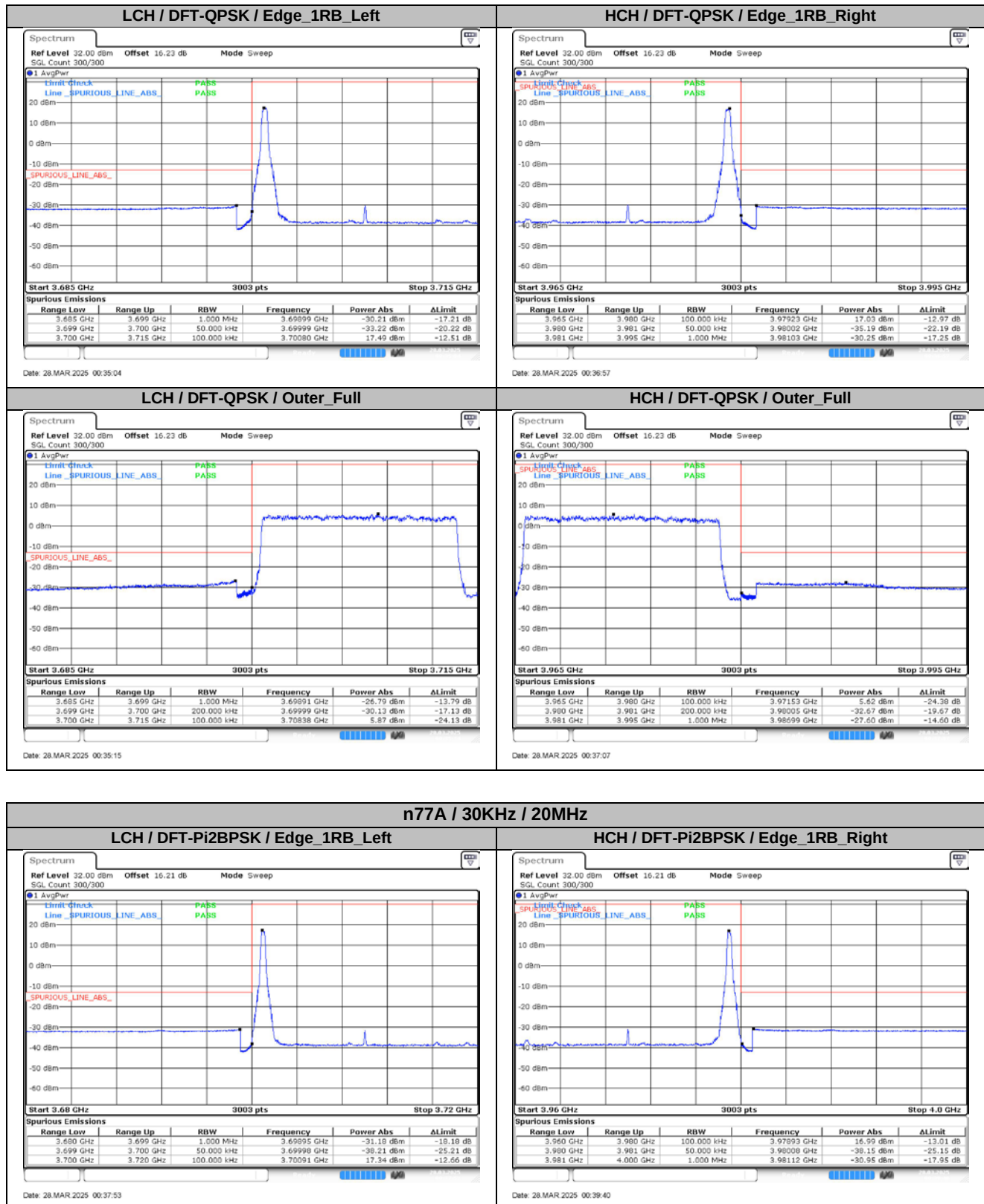
5.1. Test Plots for SCS=30KHz

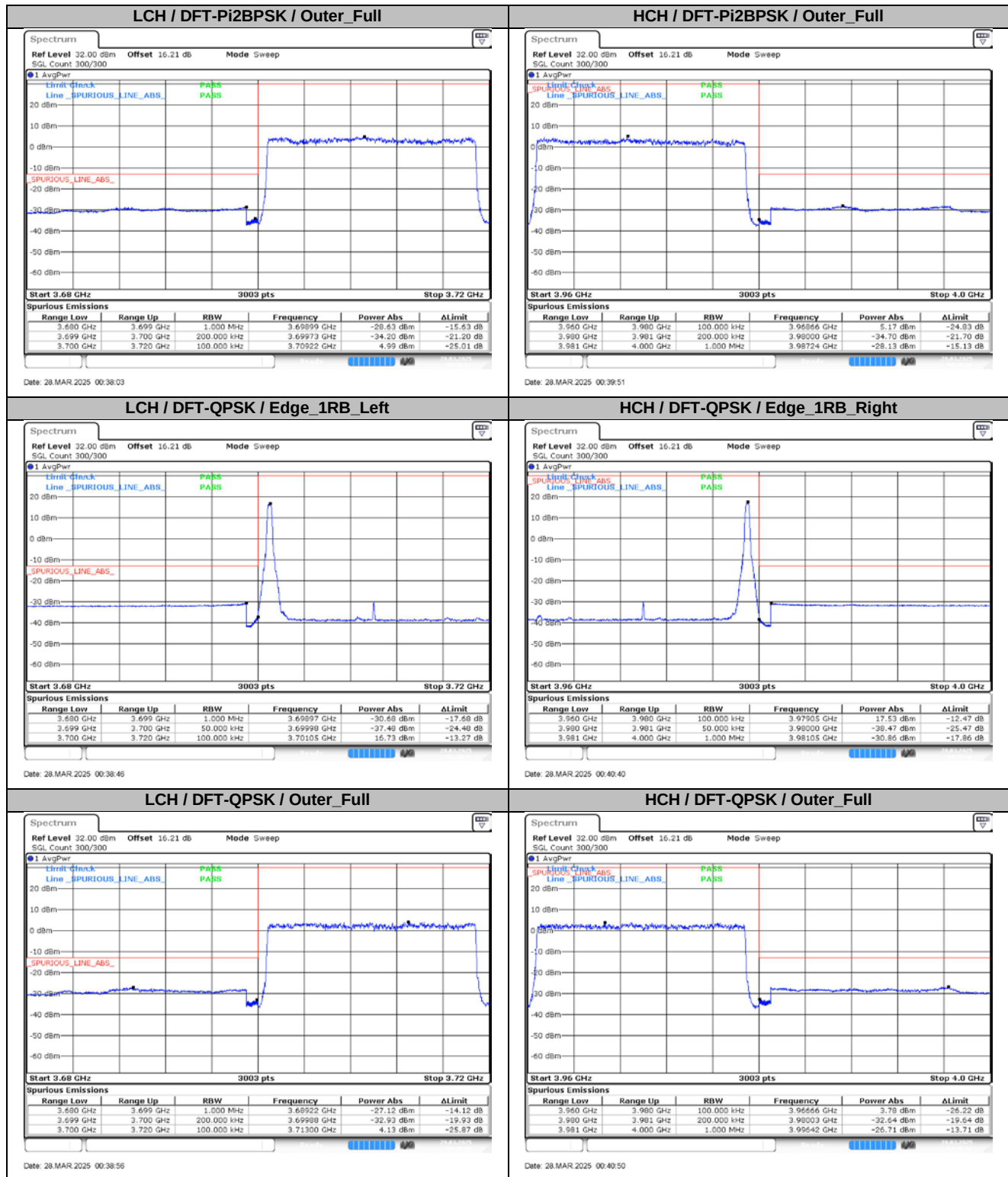




n77A / 30KHz / 15MHz

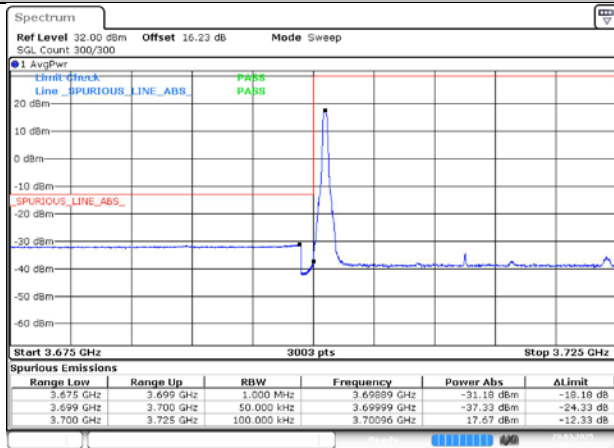






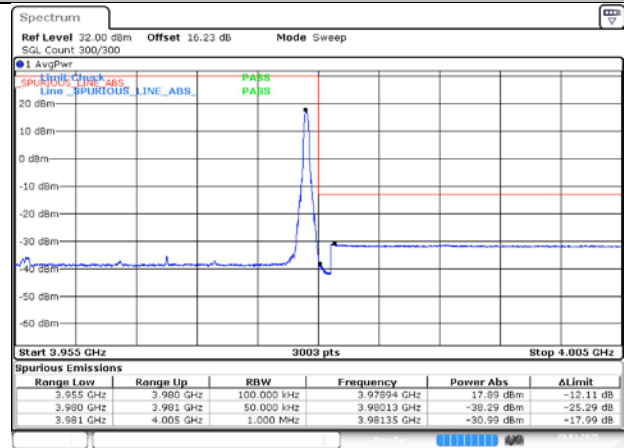
n77A / 30KHz / 25MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



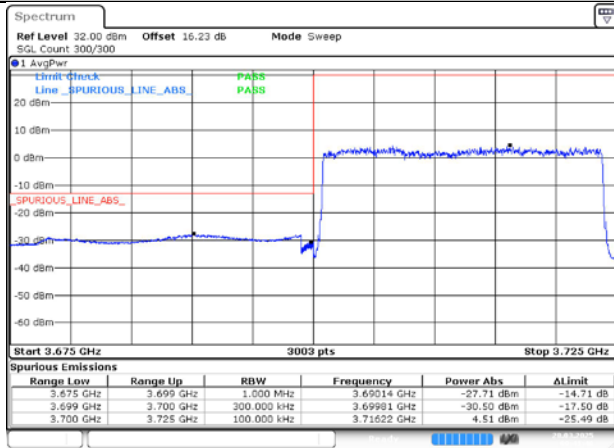
Date: 28.MAR.2025 00:41:37

HCH / DFT-Pi2BPSK / Edge_1RB_Right



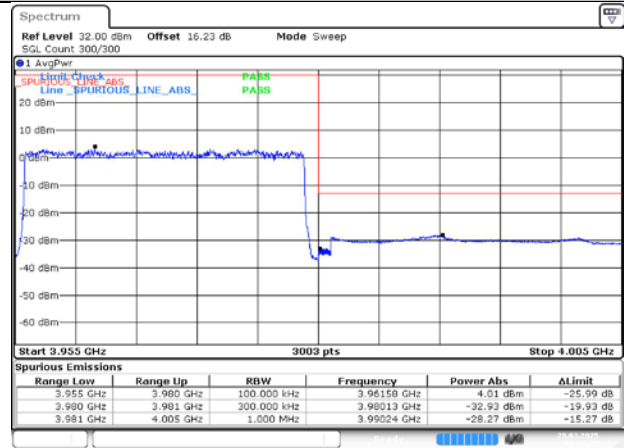
Date: 28.MAR.2025 00:43:26

LCH / DFT-Pi2BPSK / Outer_Full



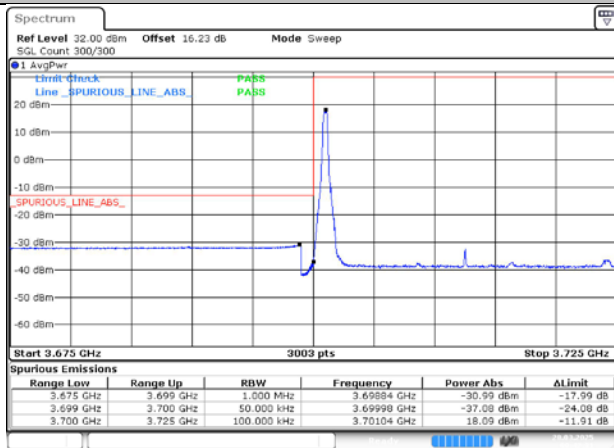
Date: 28.MAR.2025 00:41:47

HCH / DFT-Pi2BPSK / Outer_Full



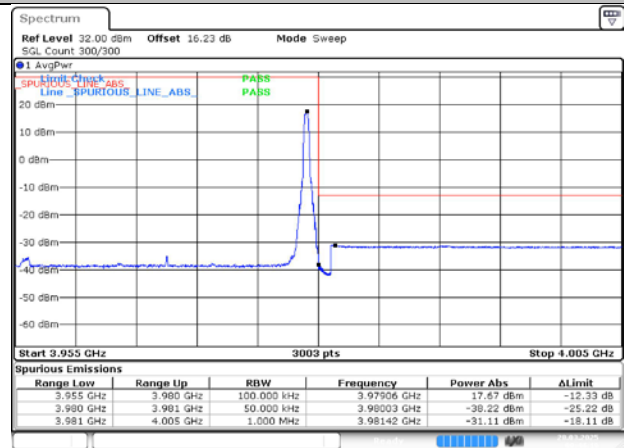
Date: 28.MAR.2025 00:43:36

LCH / DFT-QPSK / Edge_1RB_Left



Date: 28.MAR.2025 00:42:31

HCH / DFT-QPSK / Edge_1RB_Right



Date: 28.MAR.2025 00:44:19

