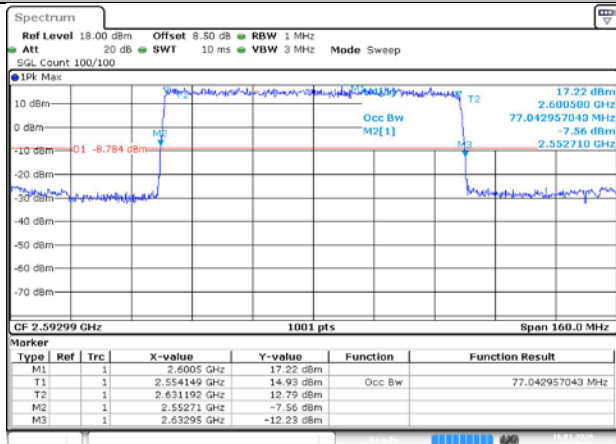


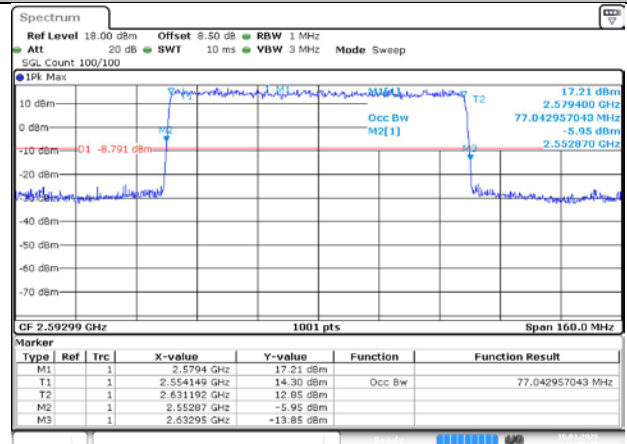
n41A / 30KHz / 80MHz

MCH / DFT-Pi2BPSK / Outer_Full



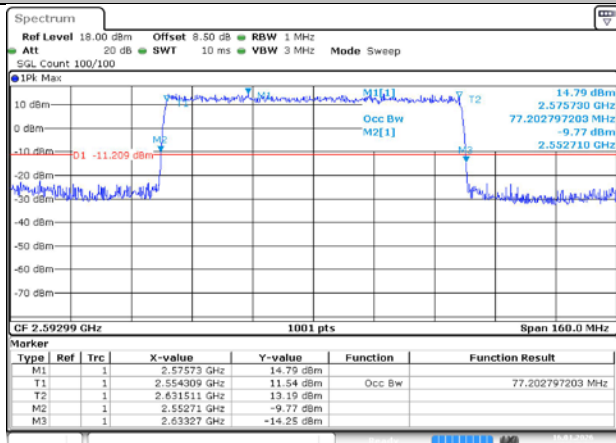
Date: 16 JAN 2026 16:45:09

MCH / DFT-QPSK / Outer_Full



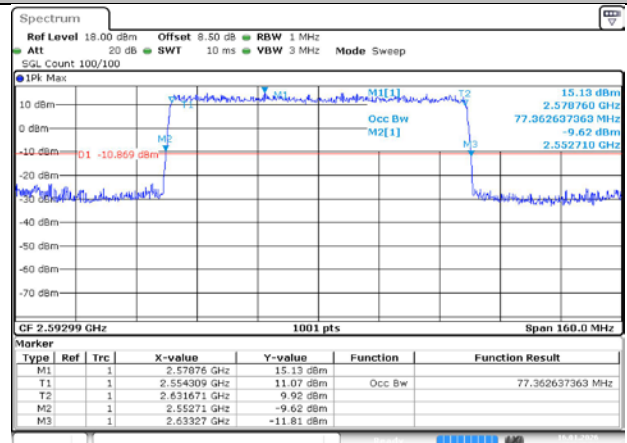
Date: 16 JAN 2026 16:45:14

MCH / CP-QPSK / Outer_Full



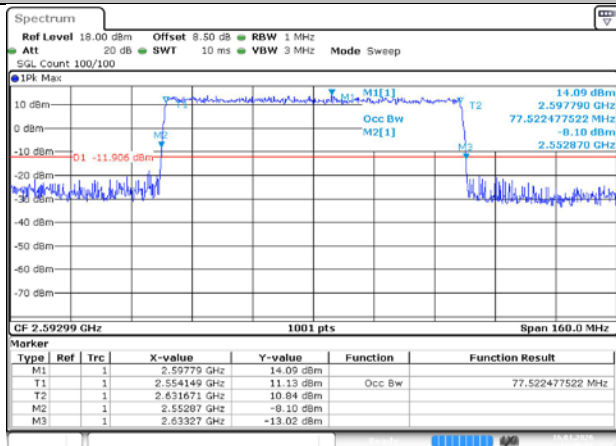
Date: 16 JAN 2026 16:45:20

MCH / CP-16QAM / Outer_Full



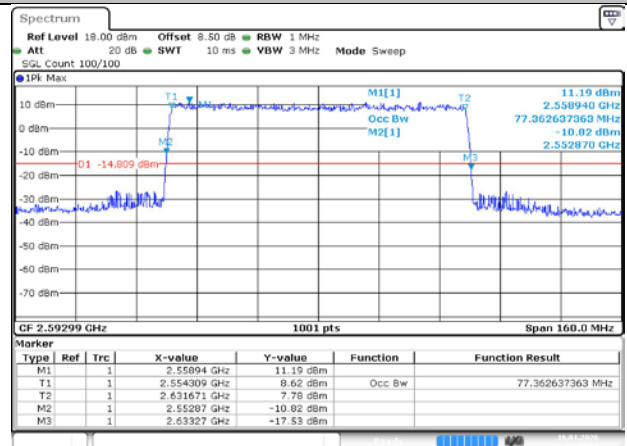
Date: 16 JAN 2026 16:45:24

MCH / CP-64QAM / Outer_Full



Date: 16 JAN 2026 16:45:28

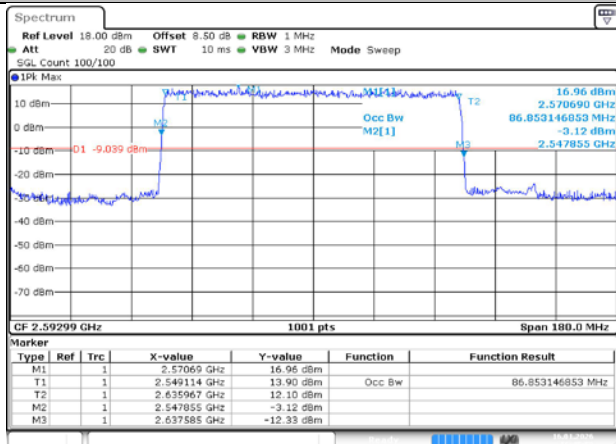
MCH / CP-256QAM / Outer_Full



Date: 16 JAN 2026 16:45:34

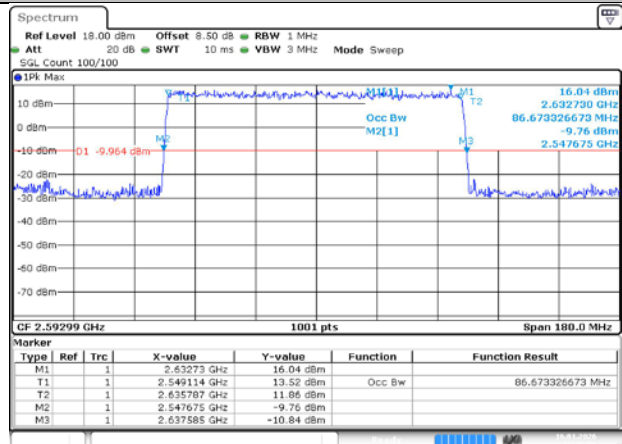
n41A / 30KHz / 90MHz

MCH / DFT-Pi2BPSK / Outer_Full



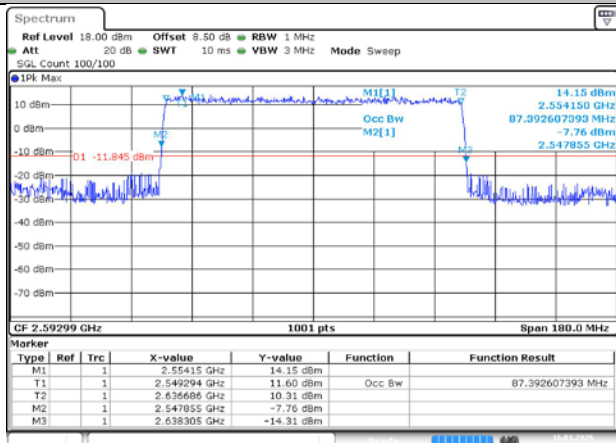
Date: 16 JAN 2026 16:45:53

MCH / DFT-QPSK / Outer_Full



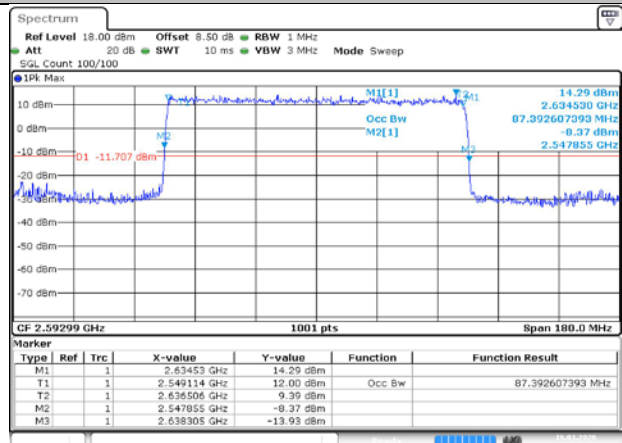
Date: 16 JAN 2026 16:46:01

MCH / CP-QPSK / Outer_Full



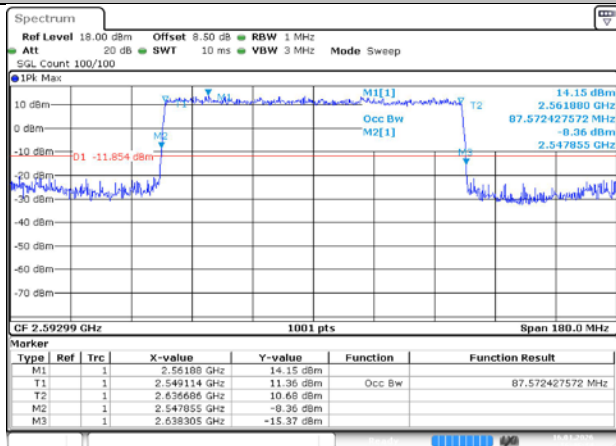
Date: 16 JAN 2026 16:46:09

MCH / CP-16QAM / Outer_Full



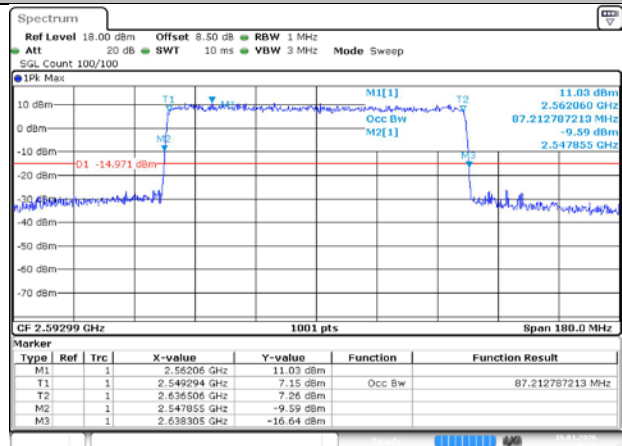
Date: 16 JAN 2026 16:46:11

MCH / CP-64QAM / Outer_Full



Date: 16 JAN 2026 16:46:15

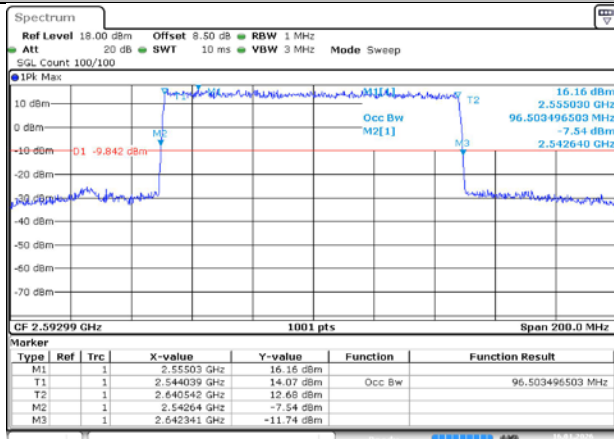
MCH / CP-256QAM / Outer_Full



Date: 16 JAN 2026 16:46:21

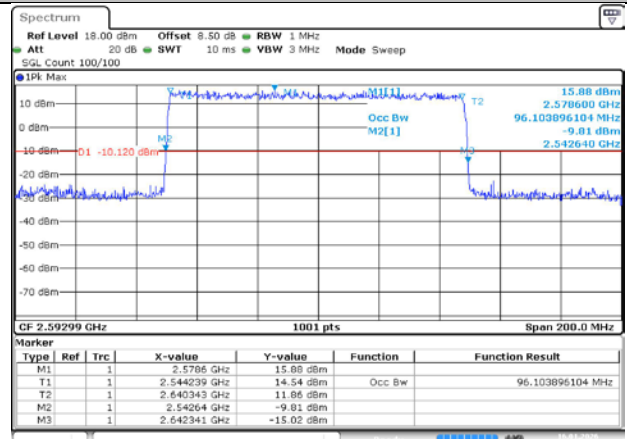
n41A / 30KHz / 100MHz

MCH / DFT-Pi2BPSK / Outer_Full



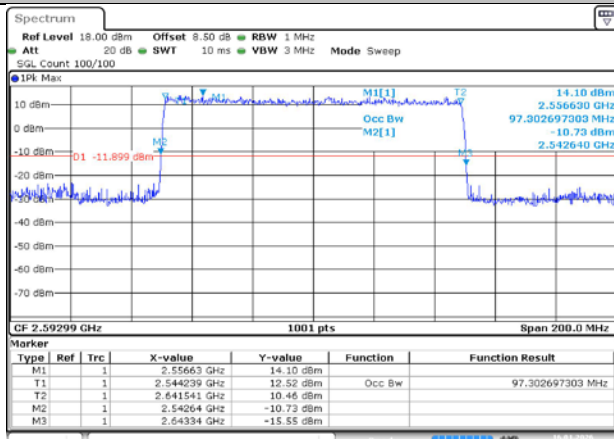
Date: 16 JAN 2026 16:46:32

MCH / DFT-QPSK / Outer_Full



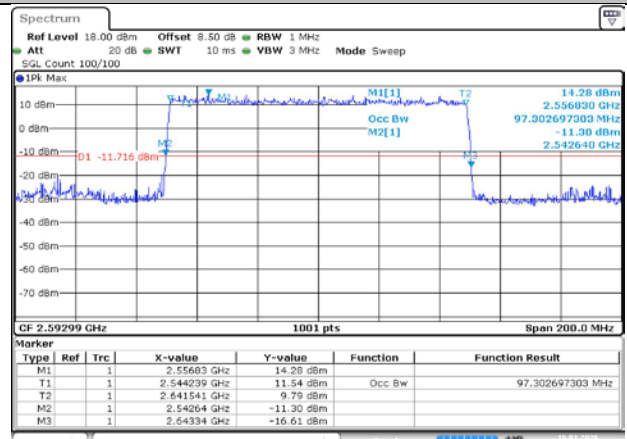
Date: 16 JAN 2026 16:46:40

MCH / CP-QPSK / Outer_Full



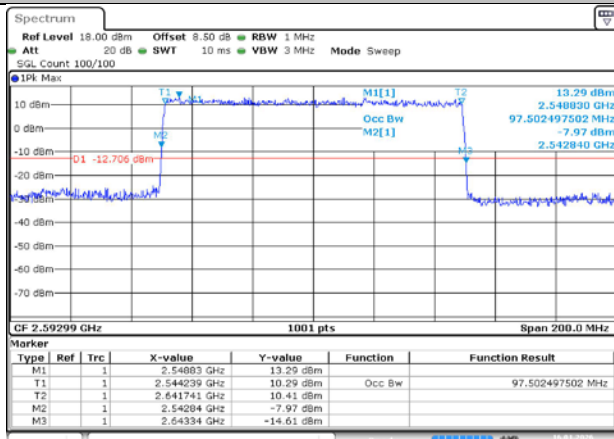
Date: 16 JAN 2026 16:46:45

MCH / CP-16QAM / Outer_Full



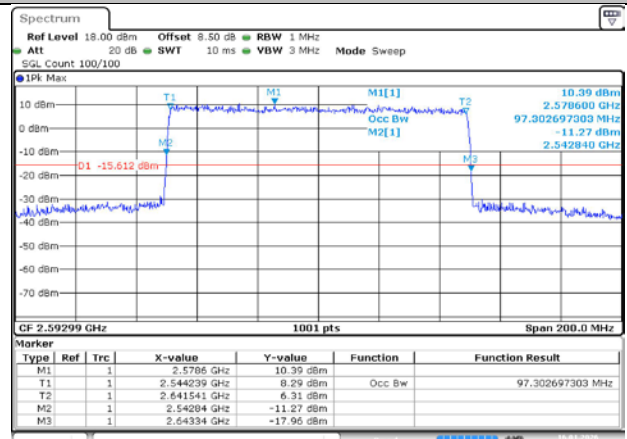
Date: 16 JAN 2026 16:46:50

MCH / CP-64QAM / Outer_Full



Date: 16 JAN 2026 16:46:54

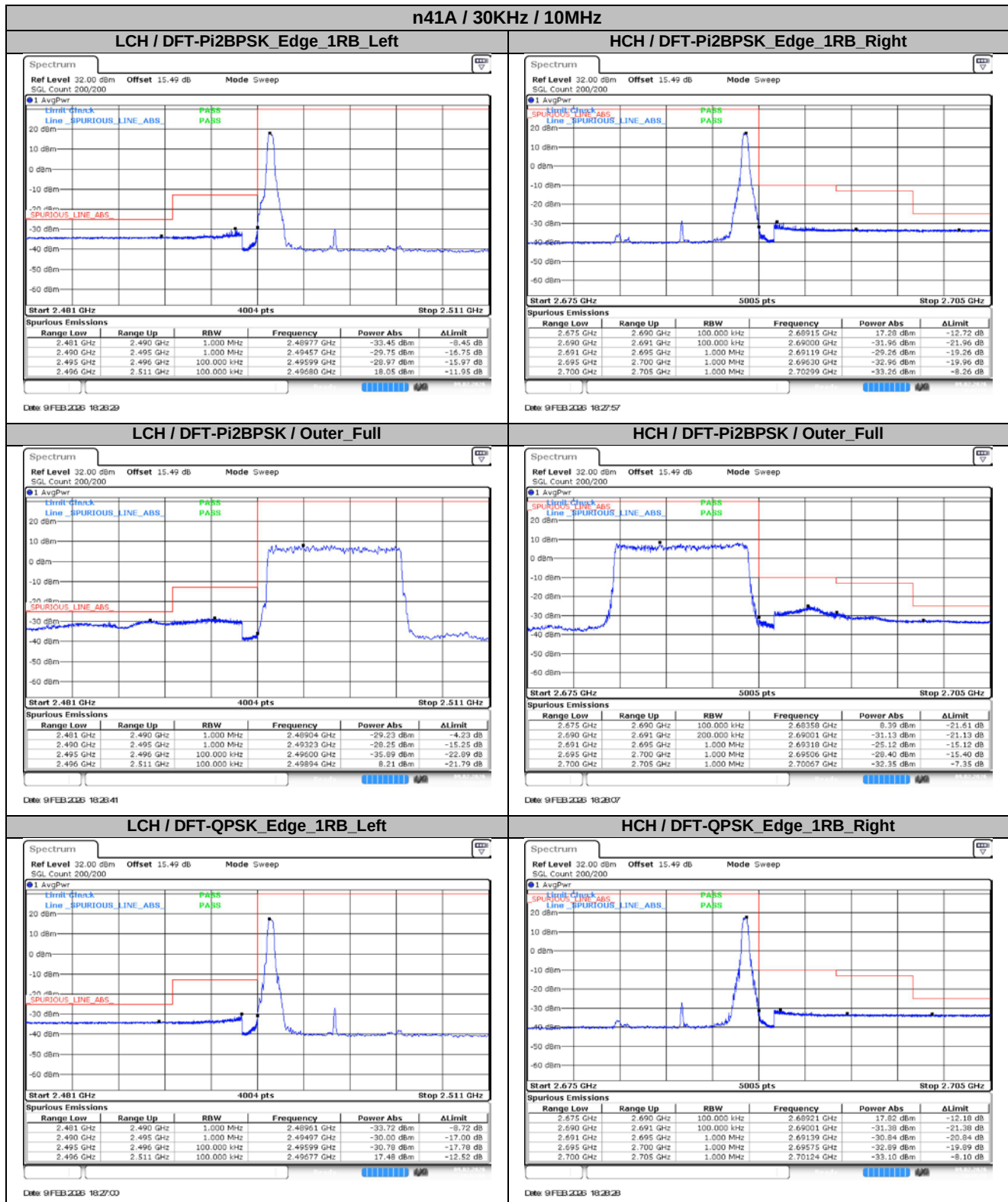
MCH / CP-256QAM / Outer_Full

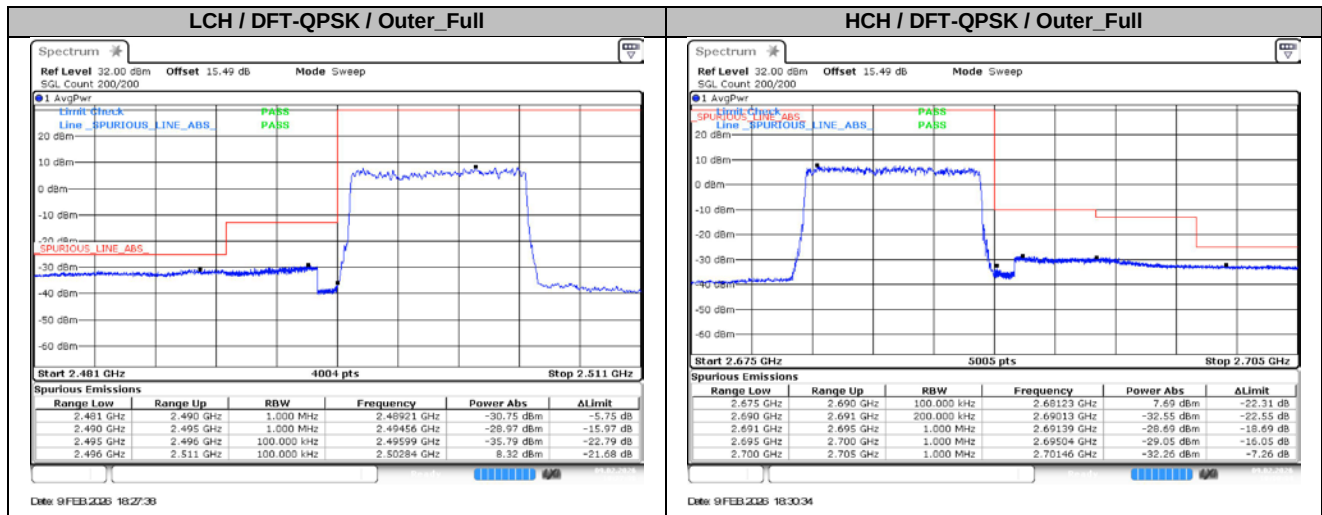
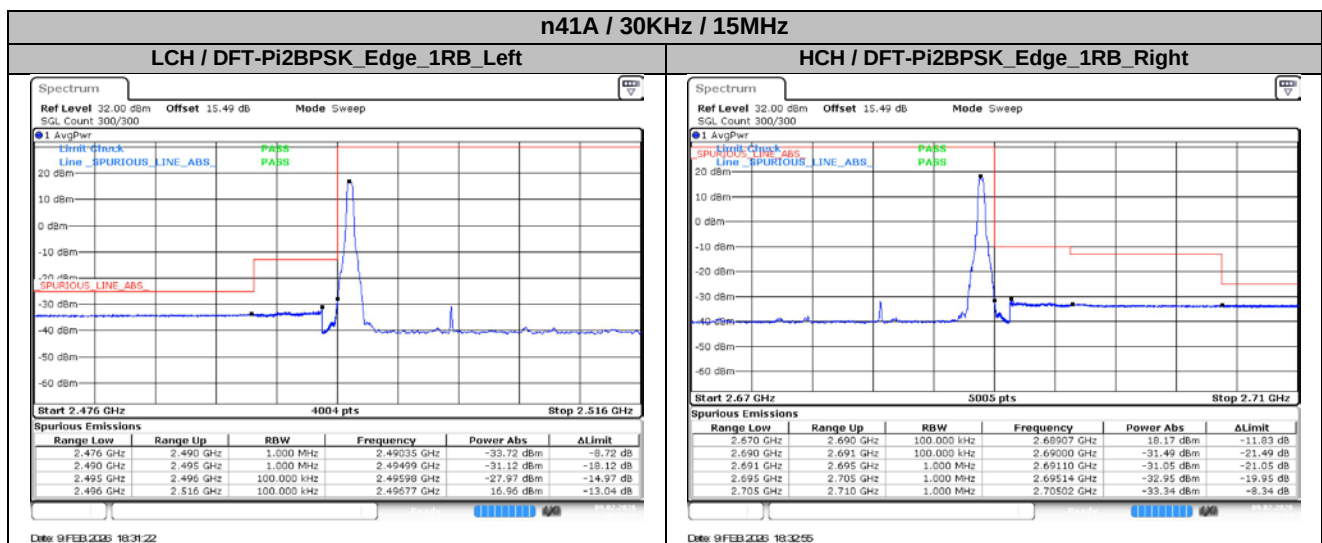
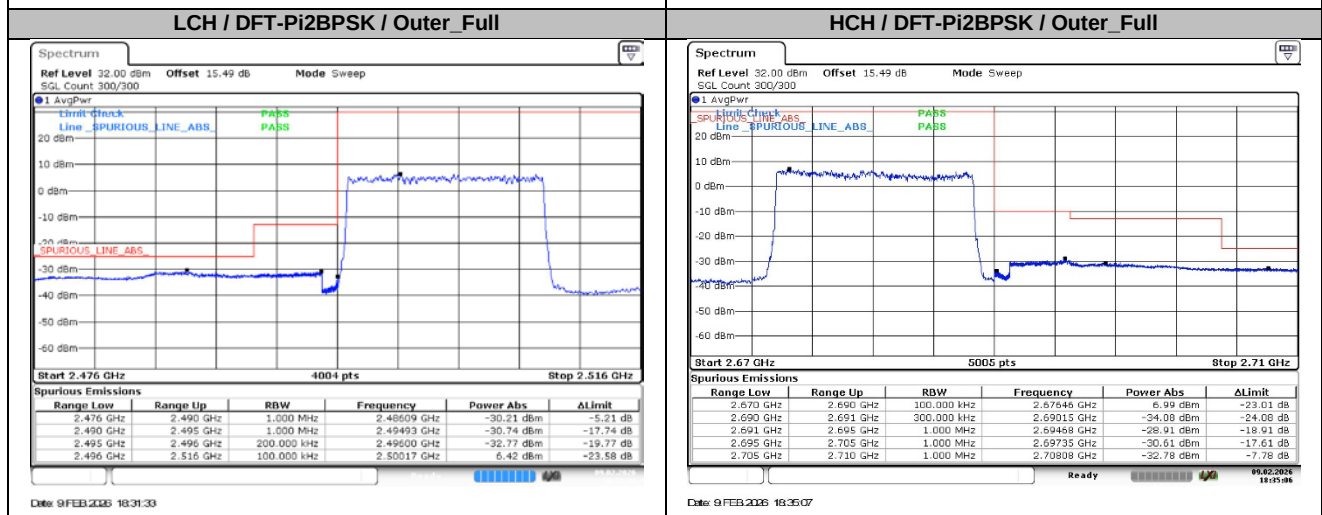


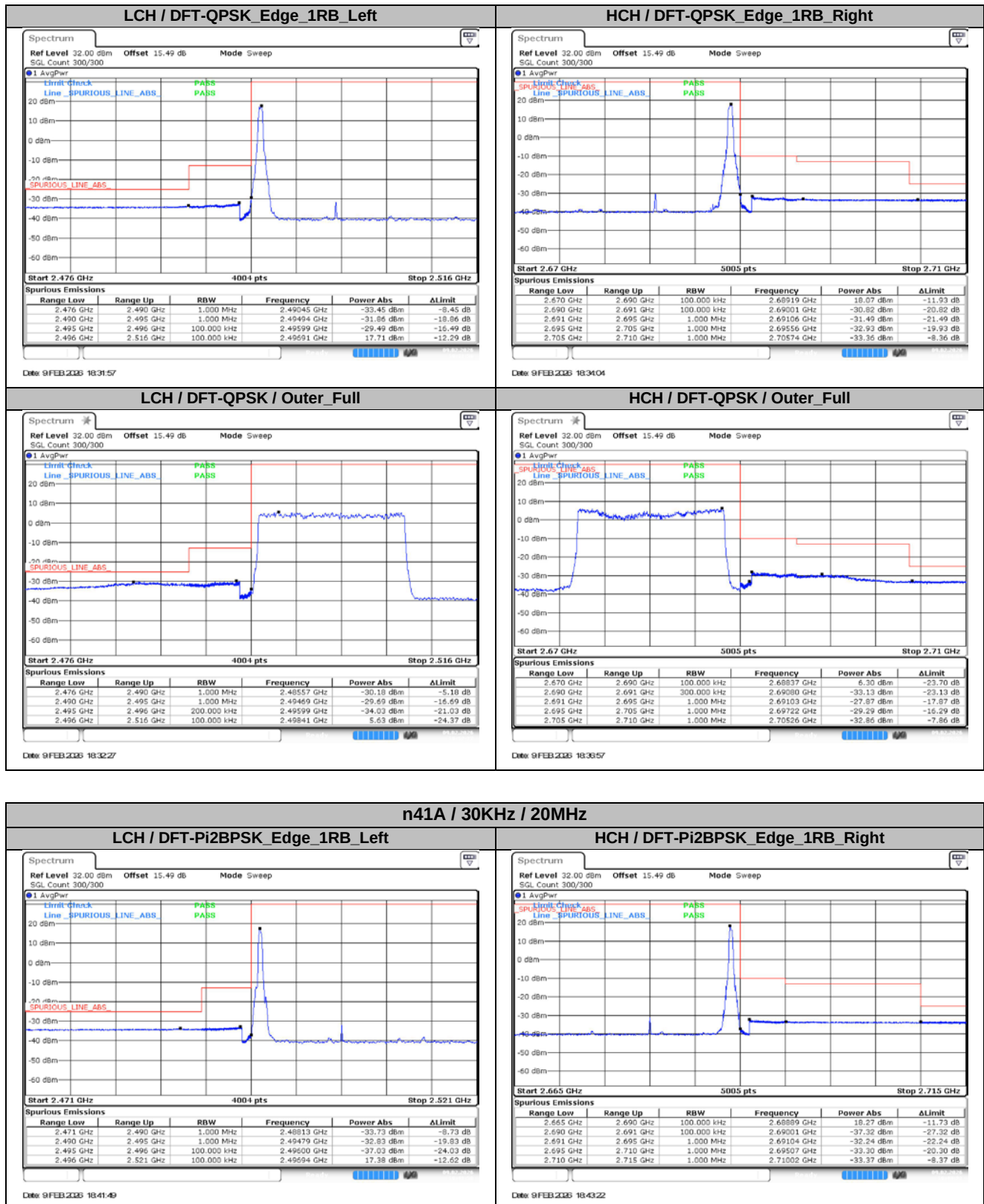
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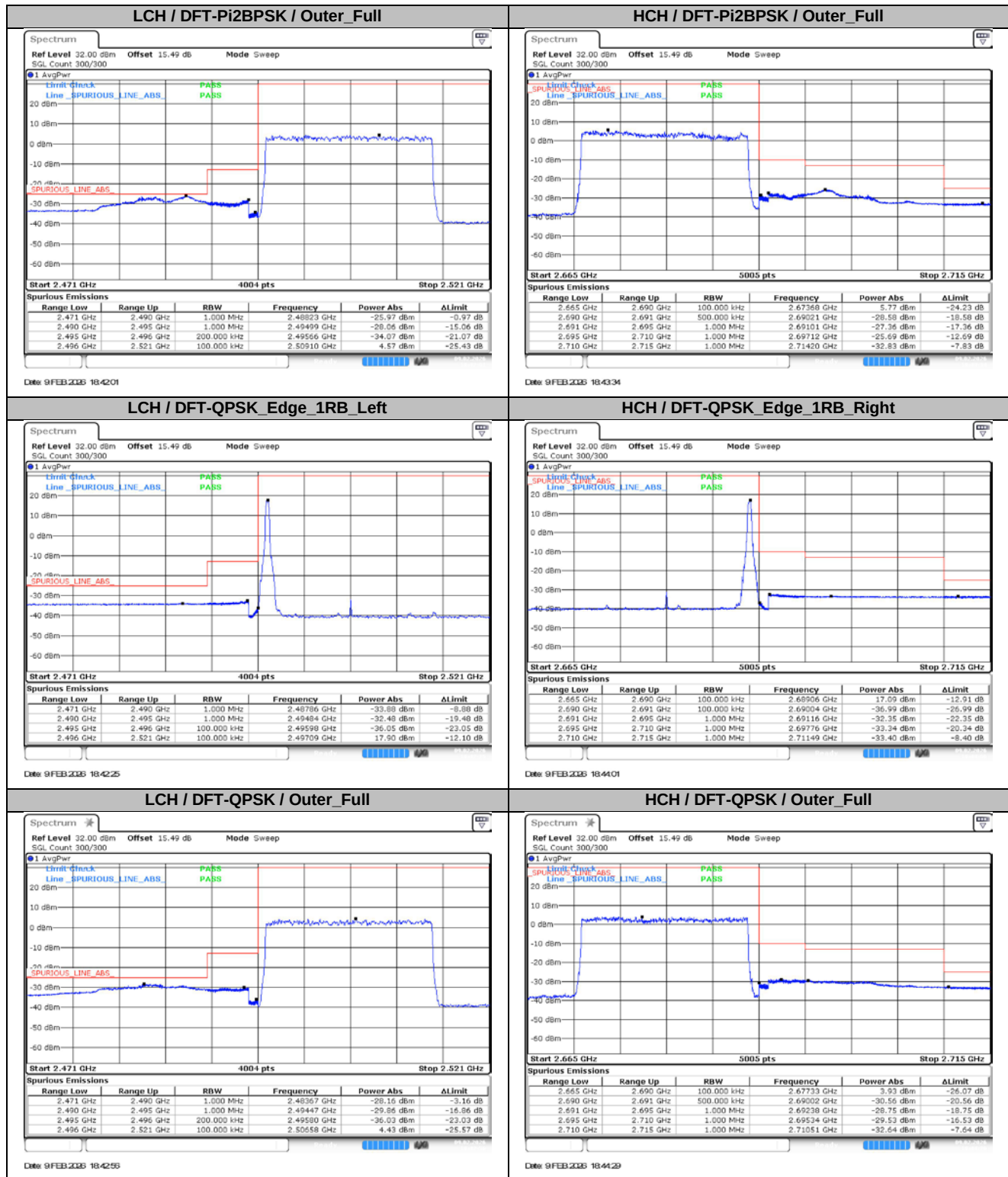
5. Conducted Band Edges

5.1. Test Plots for SCS=30KHz



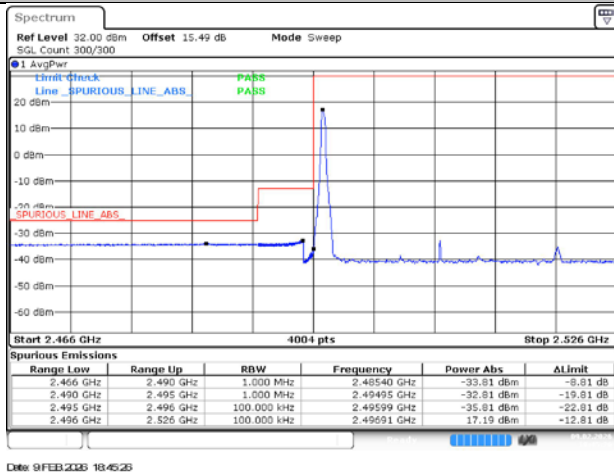




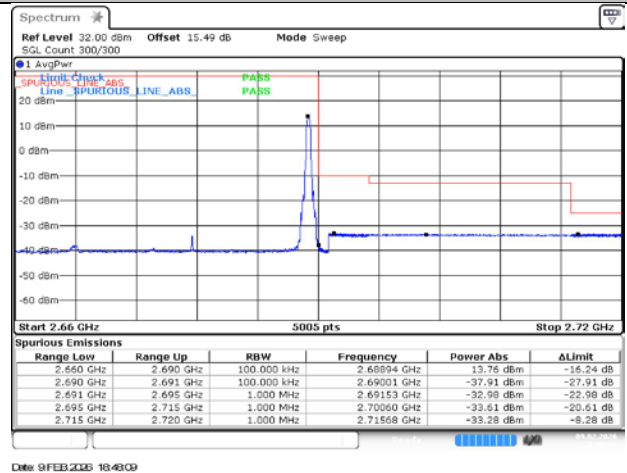


n41A / 30KHz / 25MHz

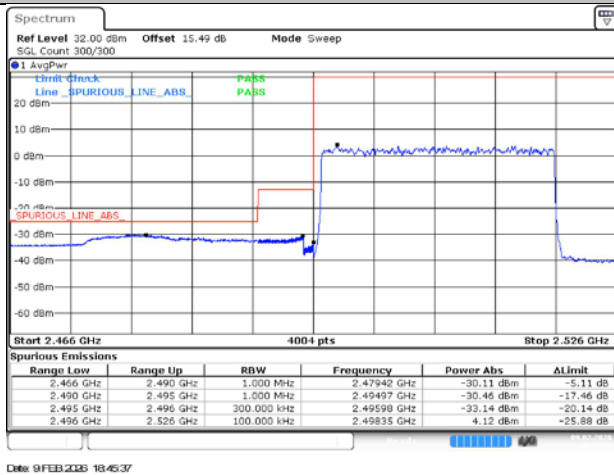
LCH / DFT-Pi2BPSK_Edge_1RB_Left



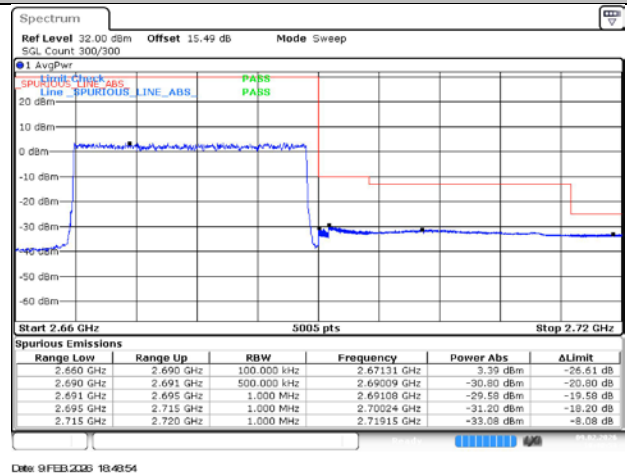
HCH / DFT-Pi2BPSK_Edge_1RB_Right



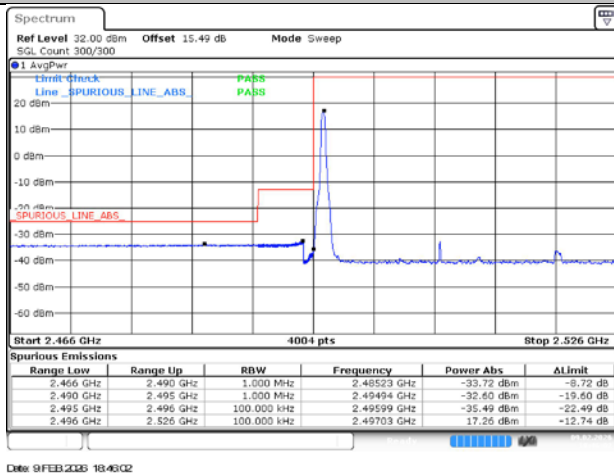
LCH / DFT-Pi2BPSK / Outer_Full



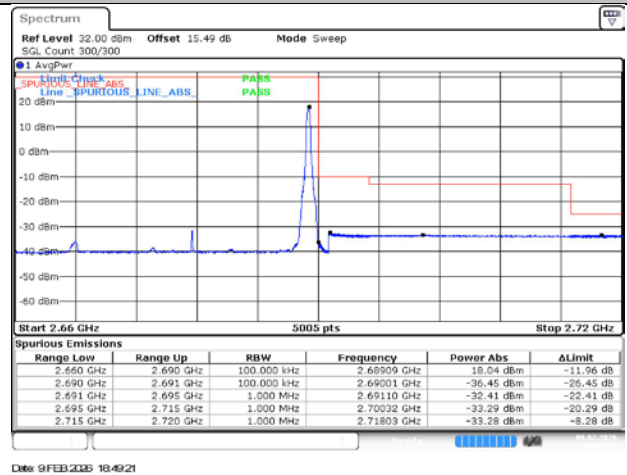
HCH / DFT-Pi2BPSK / Outer_Full

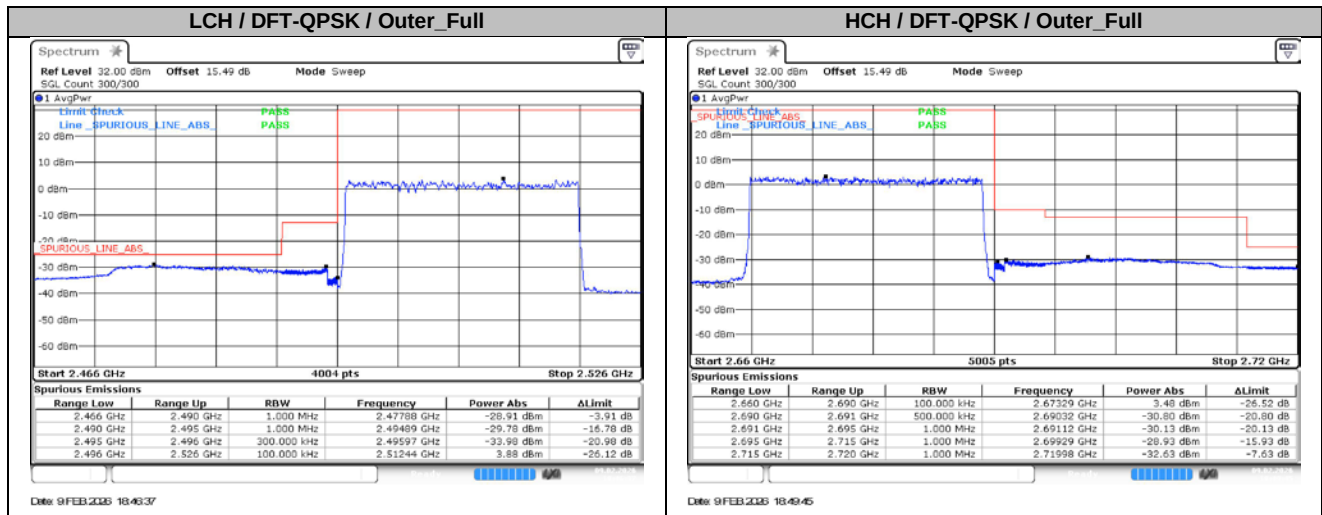
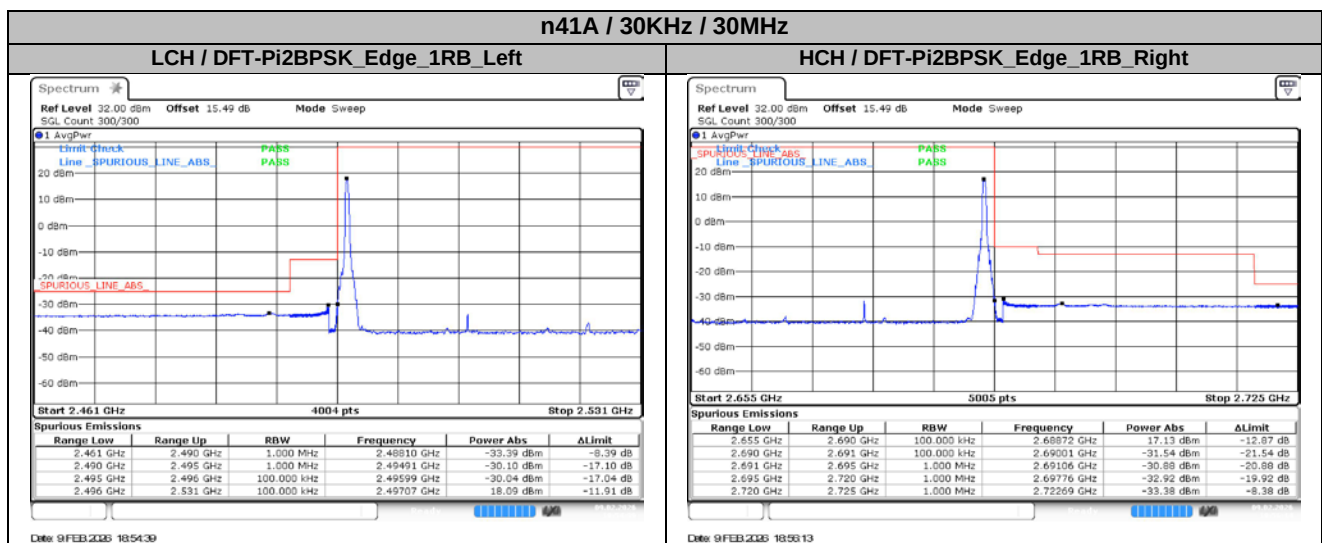
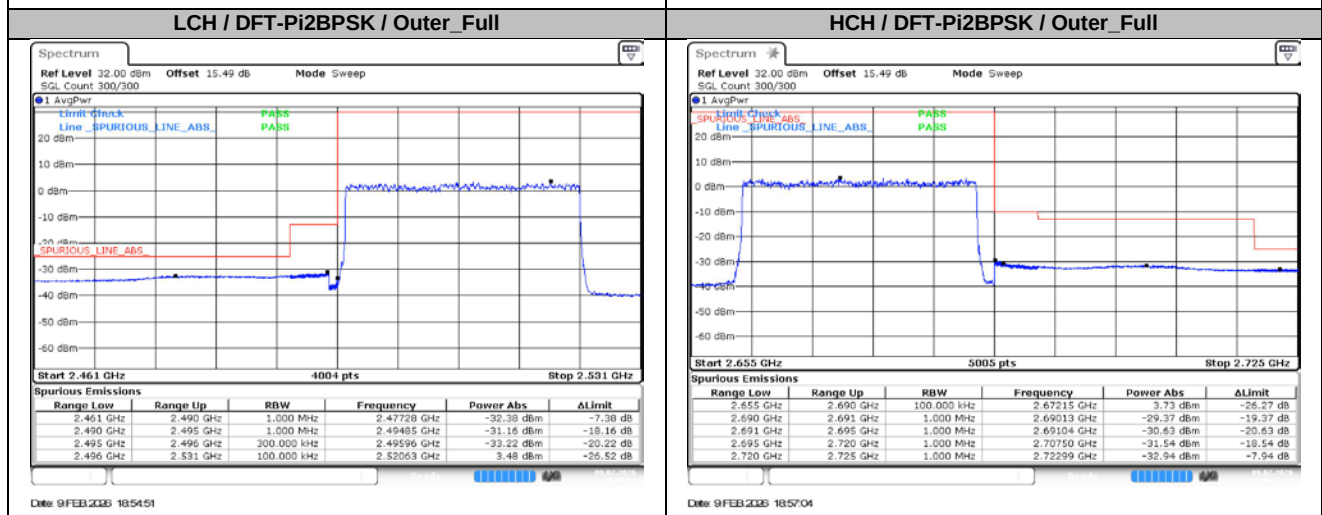


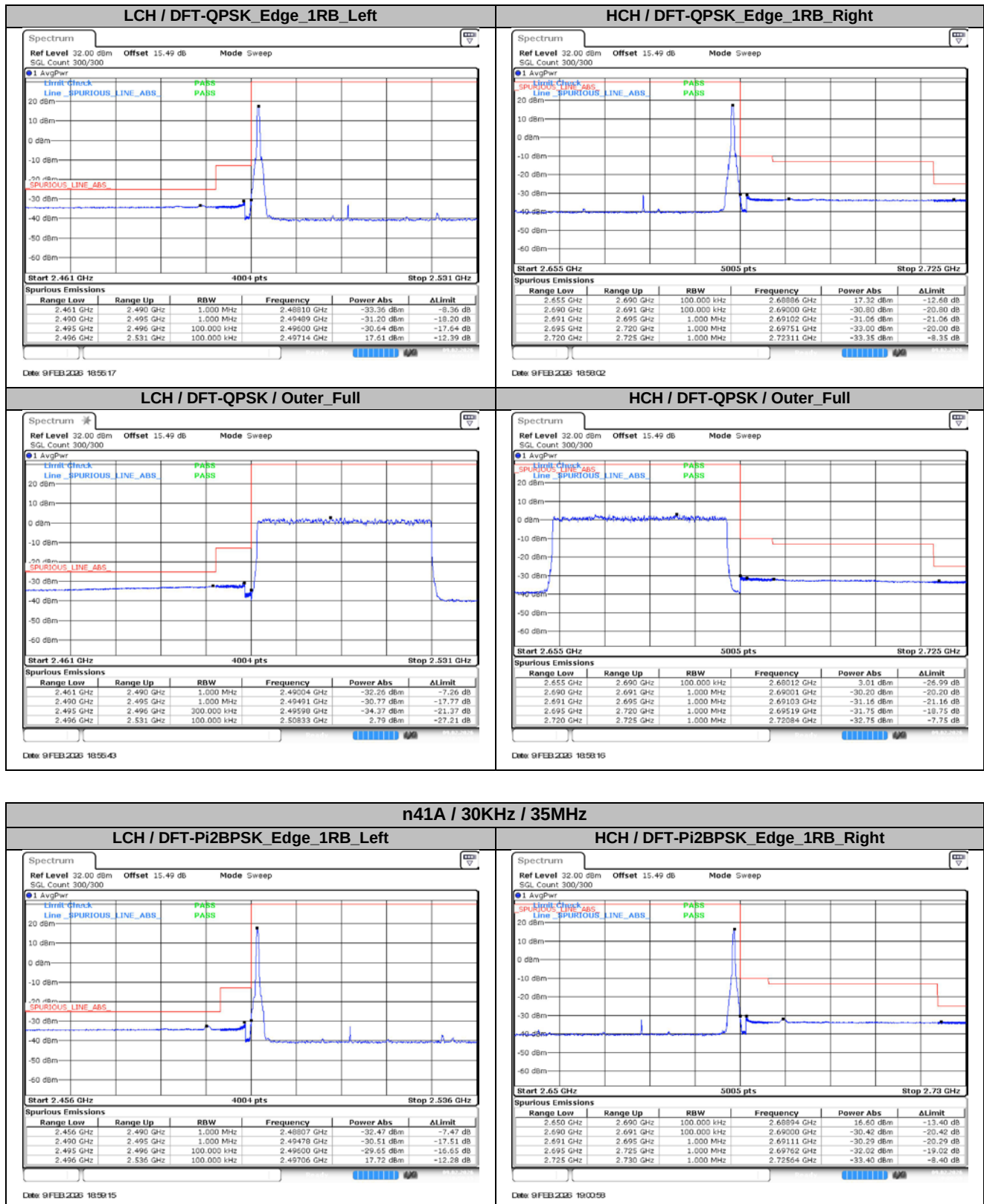
LCH / DFT-QPSK_Edge_1RB_Left

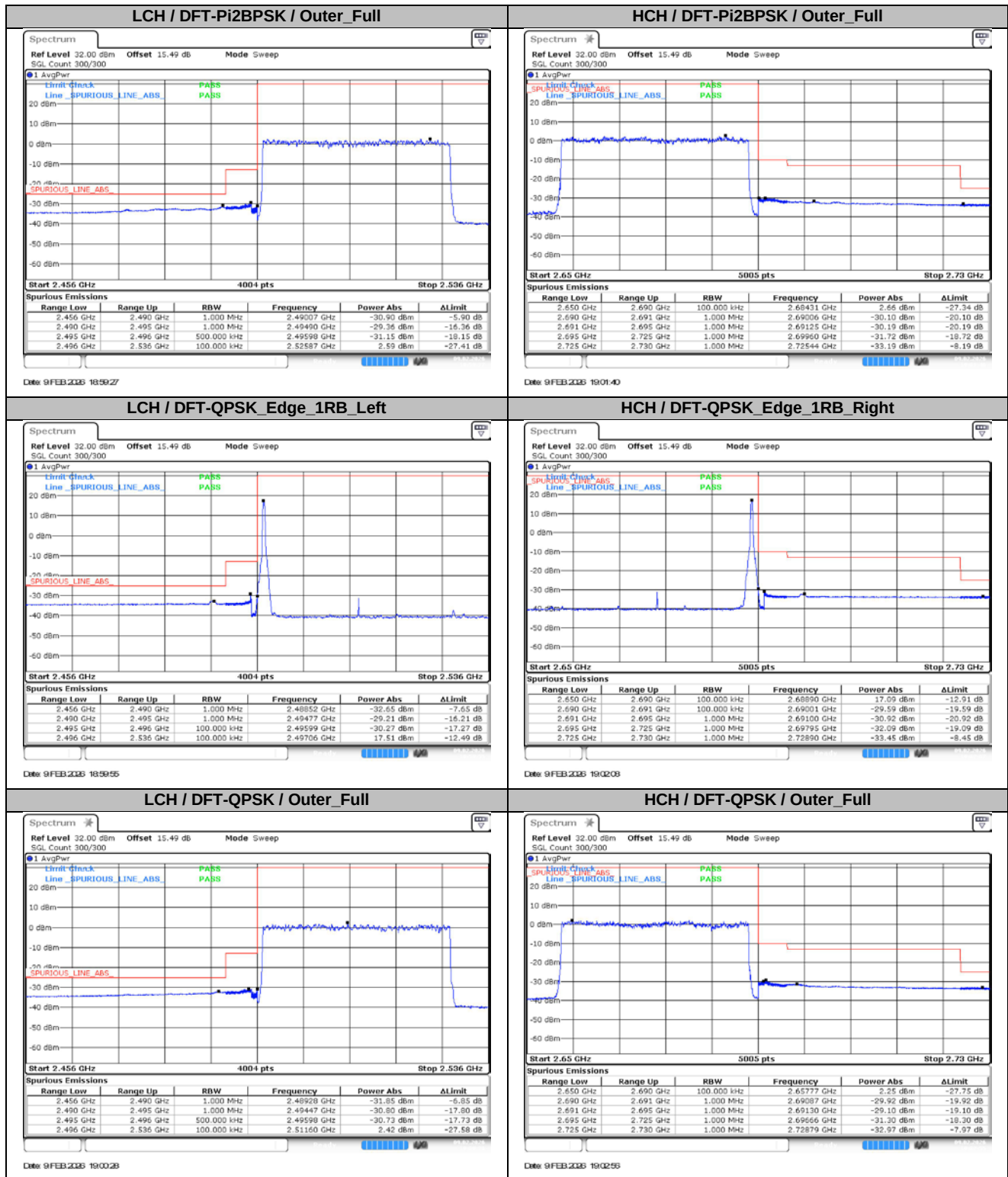


HCH / DFT-QPSK_Edge_1RB_Right



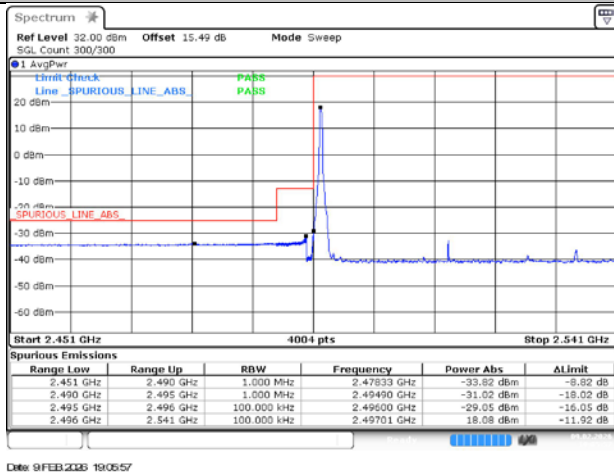




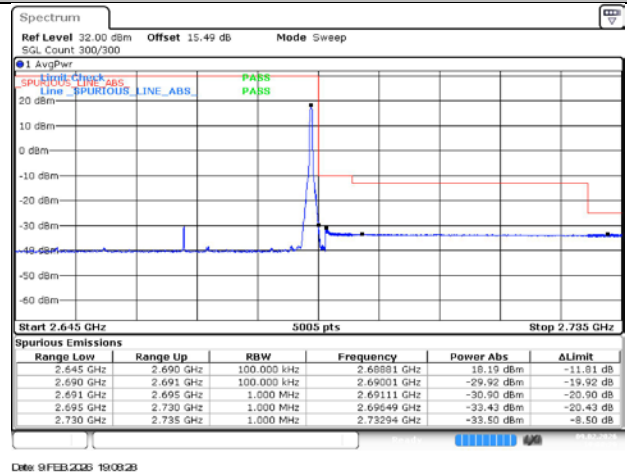


n41A / 30KHz / 40MHz

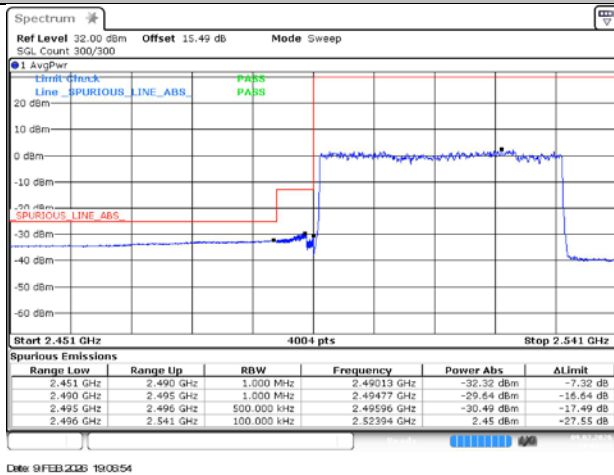
LCH / DFT-Pi2BPSK_Edge_1RB_Left



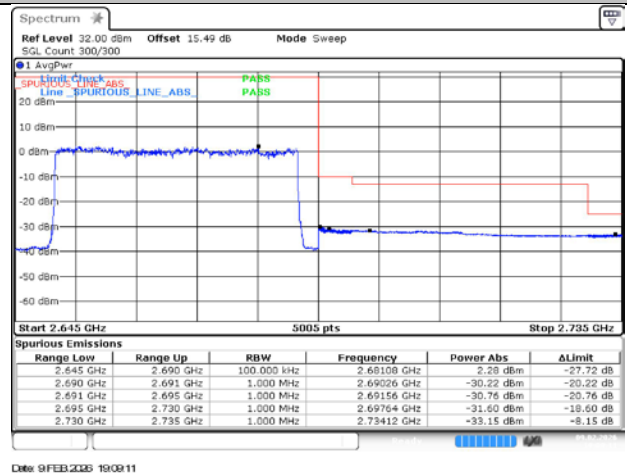
HCH / DFT-Pi2BPSK_Edge_1RB_Right



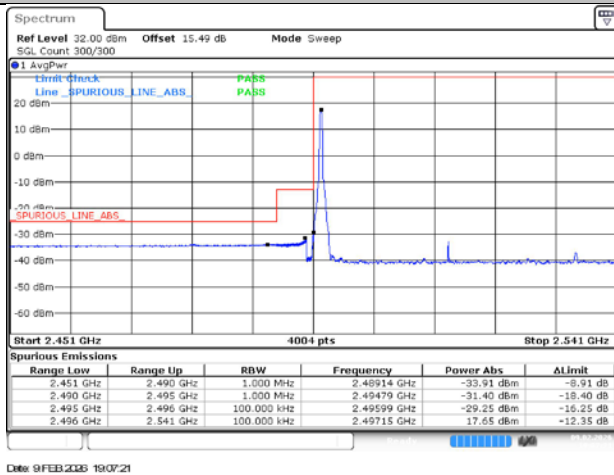
LCH / DFT-Pi2BPSK / Outer_Full



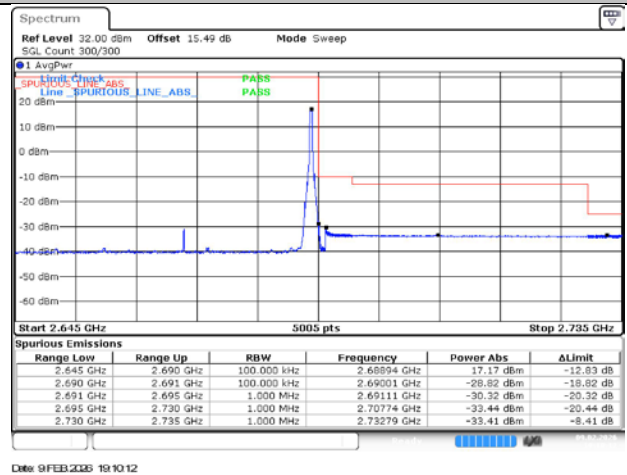
HCH / DFT-Pi2BPSK / Outer_Full

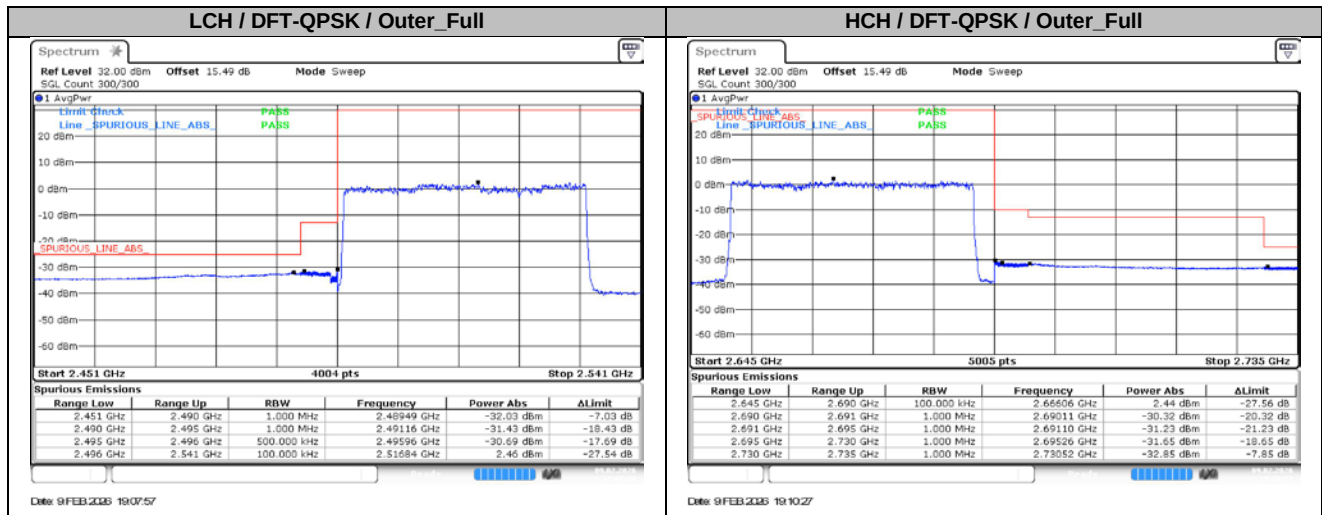


LCH / DFT-QPSK_Edge_1RB_Left



HCH / DFT-QPSK_Edge_1RB_Right





n41A / 30KHz / 45MHz

