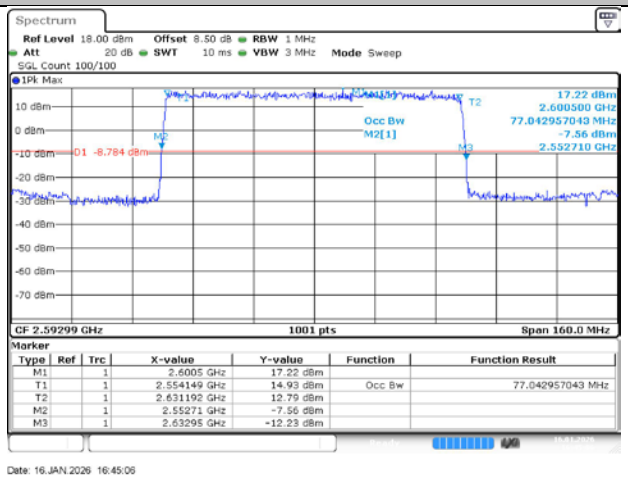
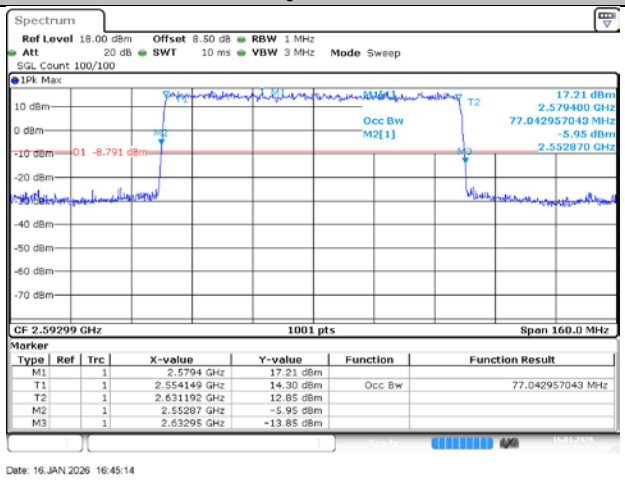
n41A / 30KHz / 80MHz

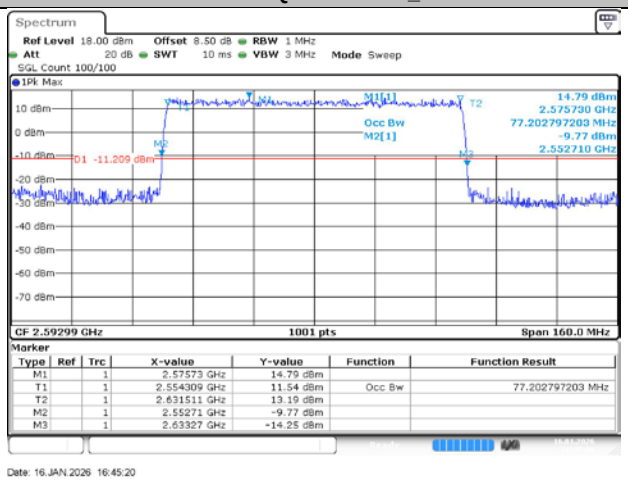
MCH / DFT-Pi2BPSK / Outer_Full



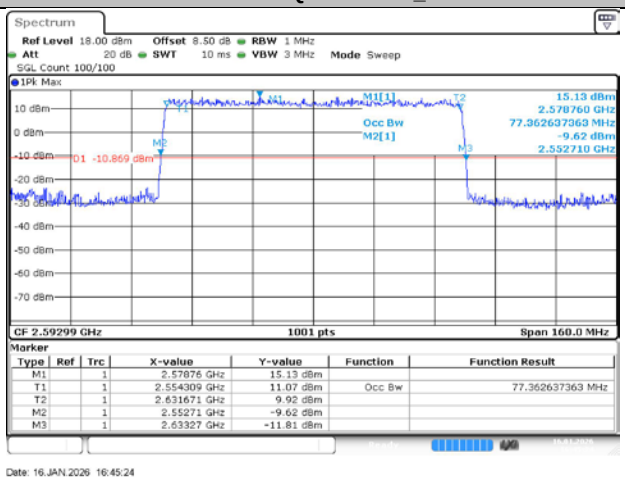
MCH / DFT-QPSK / Outer_Full



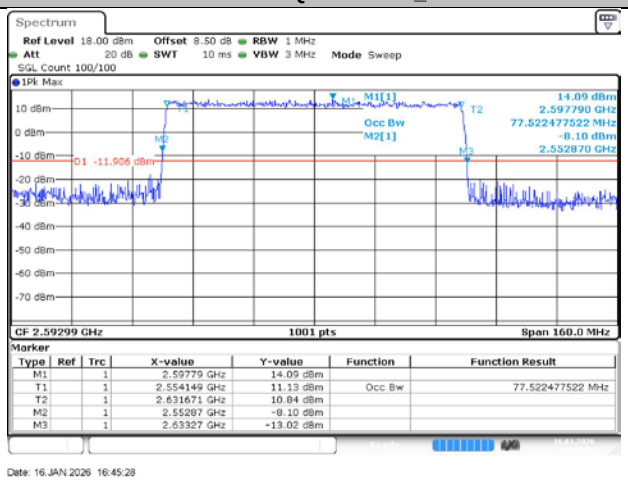
MCH / CP-QPSK / Outer_Full



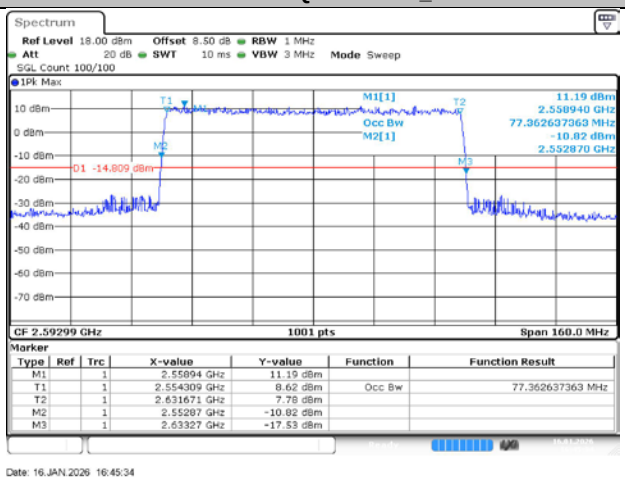
MCH / CP-16QAM / Outer_Full



MCH / CP-64QAM / Outer_Full

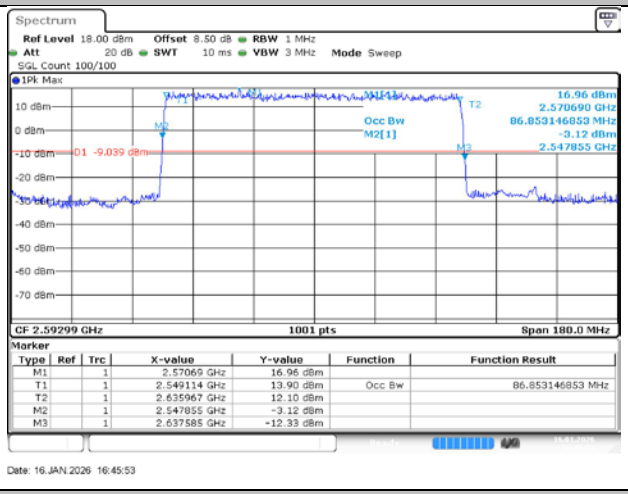


MCH / CP-256QAM / Outer_Full

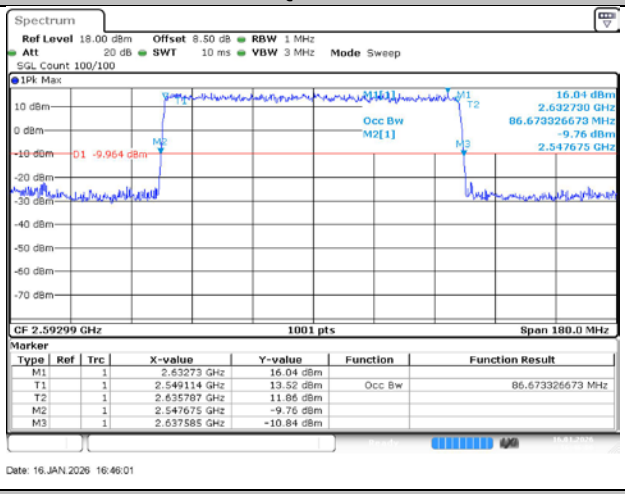


n41A / 30KHz / 90MHz

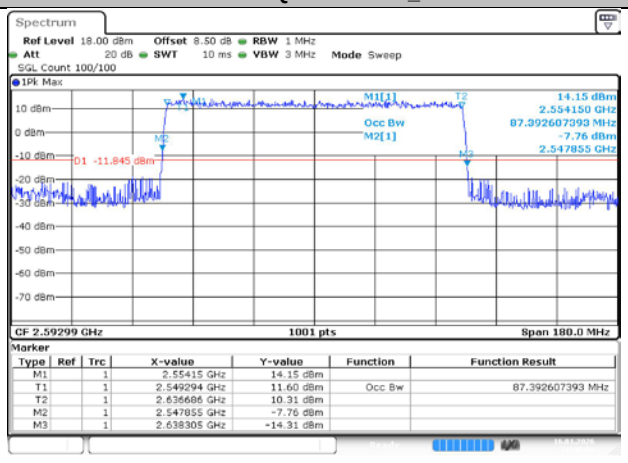
MCH / DFT-Pi2BPSK / Outer_Full



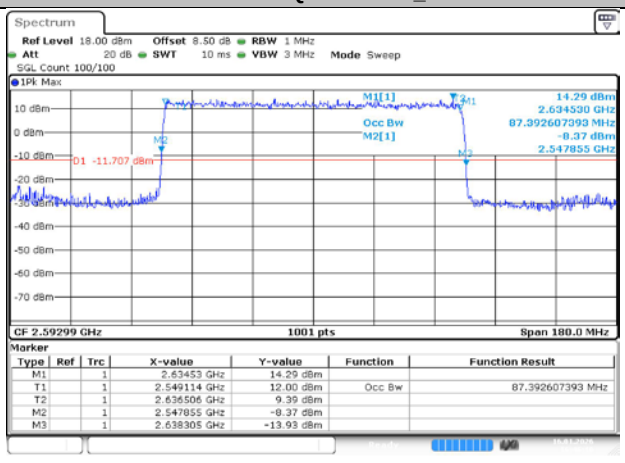
MCH / DFT-QPSK / Outer_Full



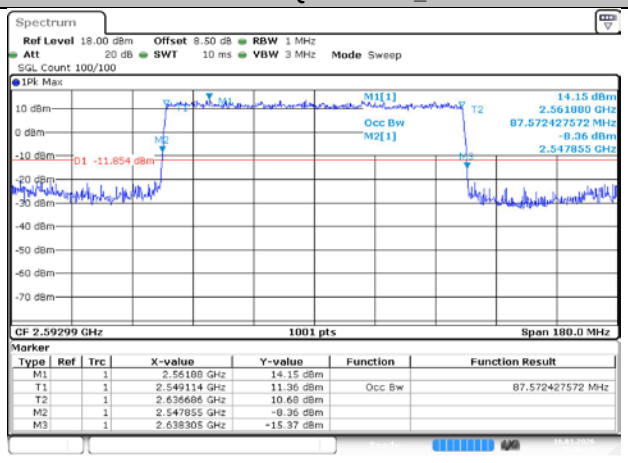
MCH / CP-QPSK / Outer_Full



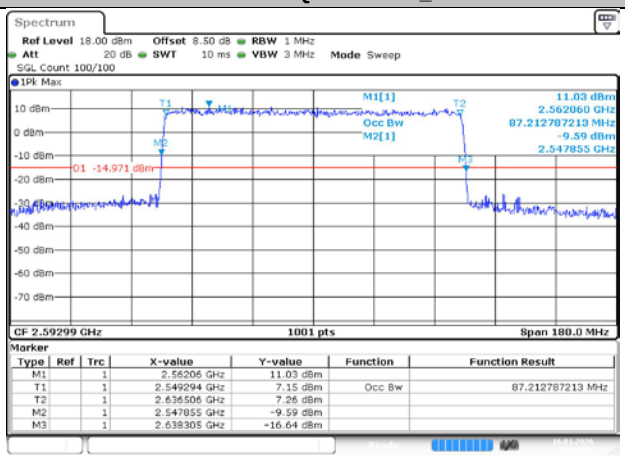
MCH / CP-16QAM / Outer_Full

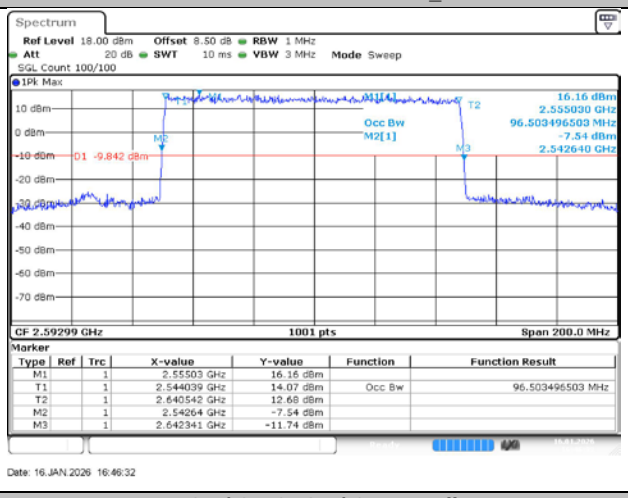
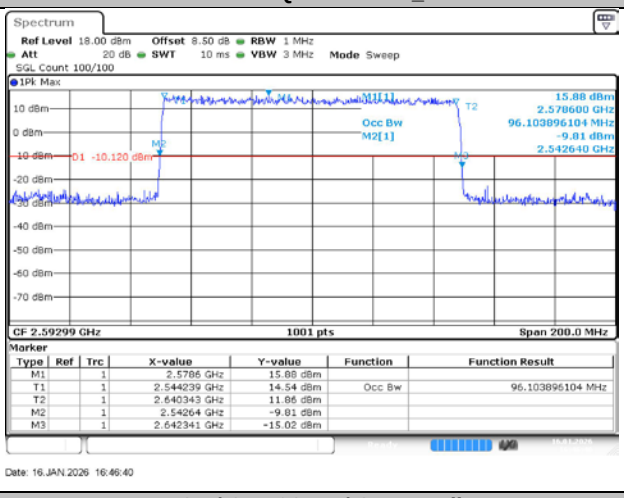
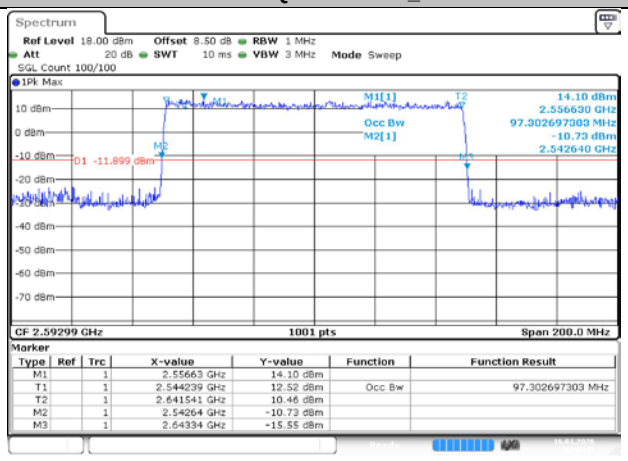
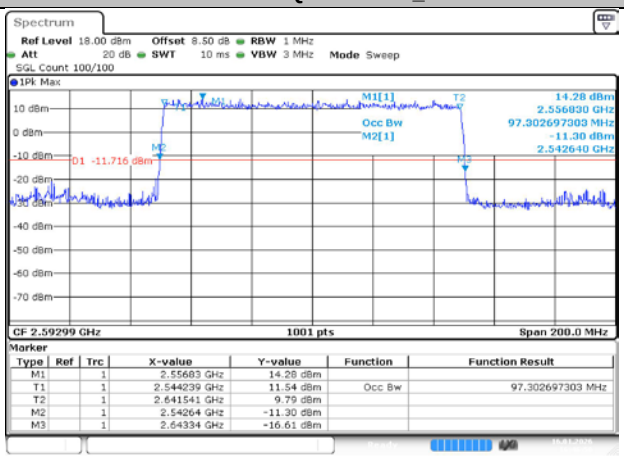
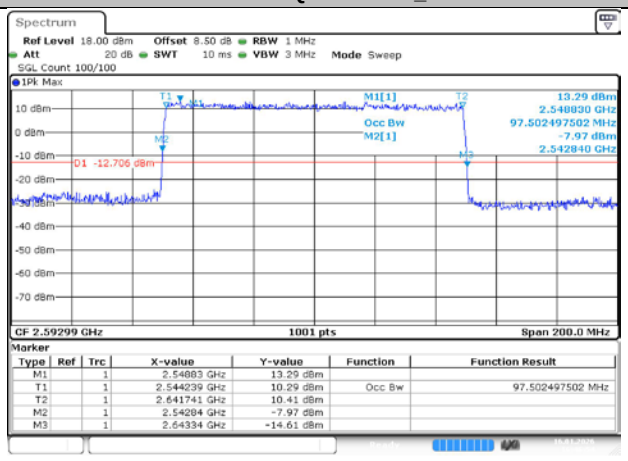
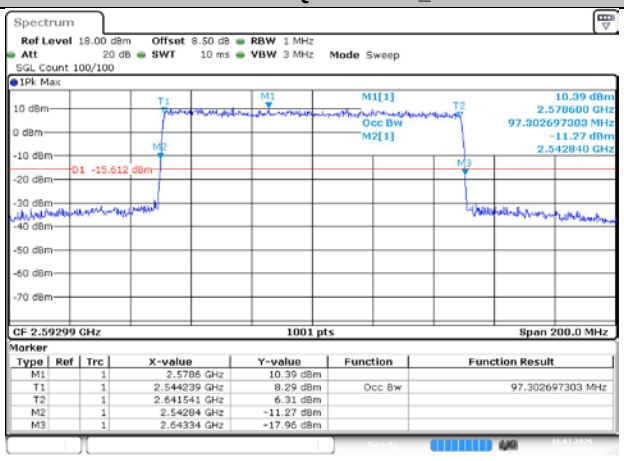


MCH / CP-64QAM / Outer_Full



MCH / CP-256QAM / Outer_Full



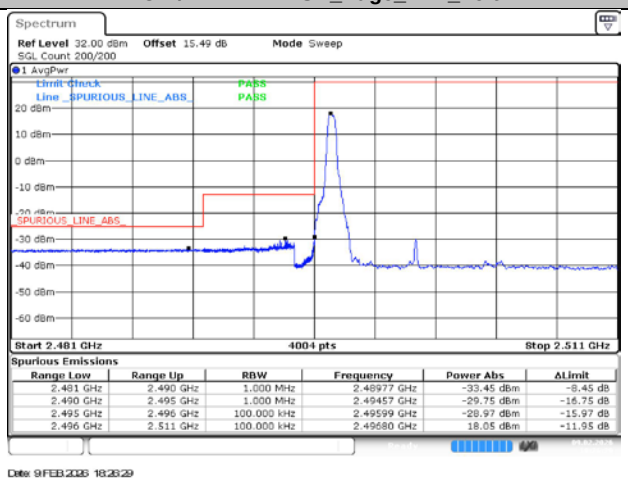
n41A / 30KHz / 100MHz	
MCH / DFT-Pi2BPSK / Outer_Full	MCH / DFT-QPSK / Outer_Full
	
MCH / CP-QPSK / Outer_Full	MCH / CP-16QAM / Outer_Full
	
MCH / CP-64QAM / Outer_Full	MCH / CP-256QAM / Outer_Full
	

5. Conducted Band Edges

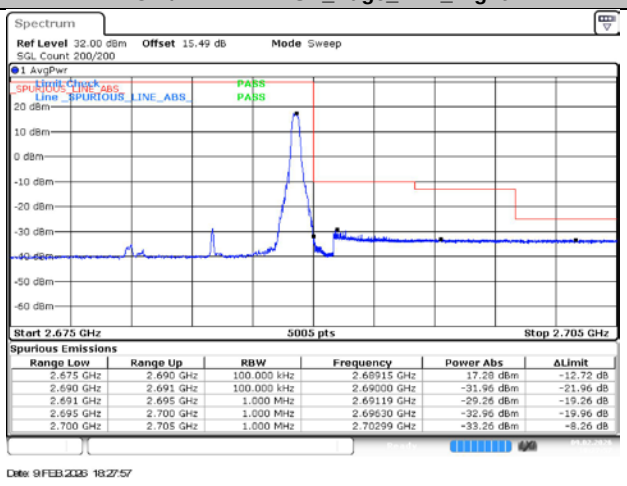
5.1. Test Plots for SCS=30KHz

n41A / 30KHz / 10MHz

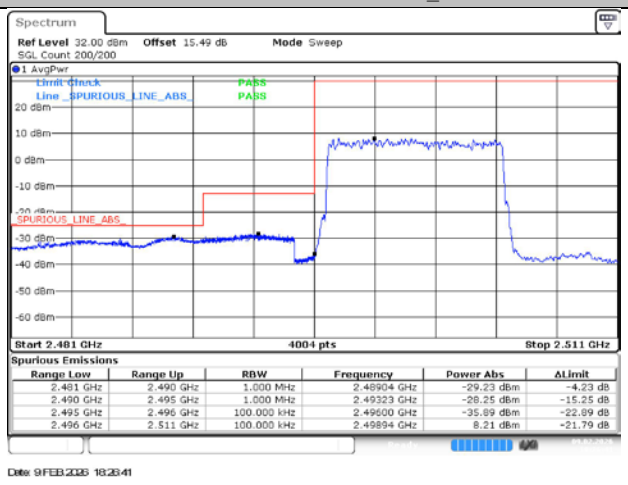
LCH / DFT-Pi2BPSK_Edge_1RB_Left



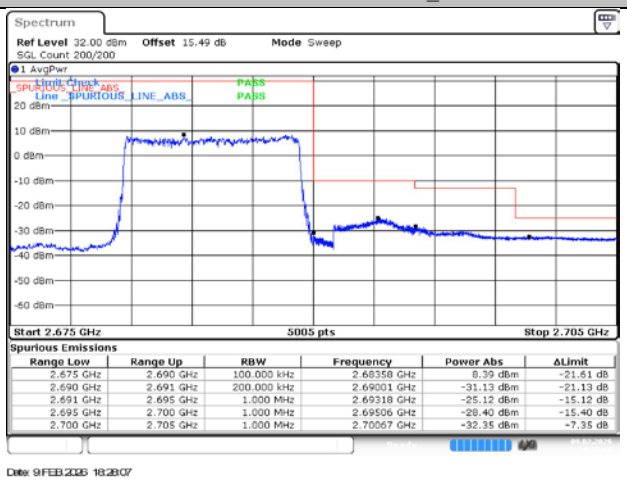
HCH / DFT-Pi2BPSK_Edge_1RB_Right



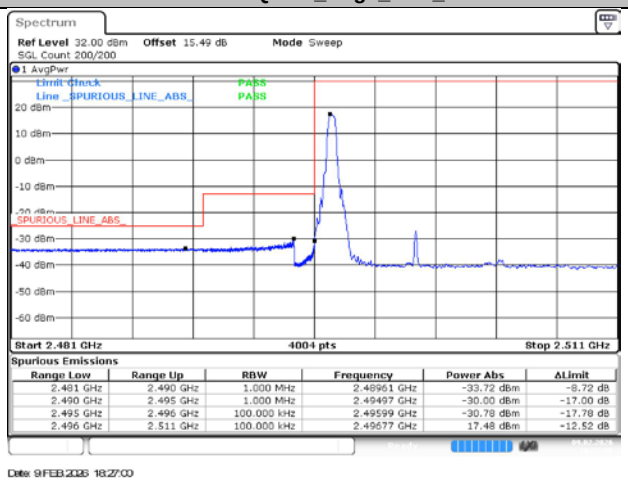
LCH / DFT-Pi2BPSK / Outer_Full



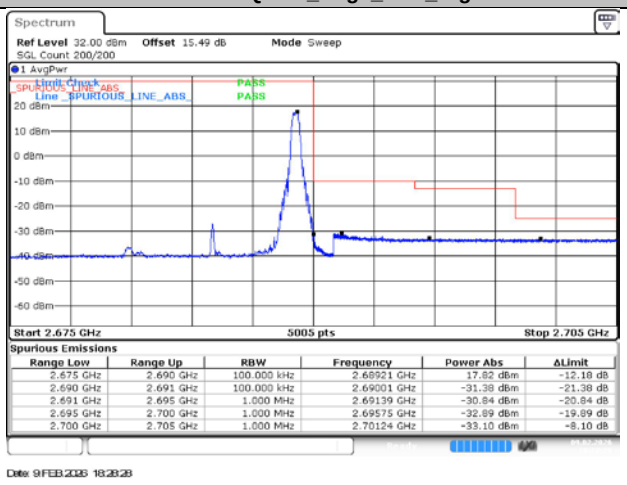
HCH / DFT-Pi2BPSK / Outer_Full

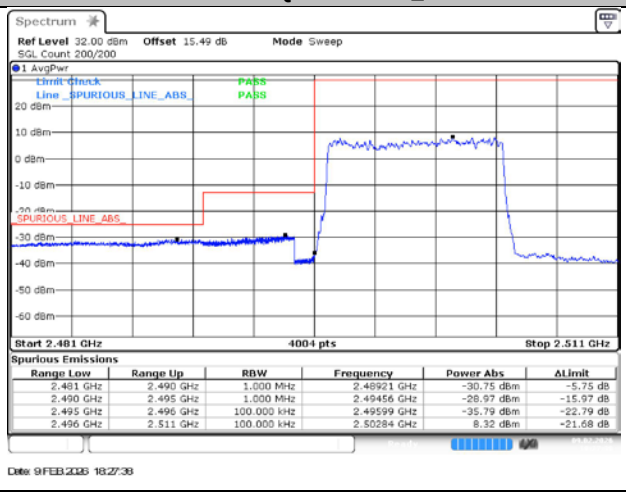
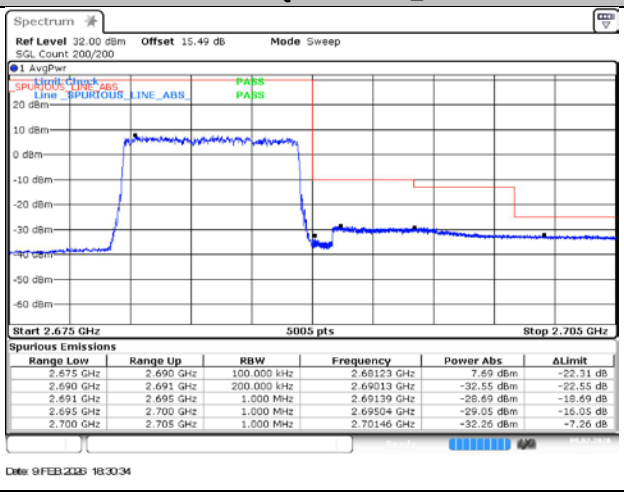


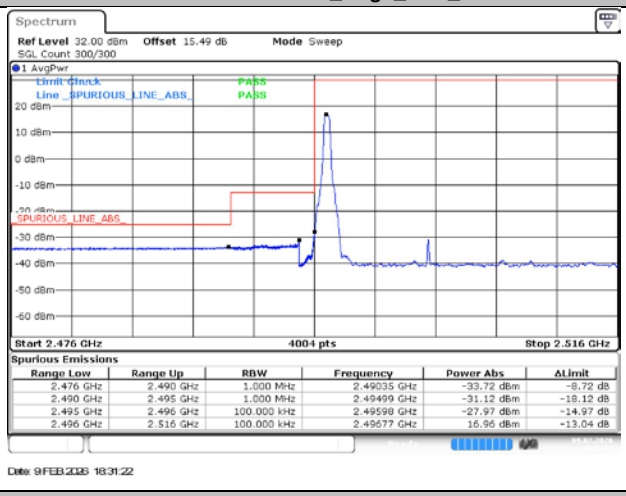
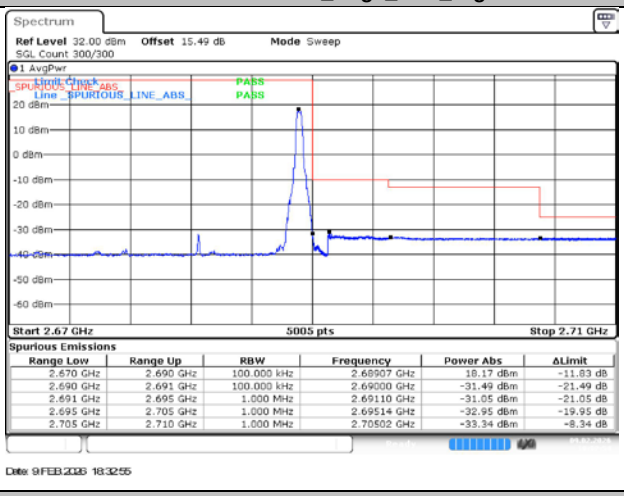
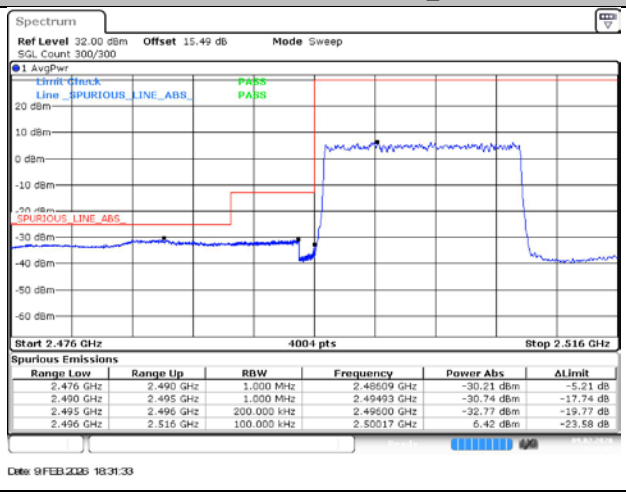
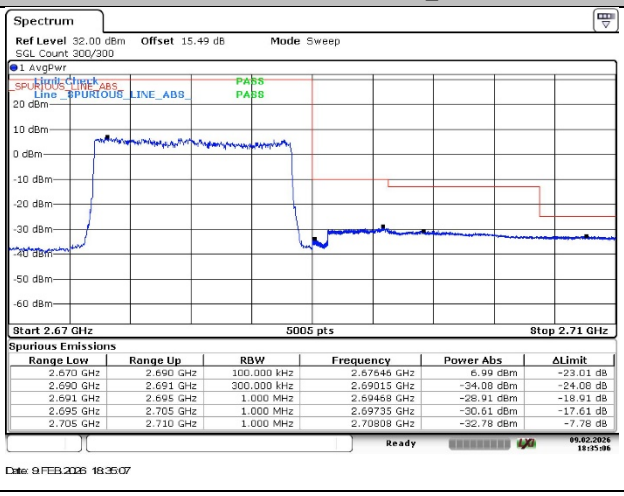
LCH / DFT-QPSK_Edge_1RB_Left

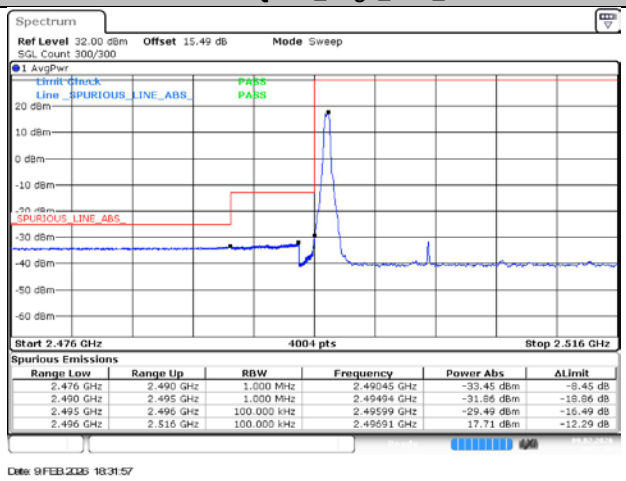
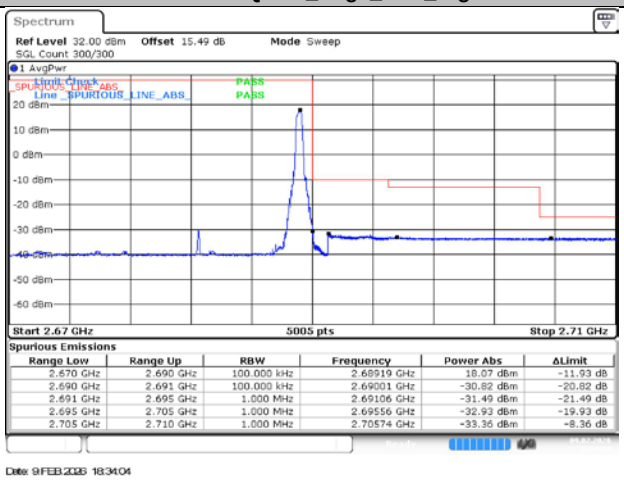
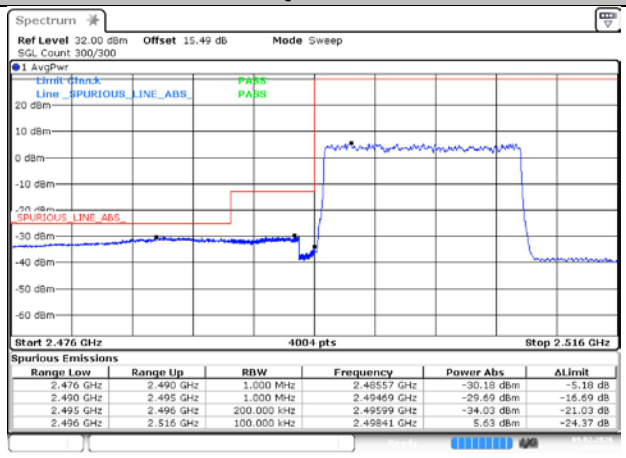
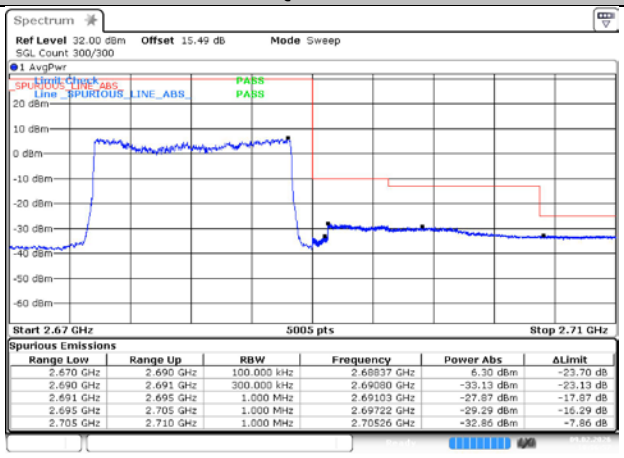


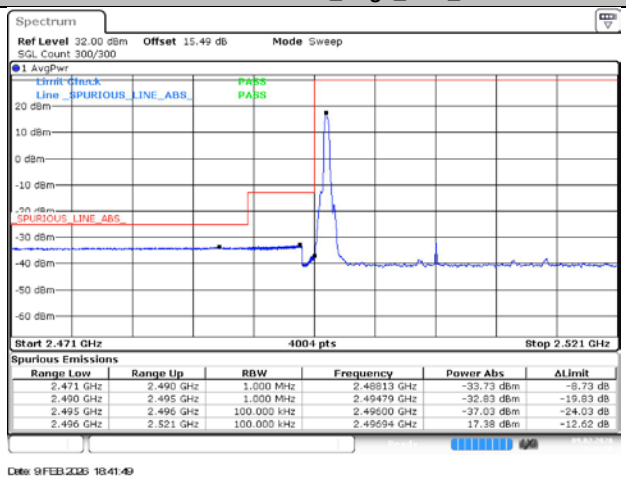
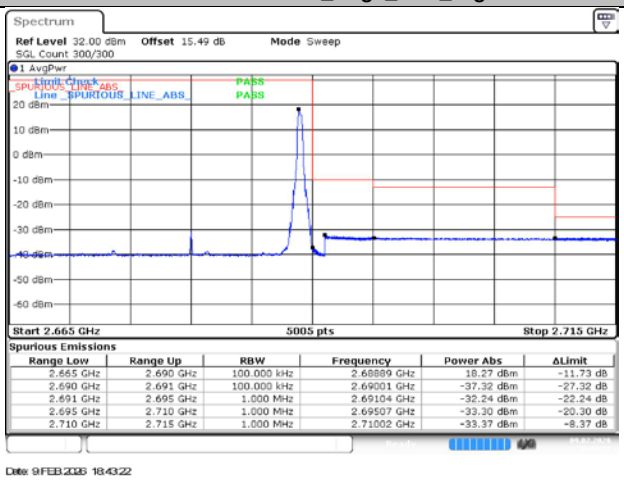
HCH / DFT-QPSK_Edge_1RB_Right

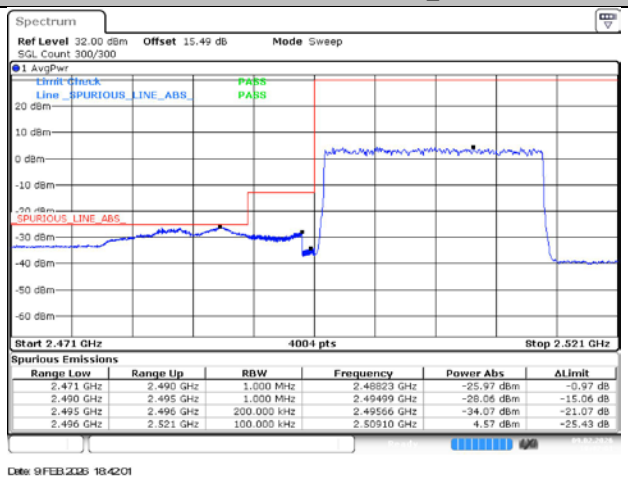
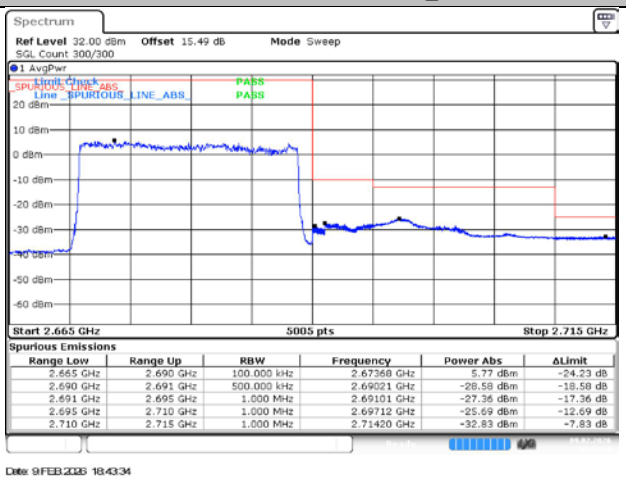
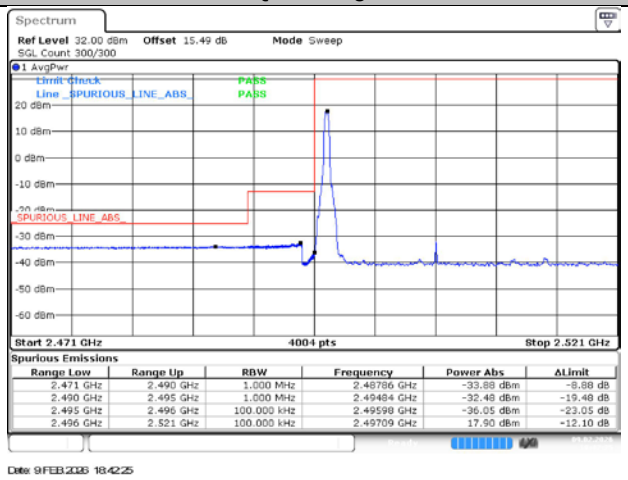
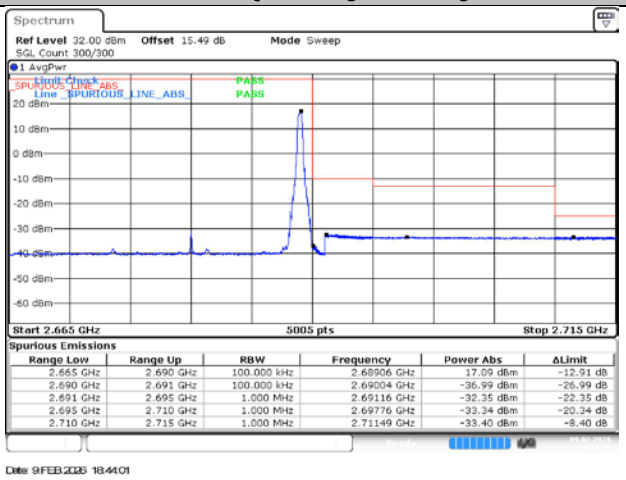
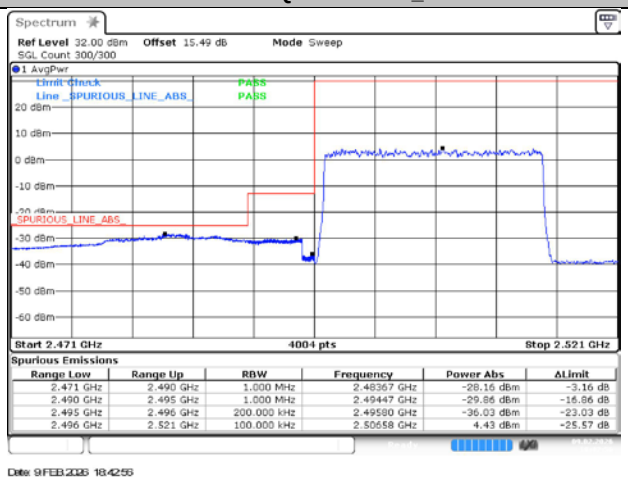
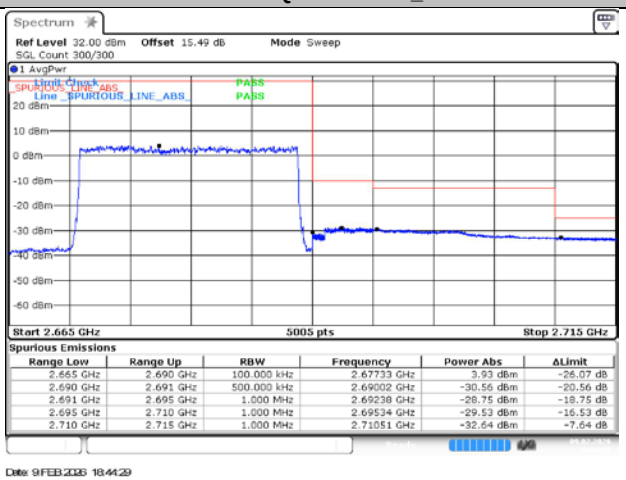


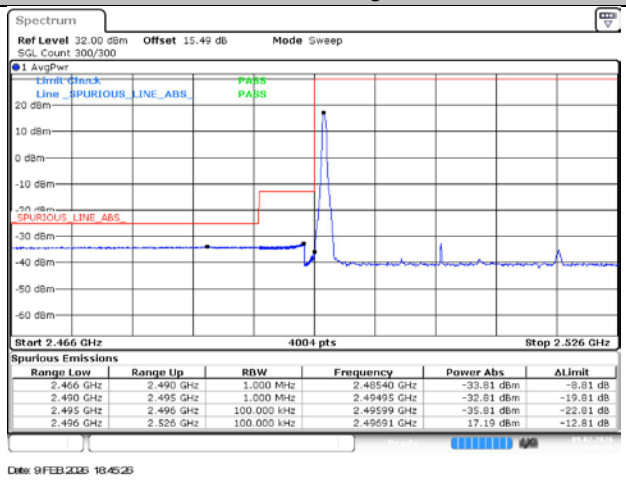
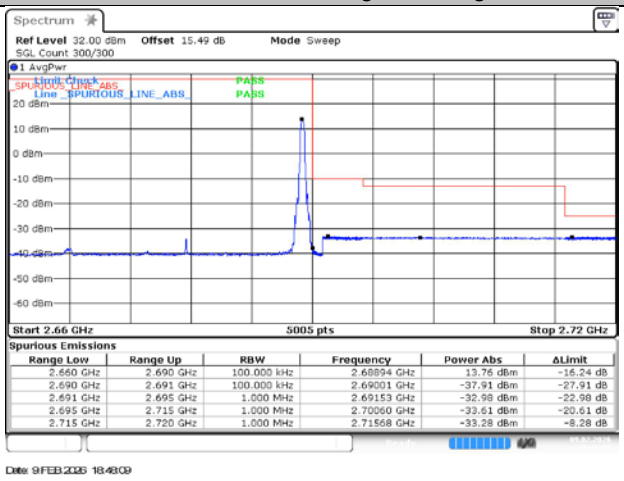
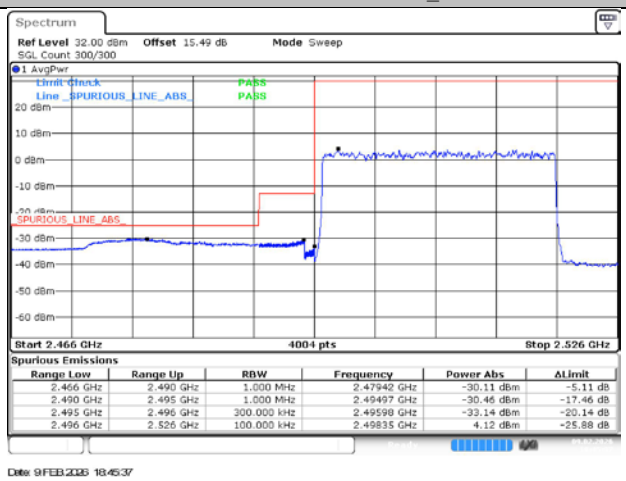
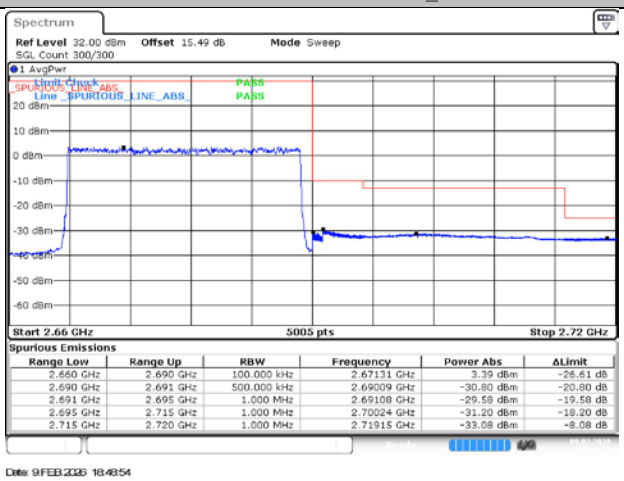
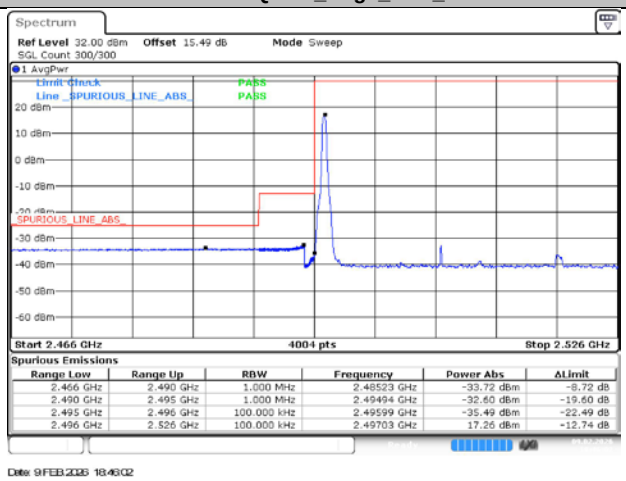
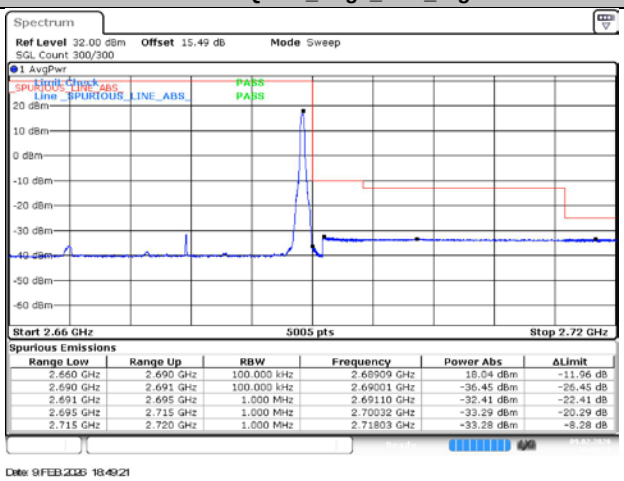
LCH / DFT-QPSK / Outer_Full	HCH / DFT-QPSK / Outer_Full
	

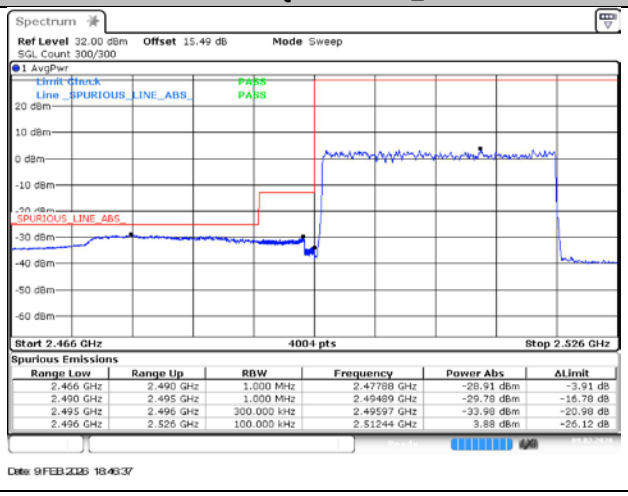
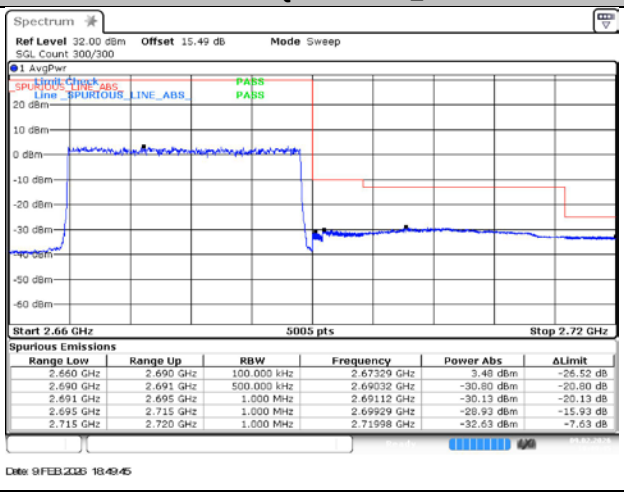
n41A / 30KHz / 15MHz	
LCH / DFT-Pi2BPSK_Edge_1RB_Left	HCH / DFT-Pi2BPSK_Edge_1RB_Right
	
LCH / DFT-Pi2BPSK / Outer_Full	HCH / DFT-Pi2BPSK / Outer_Full
	

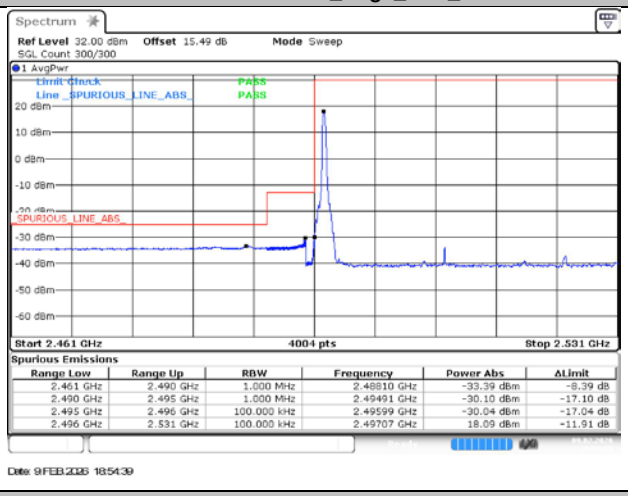
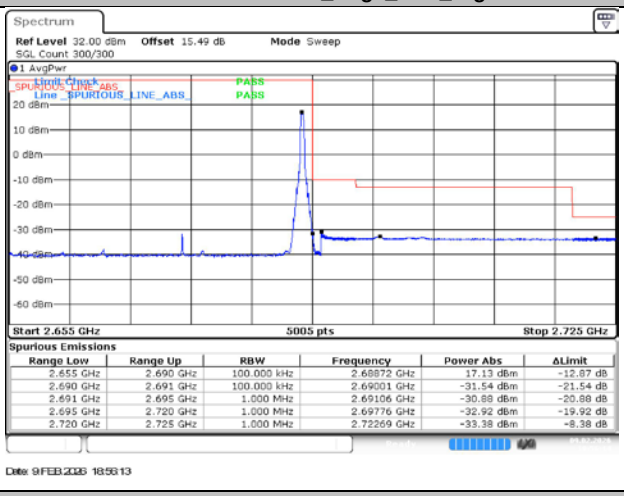
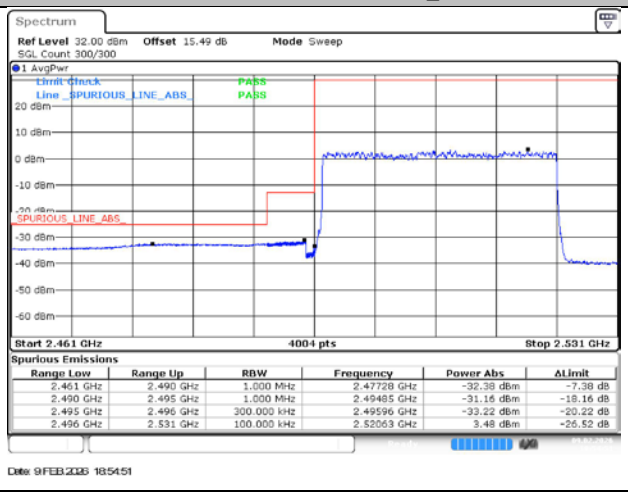
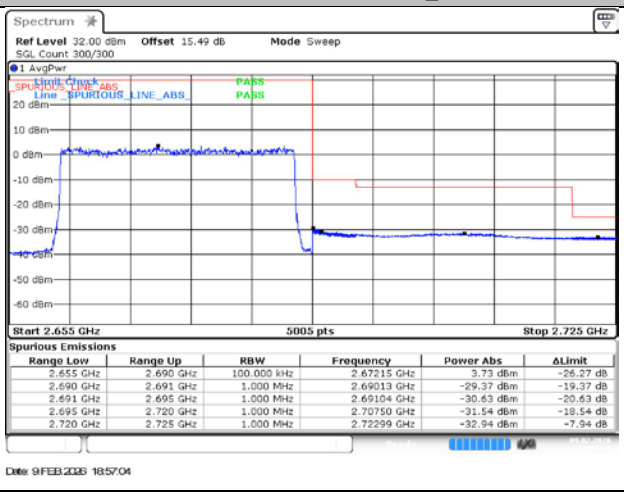
LCH / DFT-QPSK_Edge_1RB_Left	HCH / DFT-QPSK_Edge_1RB_Right
	
LCH / DFT-QPSK / Outer_Full	HCH / DFT-QPSK / Outer_Full
	

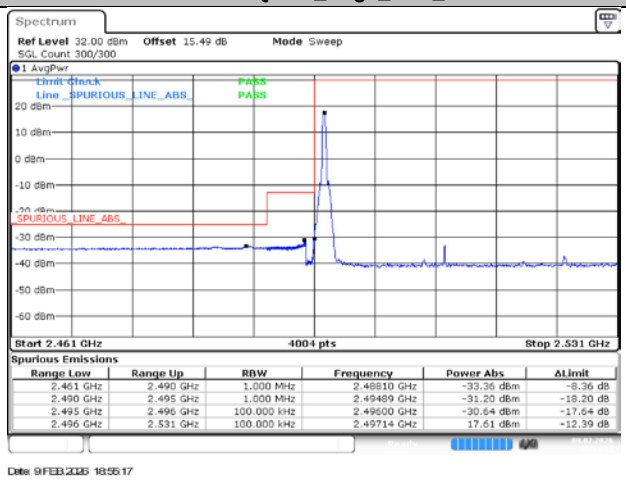
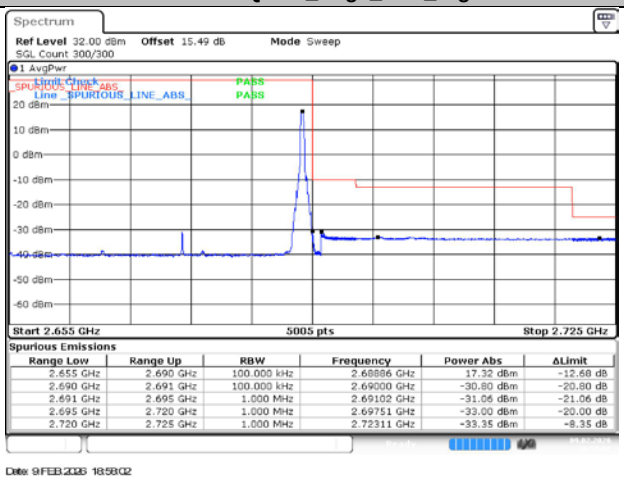
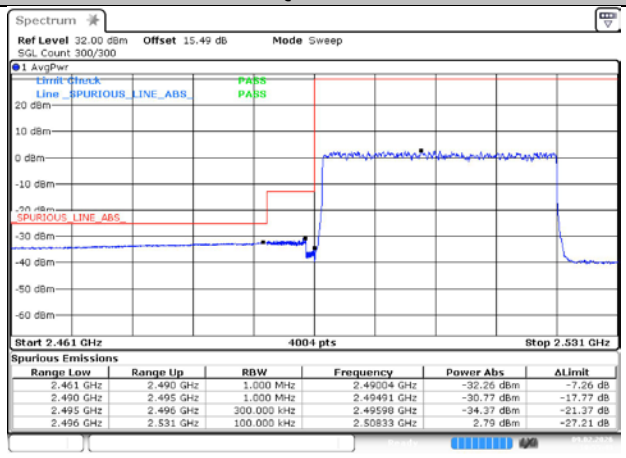
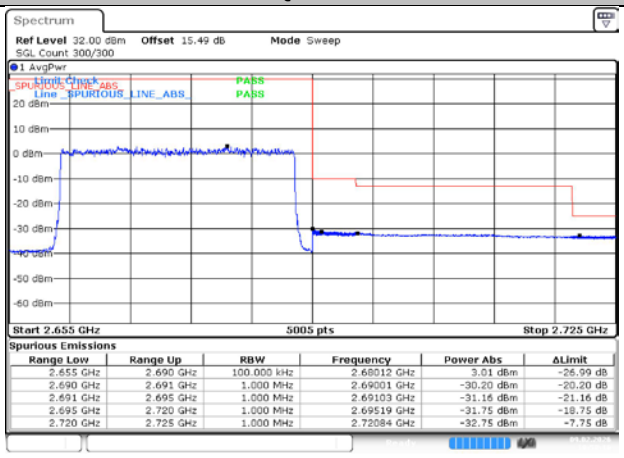
n41A / 30KHz / 20MHz	
LCH / DFT-Pi2BPSK_Edge_1RB_Left	HCH / DFT-Pi2BPSK_Edge_1RB_Right
	

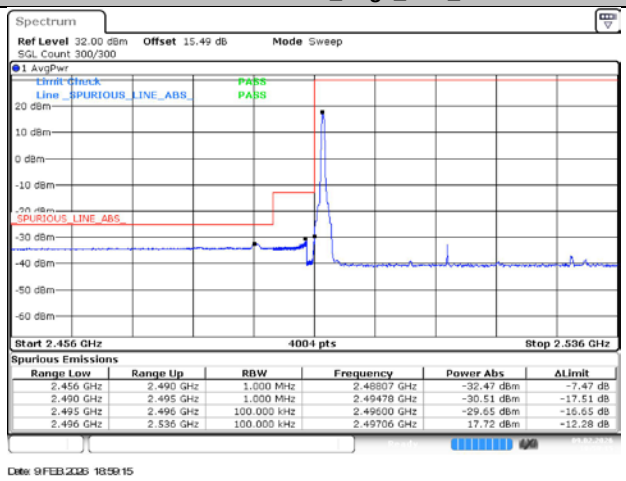
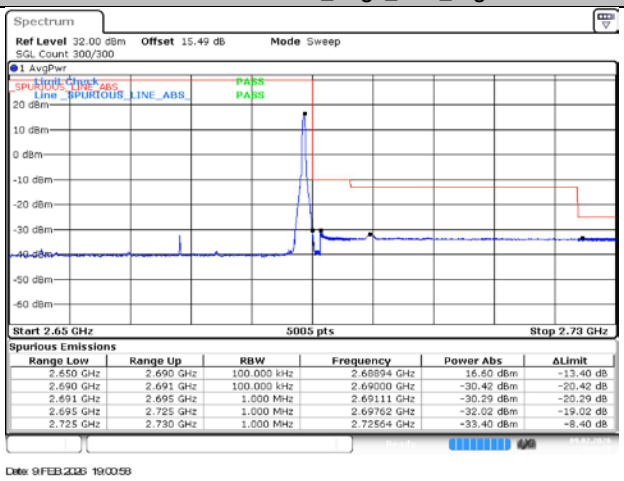
LCH / DFT-Pi2BPSK / Outer_Full	HCH / DFT-Pi2BPSK / Outer_Full
	
LCH / DFT-QPSK_Edge_1RB_Left	HCH / DFT-QPSK_Edge_1RB_Right
	
LCH / DFT-QPSK / Outer_Full	HCH / DFT-QPSK / Outer_Full
	

n41A / 30KHz / 25MHz	
LCH / DFT-Pi2BPSK_Edge_1RB_Left	HCH / DFT-Pi2BPSK_Edge_1RB_Right
	
LCH / DFT-Pi2BPSK / Outer_Full	HCH / DFT-Pi2BPSK / Outer_Full
	
LCH / DFT-QPSK_Edge_1RB_Left	HCH / DFT-QPSK_Edge_1RB_Right
	

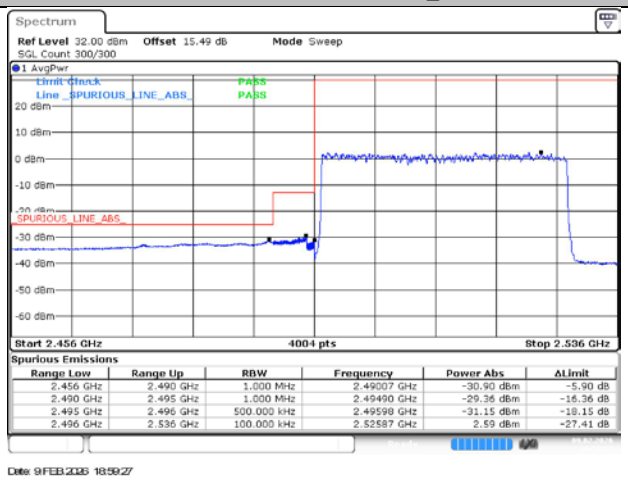
LCH / DFT-QPSK / Outer_Full	HCH / DFT-QPSK / Outer_Full
	

n41A / 30KHz / 30MHz	
LCH / DFT-Pi2BPSK_Edge_1RB_Left	HCH / DFT-Pi2BPSK_Edge_1RB_Right
	
LCH / DFT-Pi2BPSK / Outer_Full	HCH / DFT-Pi2BPSK / Outer_Full
	

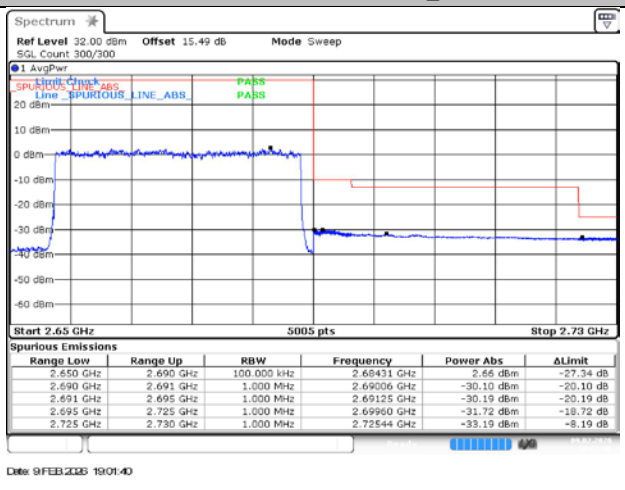
LCH / DFT-QPSK_Edge_1RB_Left	HCH / DFT-QPSK_Edge_1RB_Right
	
LCH / DFT-QPSK / Outer_Full	HCH / DFT-QPSK / Outer_Full
	

n41A / 30KHz / 35MHz	
LCH / DFT-Pi2BPSK_Edge_1RB_Left	HCH / DFT-Pi2BPSK_Edge_1RB_Right
	

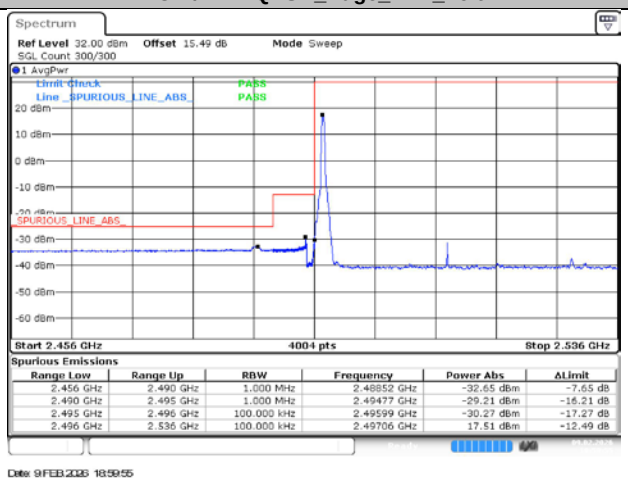
LCH / DFT-Pi2BPSK / Outer_Full



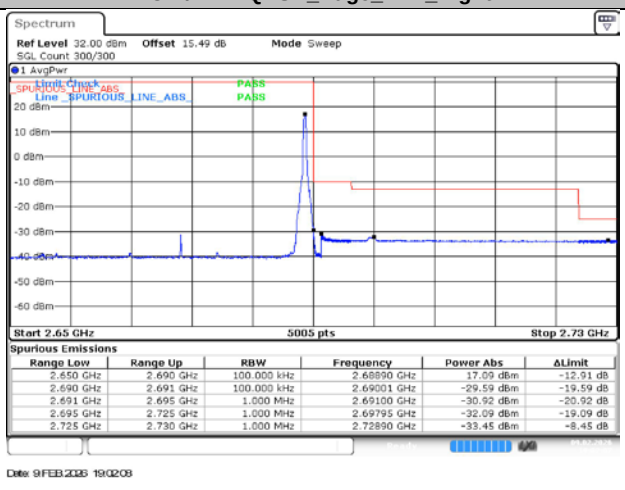
HCH / DFT-Pi2BPSK / Outer_Full



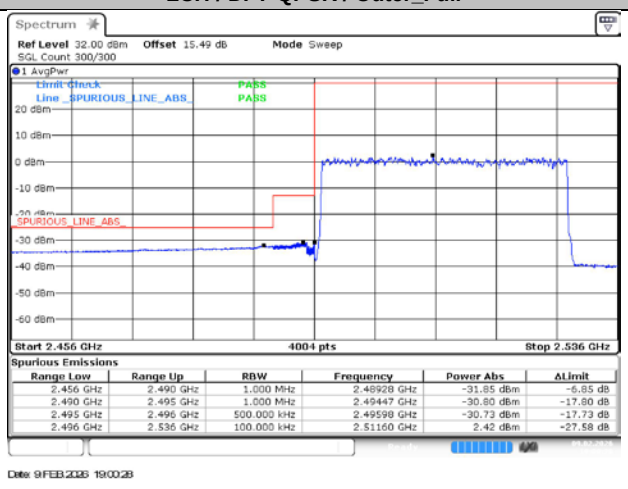
LCH / DFT-QPSK_Edge_1RB_Left



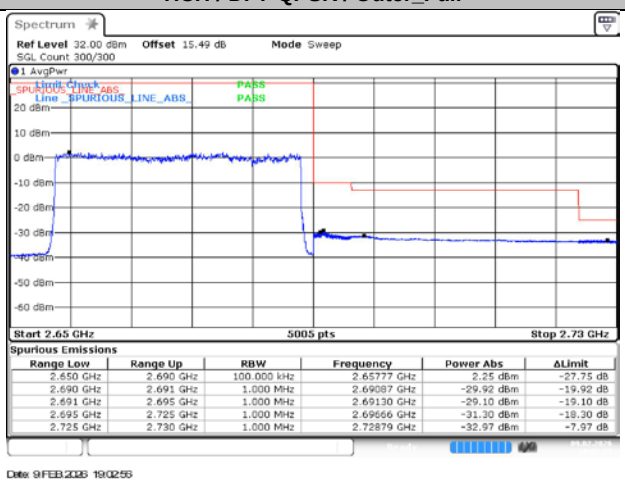
HCH / DFT-QPSK_Edge_1RB_Right

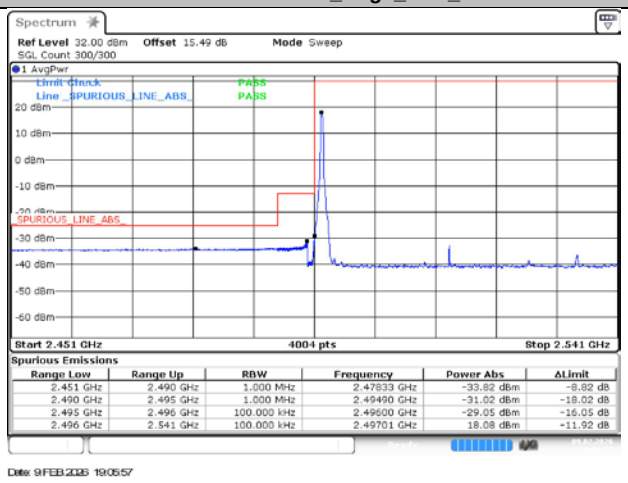
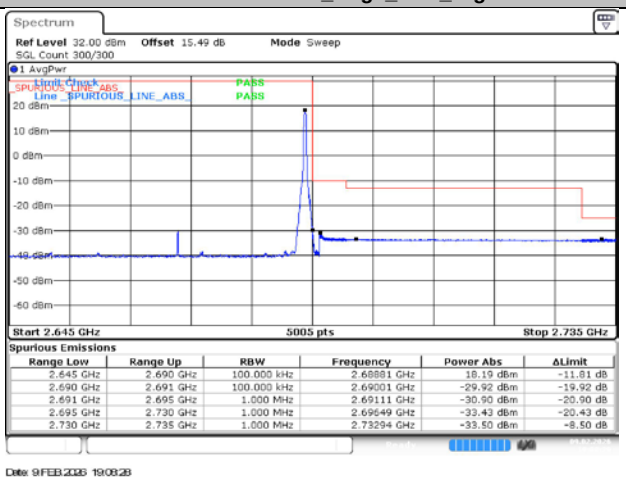
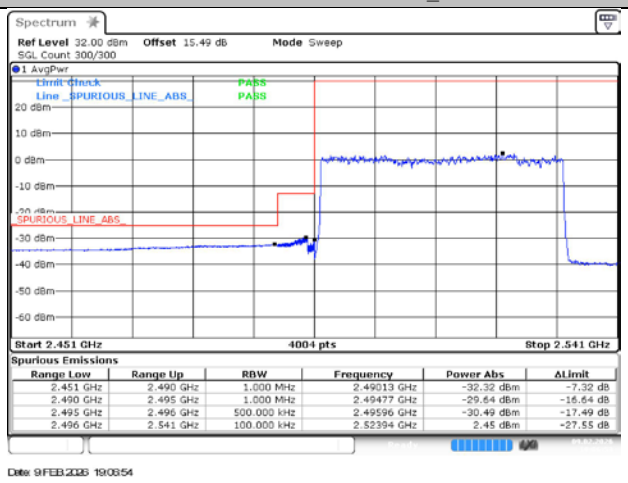
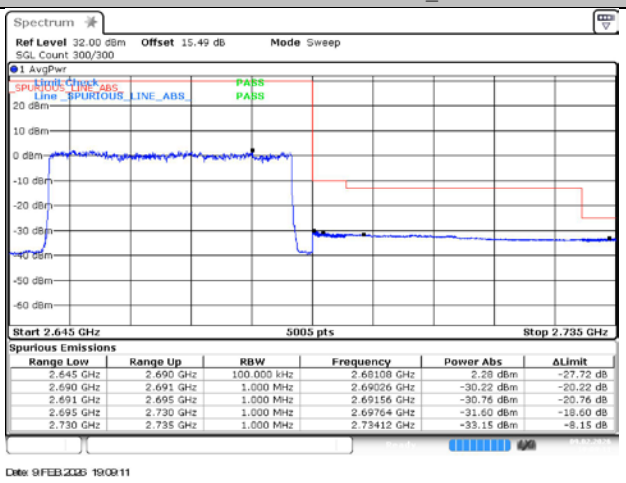
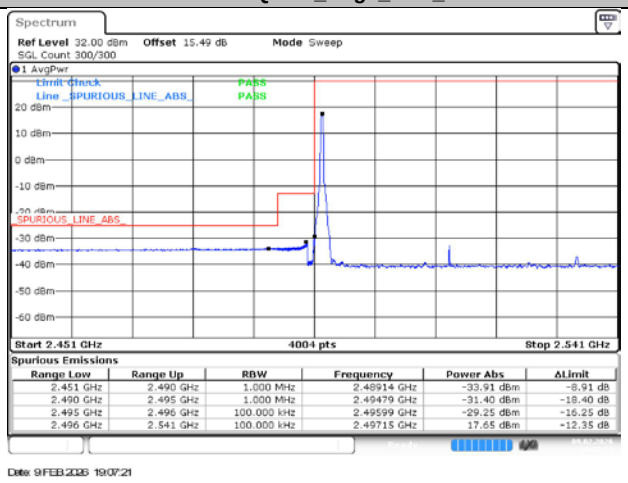
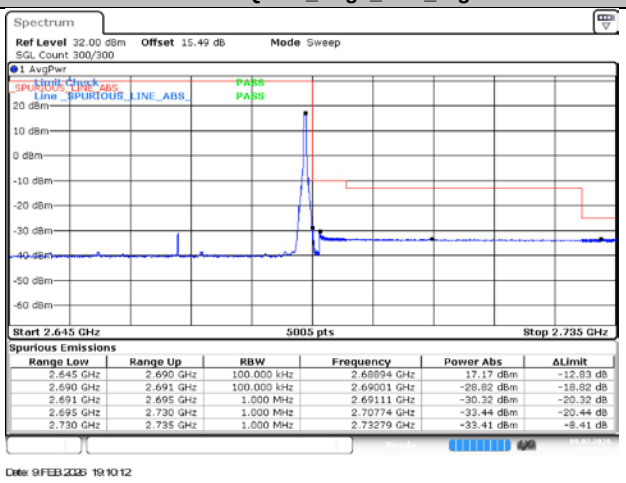


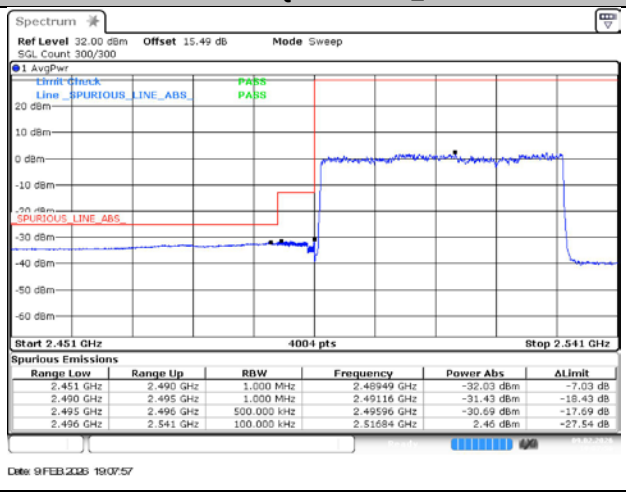
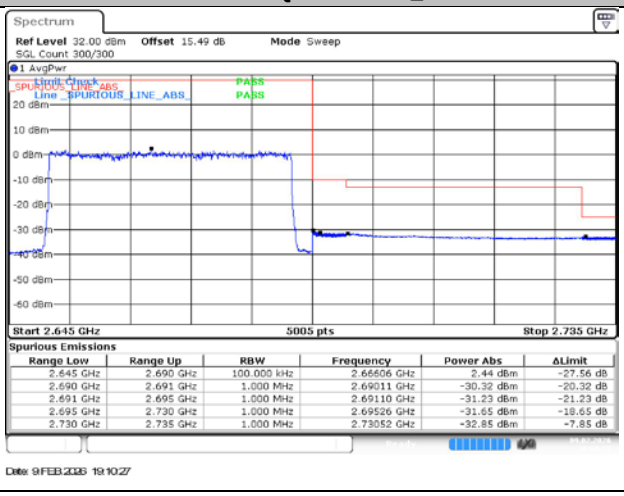
LCH / DFT-QPSK / Outer_Full

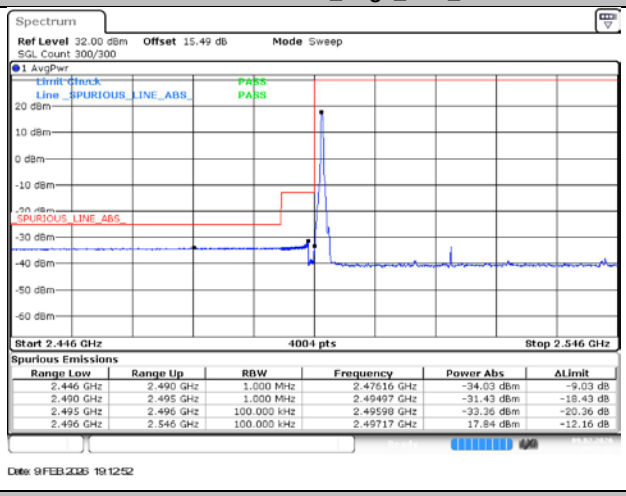
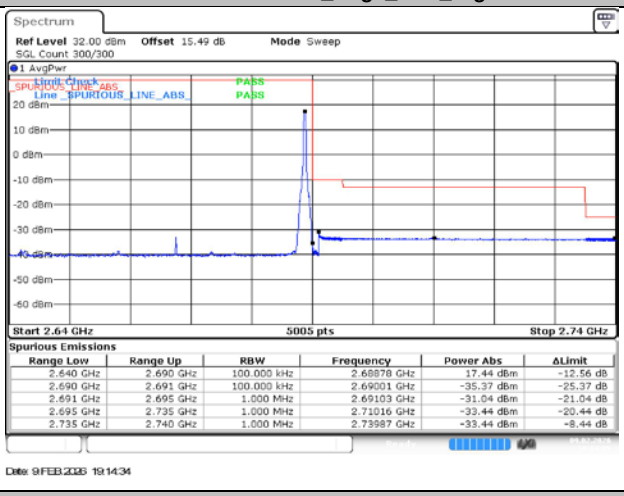
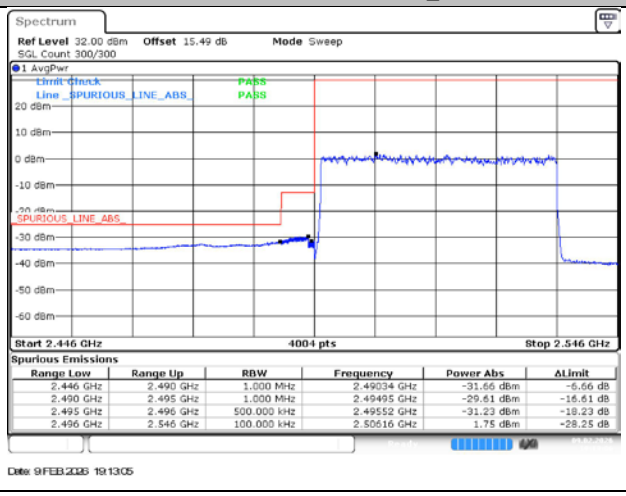
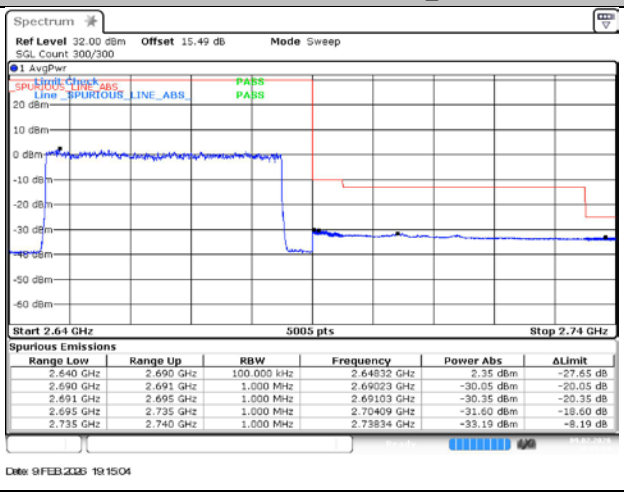


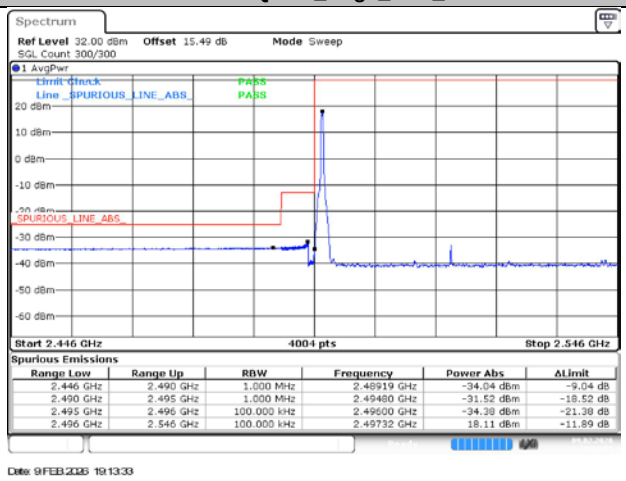
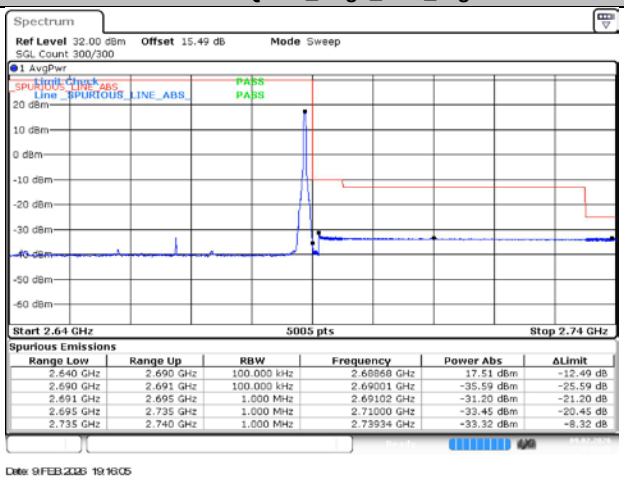
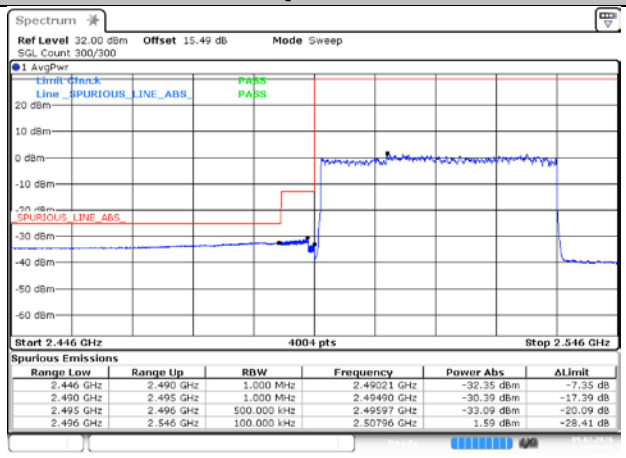
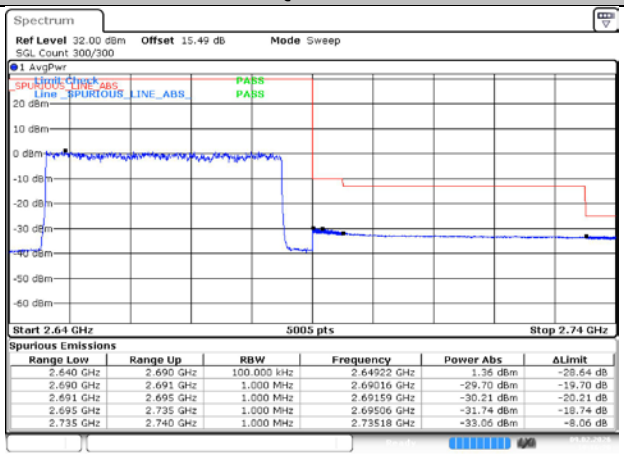
HCH / DFT-QPSK / Outer_Full

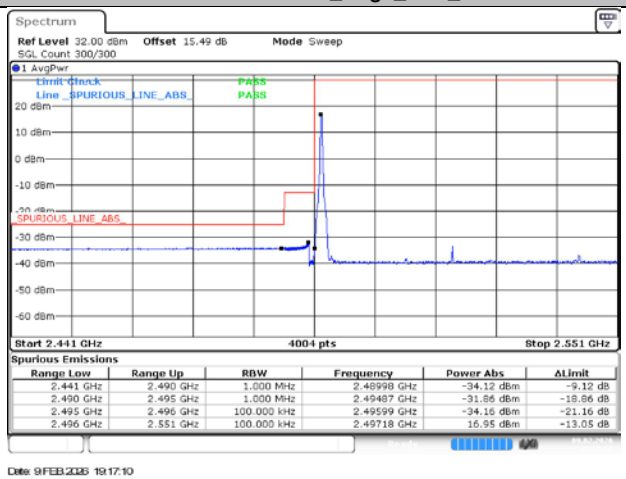
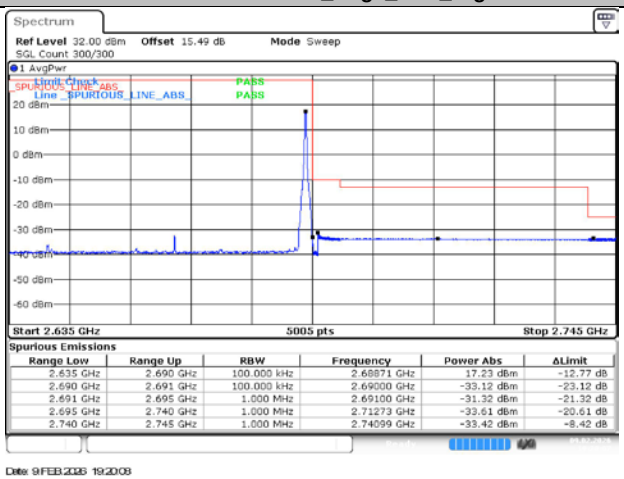


n41A / 30KHz / 40MHz	
LCH / DFT-Pi2BPSK_Edge_1RB_Left	HCH / DFT-Pi2BPSK_Edge_1RB_Right
	
LCH / DFT-Pi2BPSK / Outer_Full	HCH / DFT-Pi2BPSK / Outer_Full
	
LCH / DFT-QPSK_Edge_1RB_Left	HCH / DFT-QPSK_Edge_1RB_Right
	

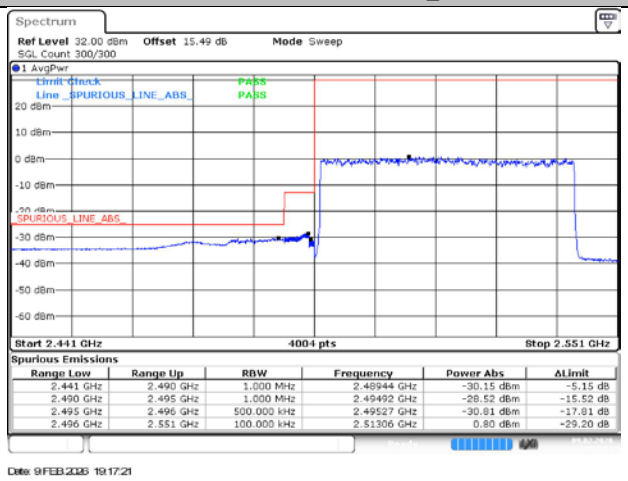
LCH / DFT-QPSK / Outer_Full	HCH / DFT-QPSK / Outer_Full
	

n41A / 30KHz / 45MHz	
LCH / DFT-Pi2BPSK_Edge_1RB_Left	HCH / DFT-Pi2BPSK_Edge_1RB_Right
	
LCH / DFT-Pi2BPSK / Outer_Full	HCH / DFT-Pi2BPSK / Outer_Full
	

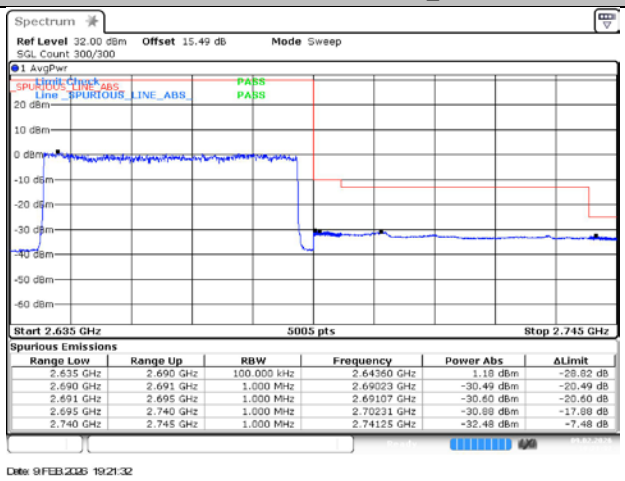
LCH / DFT-QPSK_Edge_1RB_Left	HCH / DFT-QPSK_Edge_1RB_Right
	
LCH / DFT-QPSK / Outer_Full	HCH / DFT-QPSK / Outer_Full
	

n41A / 30KHz / 50MHz	
LCH / DFT-Pi2BPSK_Edge_1RB_Left	HCH / DFT-Pi2BPSK_Edge_1RB_Right
	

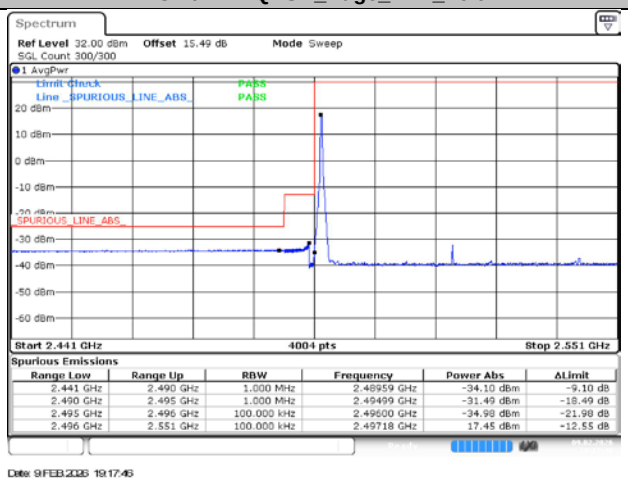
LCH / DFT-Pi2BPSK / Outer_Full



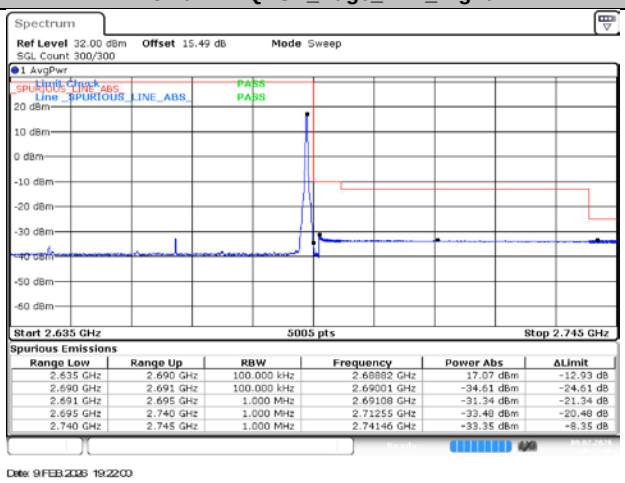
HCH / DFT-Pi2BPSK / Outer_Full



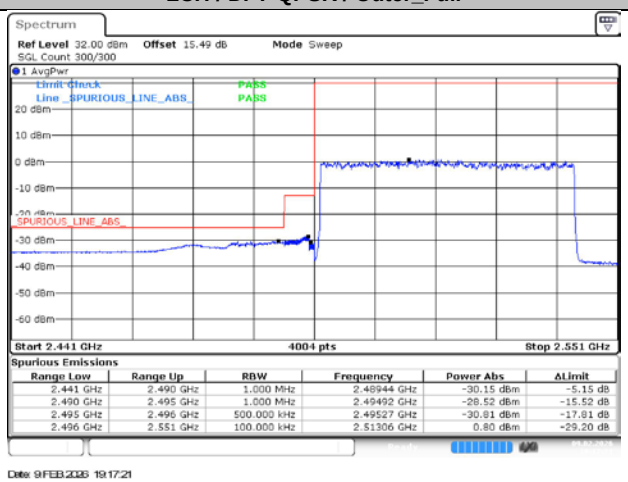
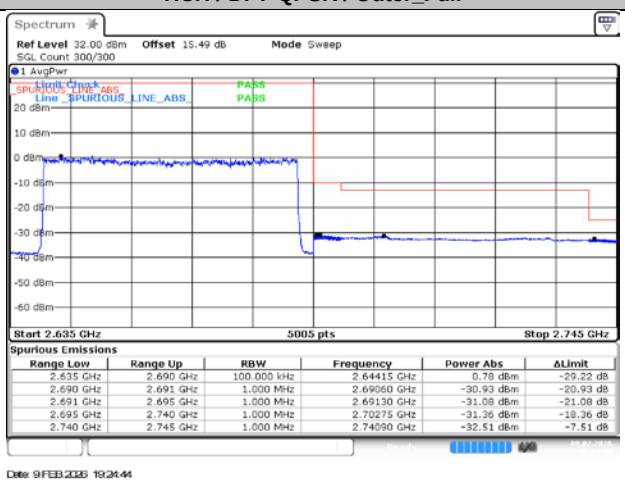
LCH / DFT-QPSK_Edge_1RB_Left



HCH / DFT-QPSK_Edge_1RB_Right



LCH / DFT-QPSK / Outer_Full



HCH / DFT-QPSK / Outer_Full