



MOTOROLA

Date: June 23, 2010

Subject: Request for additional information regarding FCC ID: IHDT56LD2

Reference:

Correspondence Reference Number:	IHD100722
Confirmation Number:	1Y1006110722-5
Date of Original Email:	June 20, 2010

Prepared by:

Andrew Bachler, Principal Staff Engineer
Motorola Mobile Device Business
Libertyville, Illinois 60048

Questions and responses follow:

1. Please submit block diagrams for the DTS and DSS transmitter chips that show some detail of the RF sections of those transmitters, as well as listing the clock/oscillator values (p. 3/5 of the BLD document is a schematic, not a block diagram).

RESPONSE: Please refer to the 2nd revised block diagrams submitted online.

2. Please verify that different DTS data rates were investigated in the course of performing the EMC measurements, and that the data provided represents the worst case data. Please note that the test report should clearly state this.

RESPONSE: We verified and clearly state this under "General and Special Conditions" of the report. Please refer to the 2nd revised DTS EMC report submitted online.

3. Please provide RF conducted bandedge plots for the DTS, in the appropriate modes of operation and data rates, demonstrating compliance with the 20 dBc limit.

RESPONSE: Please refer to page 40-43 of the 2nd revised DTS EMC report submitted online.

4. Please correct the following typo: DTS EMC report p. 5/44: "LISN" is misspelled in the text.

RESPONSE: Please refer to the 2nd revised DTS EMC report submitted online.

5. FYI: as requested previously, in the future please provide data, such as output power and radiated emissions, for the DTS and DSS test reports, in tabular form, instead of submitting scores of plots (only the plots for the worst-case values need be submitted). This will greatly aid in reducing the amount of time required for review.

RESPONSE: Noted.