



MOTOROLA

Date: June 30, 2010

Subject: Request for additional information regarding FCC ID: IHDT56LD3

Reference:

Correspondence Reference Number:	IHD100746
Confirmation Number:	1Y1006150746
Date of Original Email:	June 22, 2010

Prepared by:

Andrew Bachler, Principal Staff Engineer
Motorola Mobile Device Business
Libertyville, Illinois 60048

Questions and responses follow:

1. Please submit block diagrams for the DTS and DSS transmitter chips that show some detail of the RF sections of those transmitters, as well as listing the clock/oscillator values (p. 3/5 of the BLD document is a schematic, not a block diagram).

RESPONSE: Please refer to the revised block diagrams submitted online.

2. Exhibit 6 indicates EIRP levels exceed 2 Watts. This is not permitted per Part 24.232(c). Please address.

RESPONSE: Please refer to the revised PCE EMC report submitted online.