

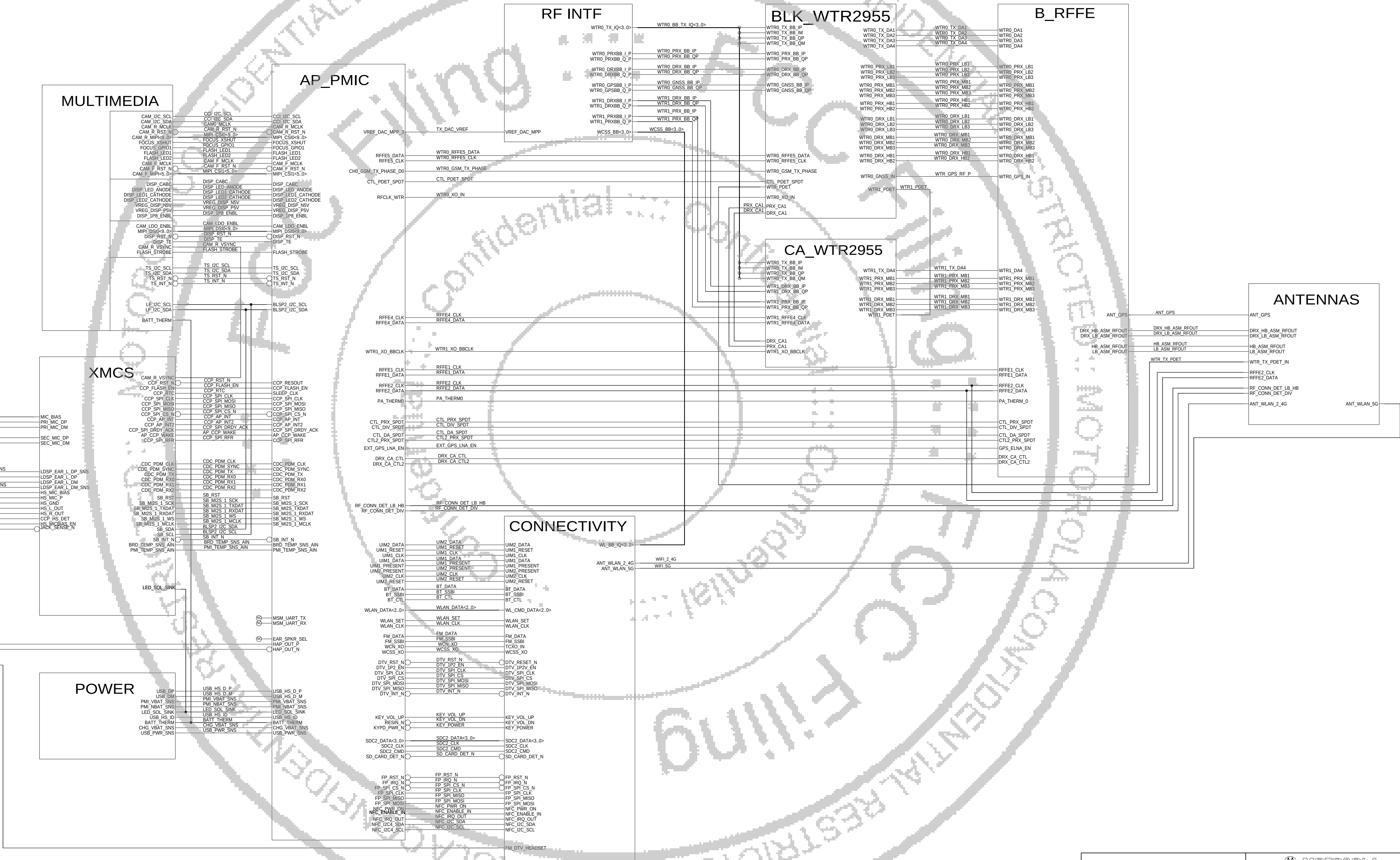
Permanent Confidentiality Requested**Exhibit 5. Schematic Diagrams of the Transmitter Block – 47 CFR 2.1003(c)(10), and RSP100, Section 5.**

The transmitter schematics below illustrate the circuitry for frequency generation, determination, and stabilization, as well as modulation limiting, power control, and suppression of spurious emissions. To view text and component values zoom.

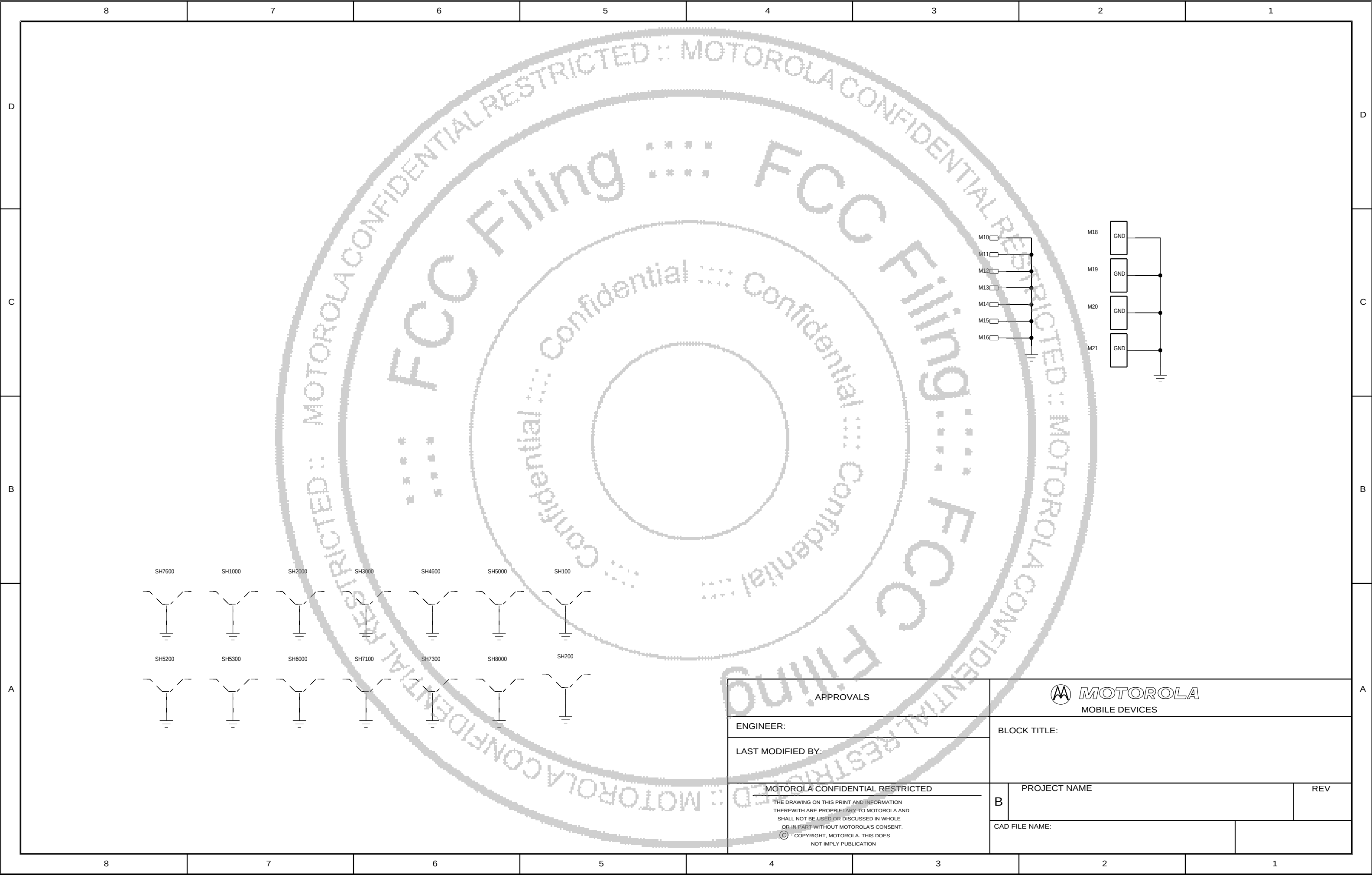
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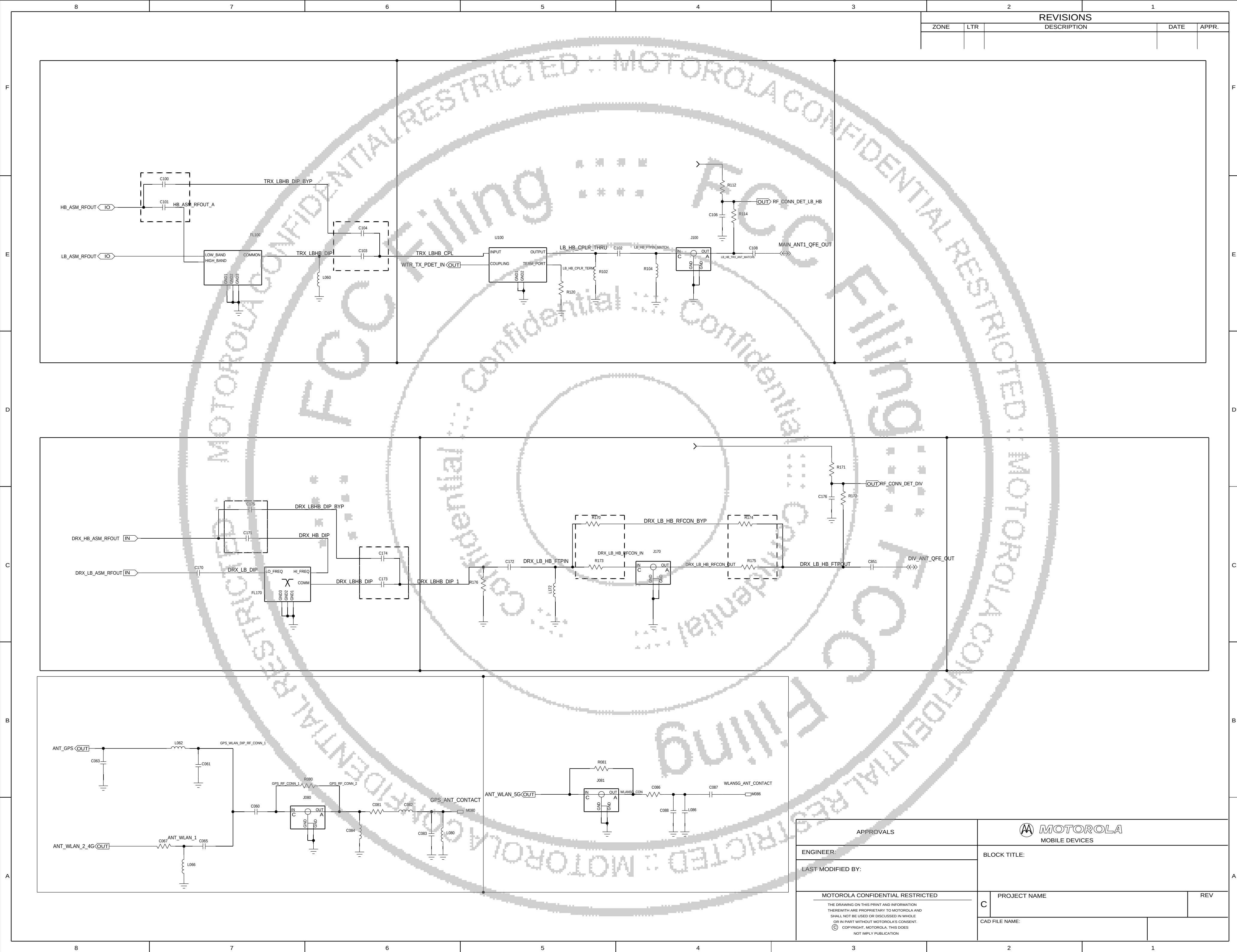
Top level	Page 2
Main RF front end, Diversity RF front end, GPS antenna, Bluetooth/WiFi antenna	Page 4
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Bluetooth and WiFi transceiver front end	Page 10
Power Management, including RF clock generation	Page 36
3GPP transceiver RF connections	Page 43
GSM, WCDMA, LTE Main TX/RX RF Front End Switch	Page 47
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WCDMA B1/B5/B8, LTE B1/B3/B5/B8/B40 diversity/MIMO antenna front end	Page 52
GPS RF front end	Page 53

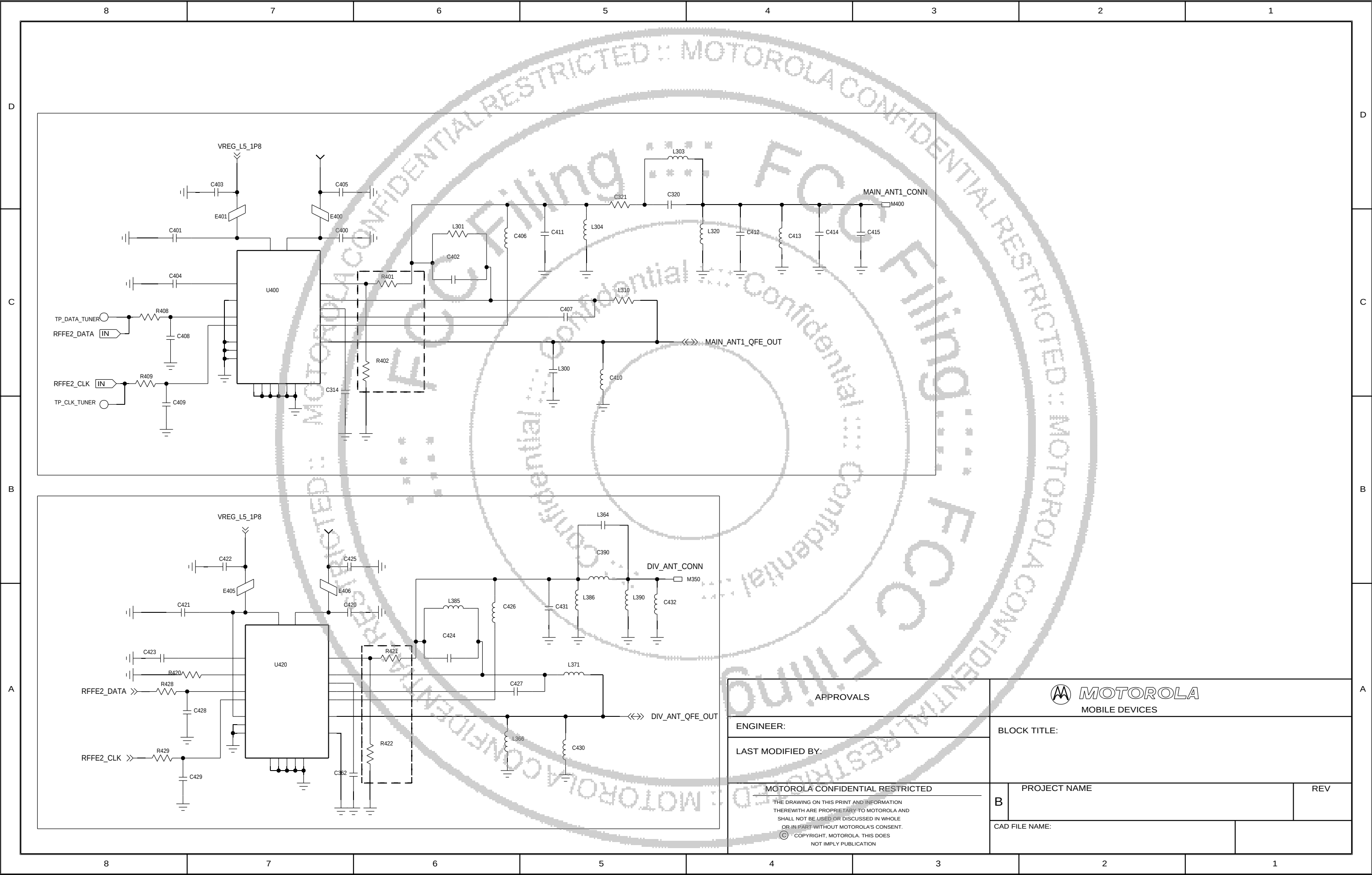
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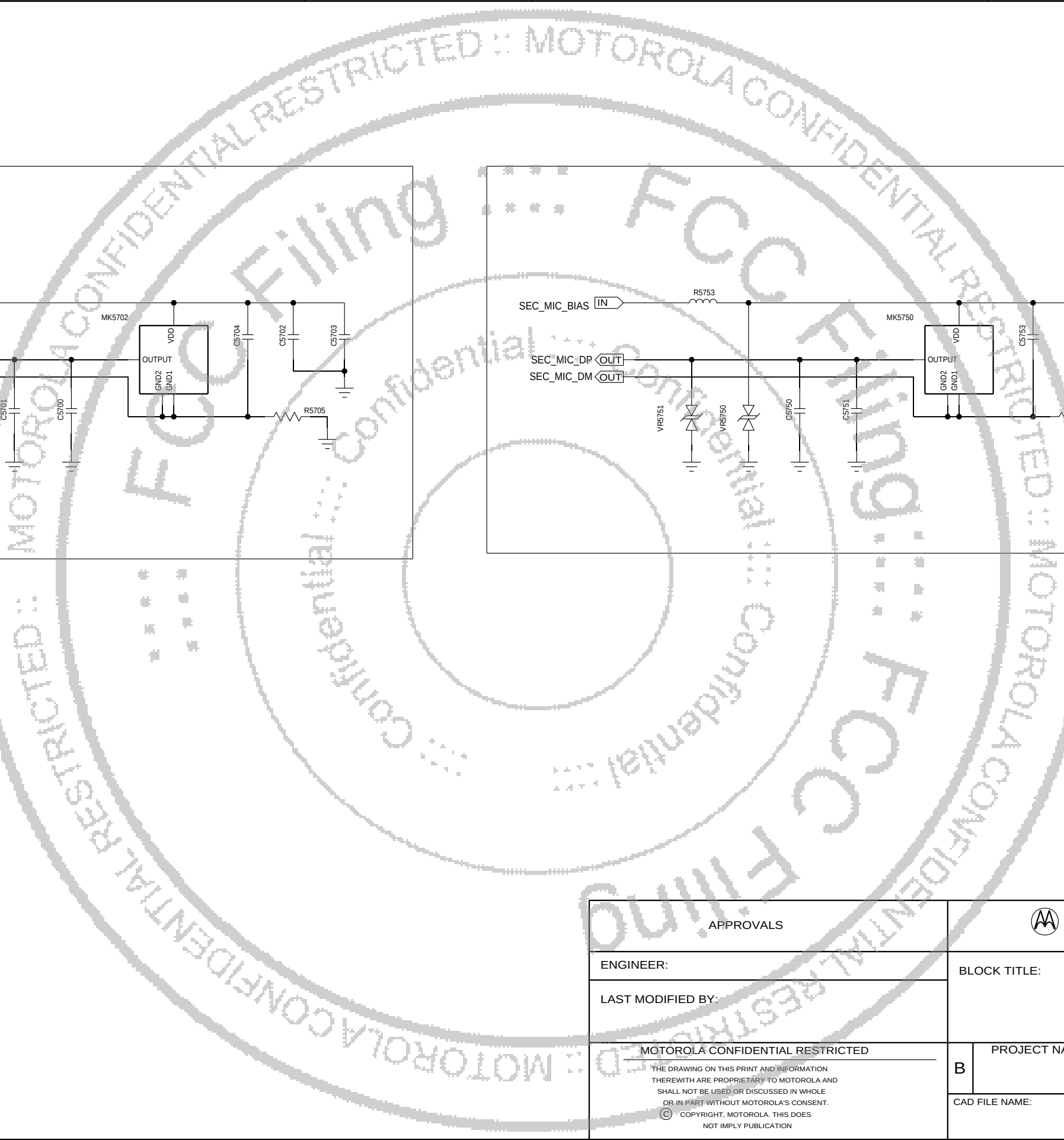


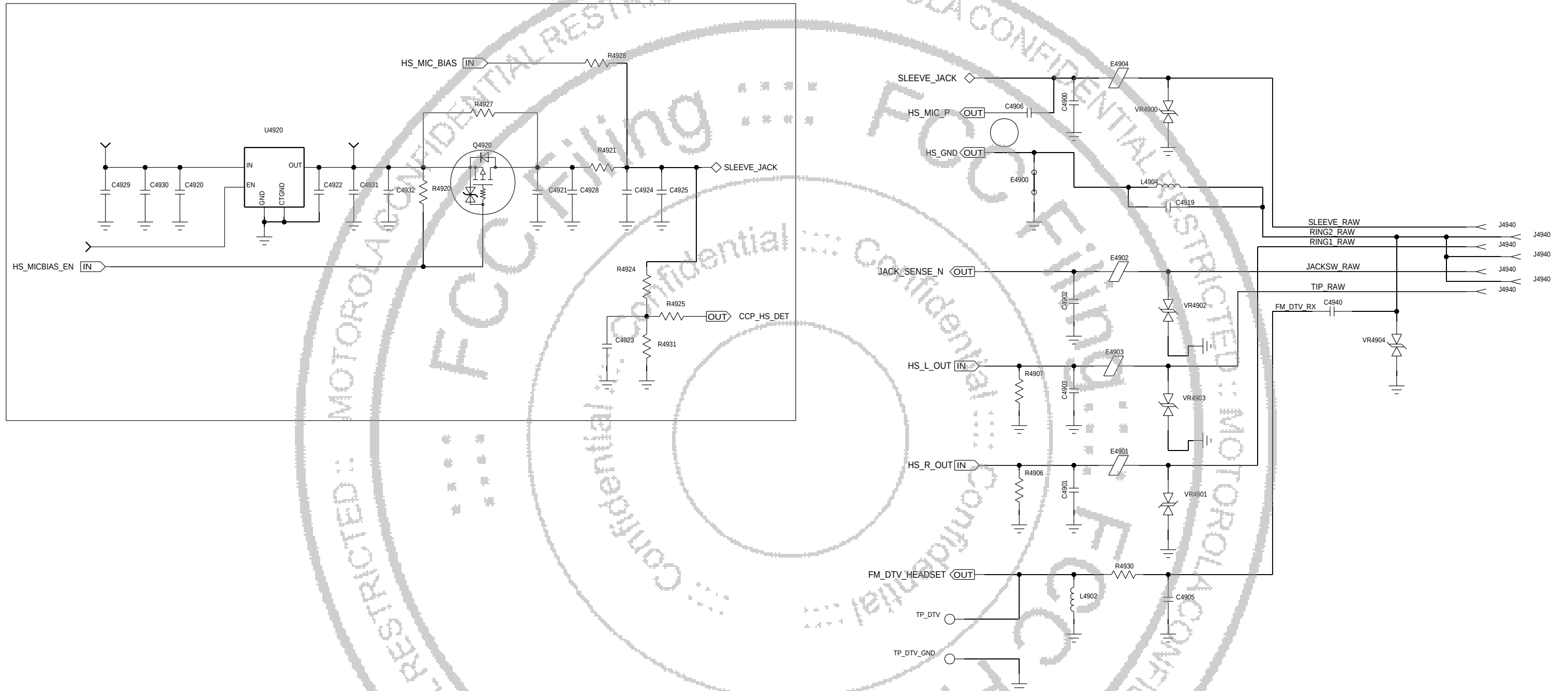
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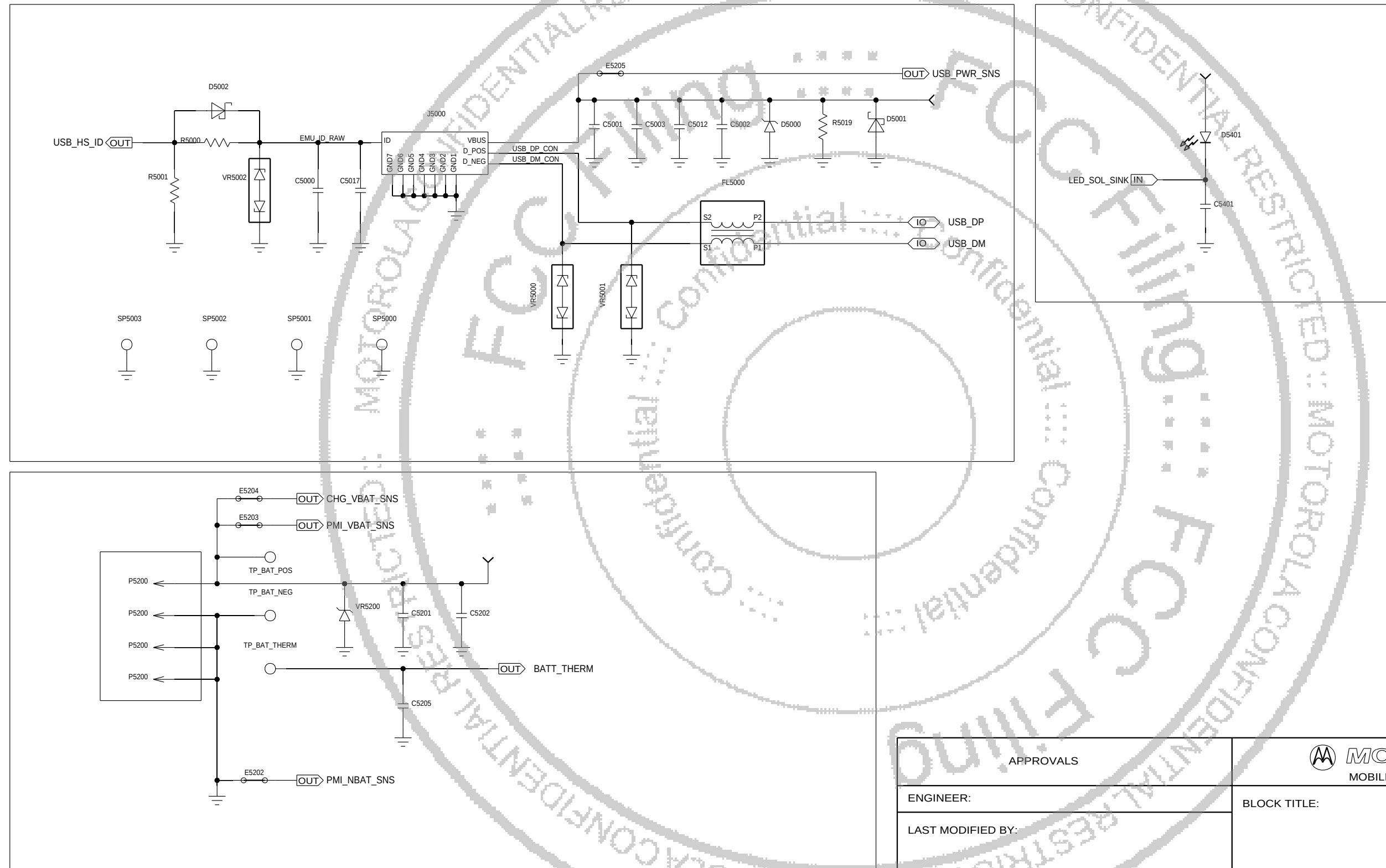




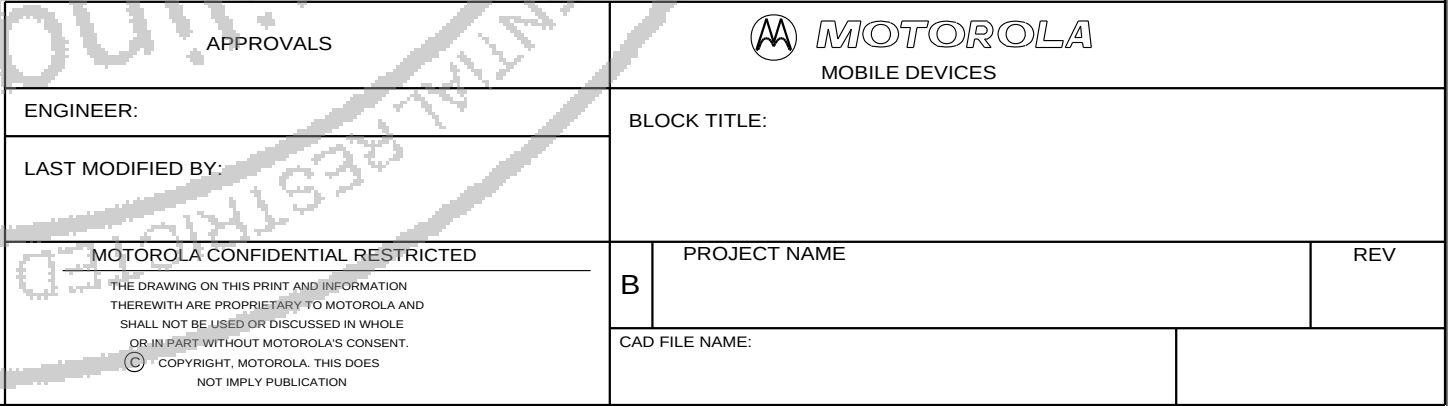


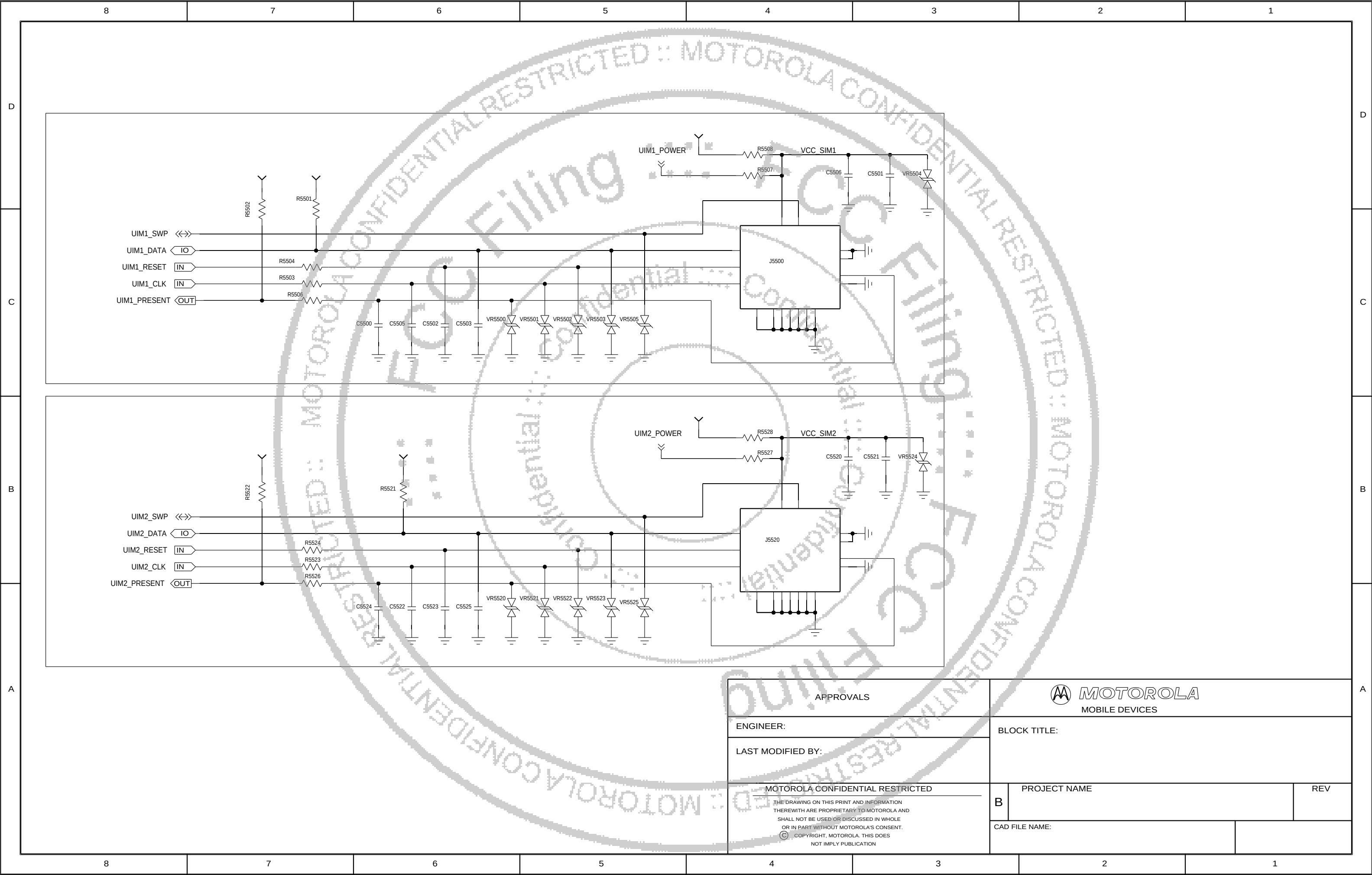






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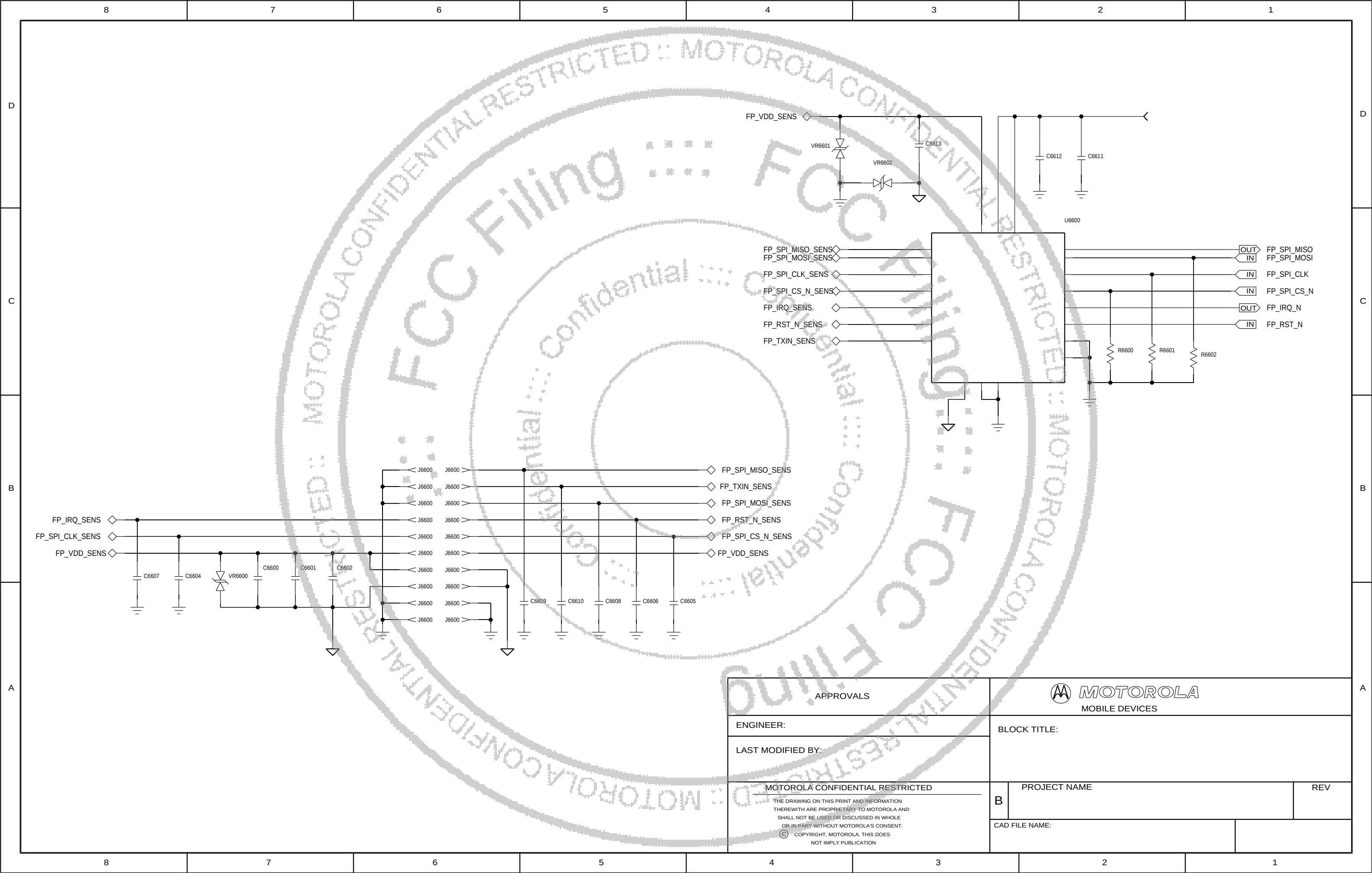
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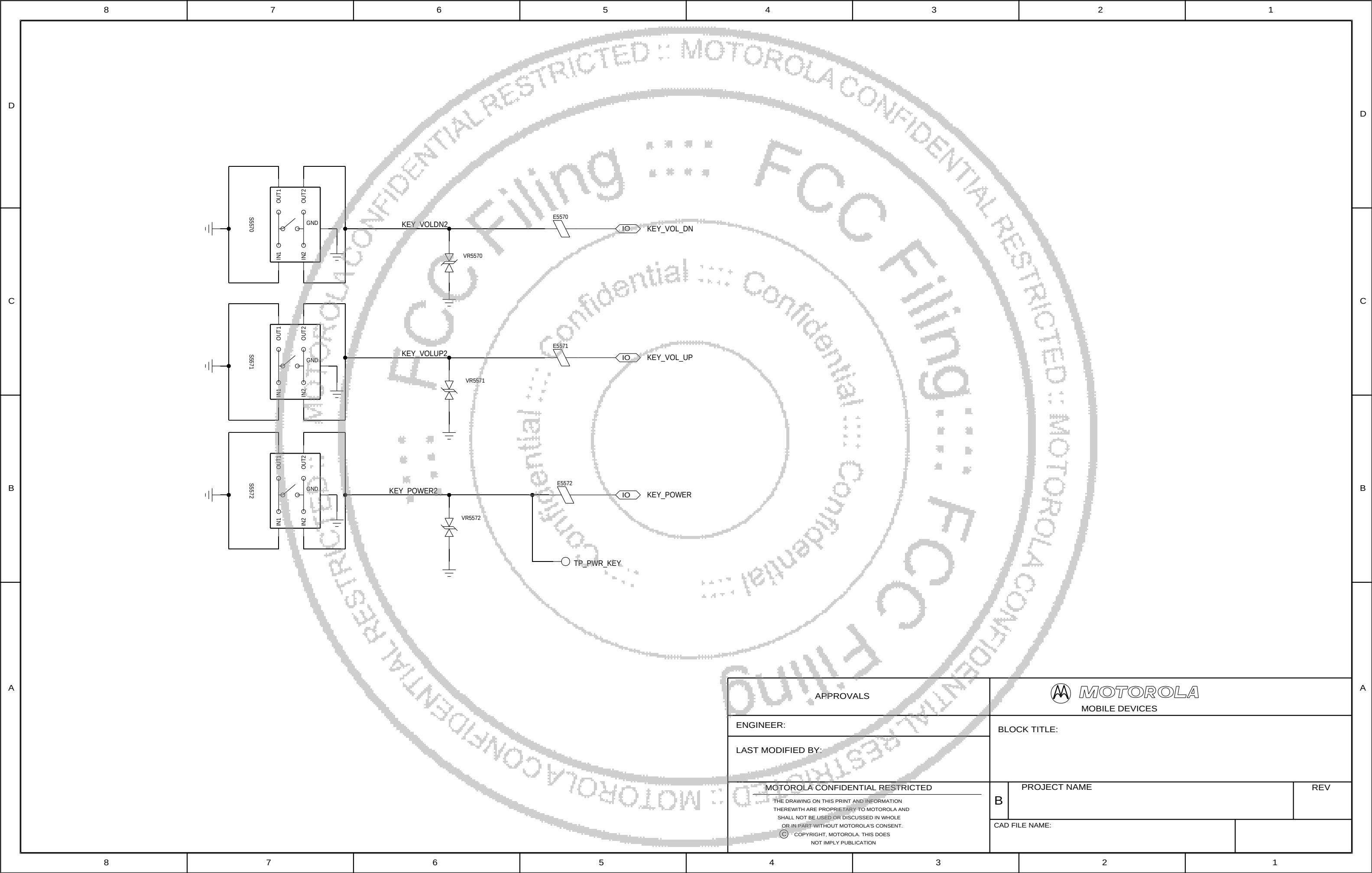
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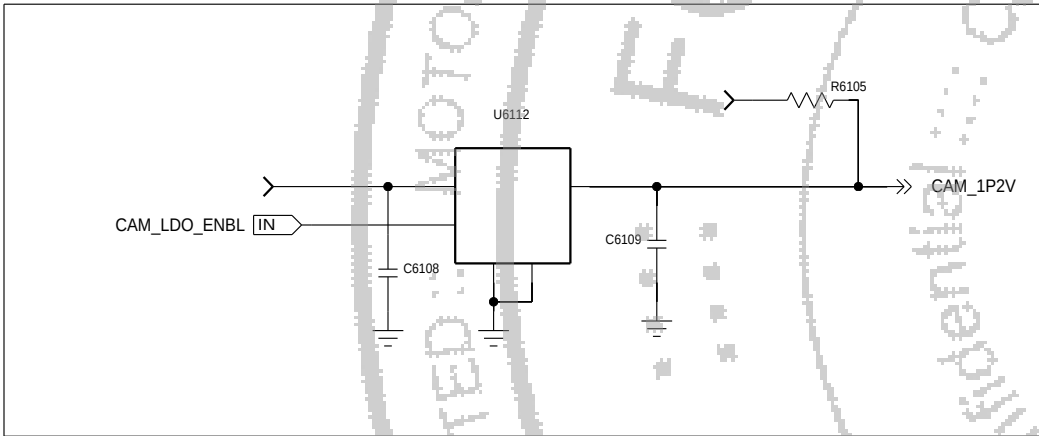
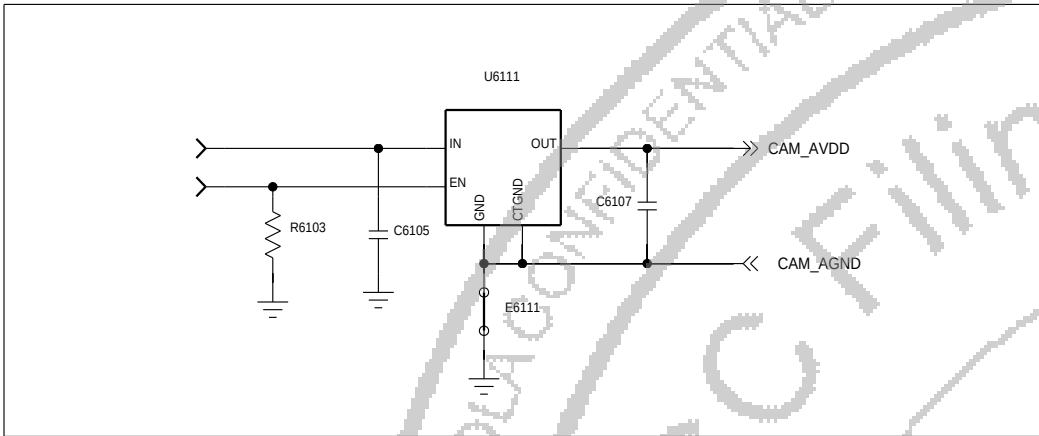
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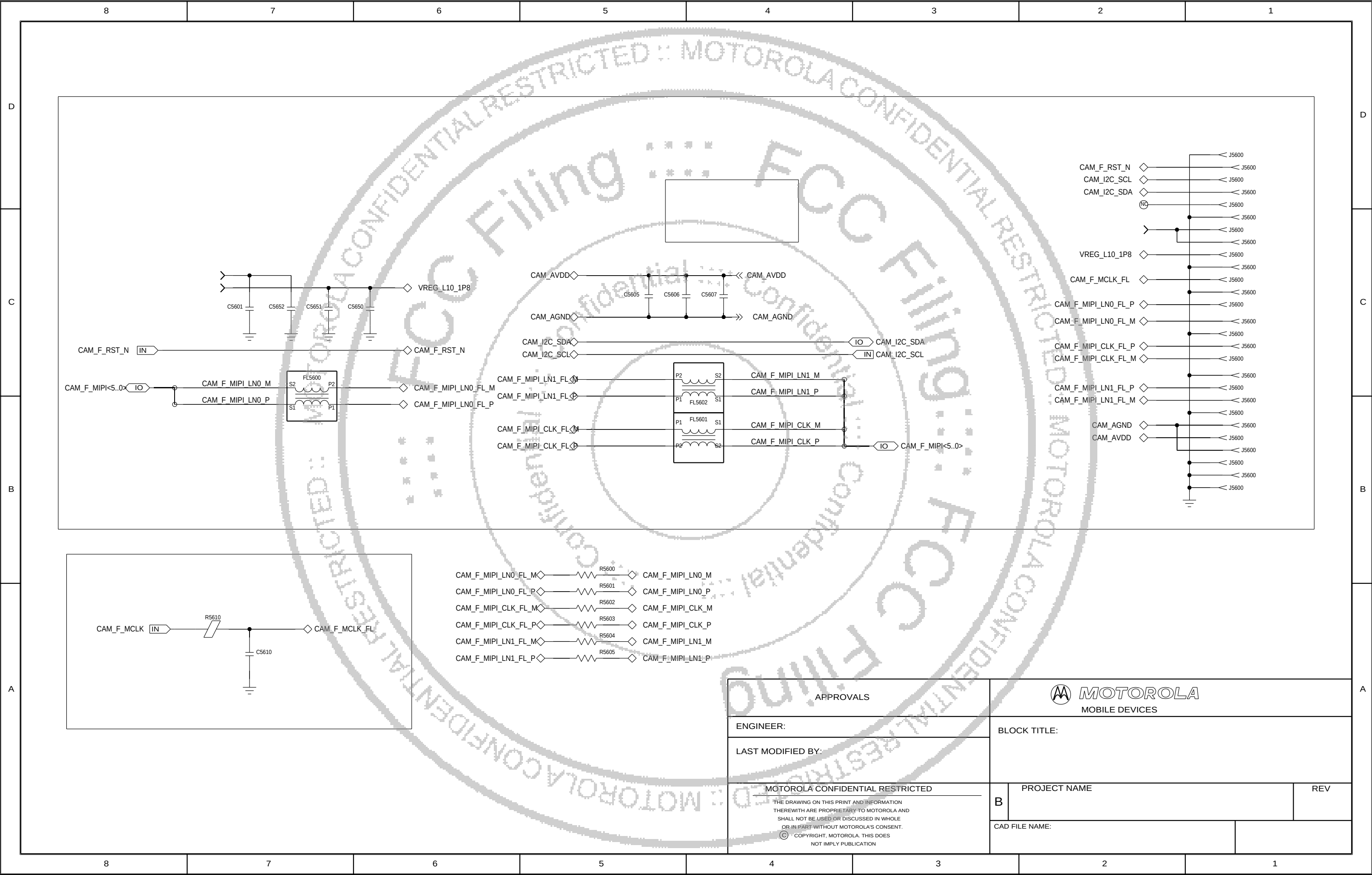
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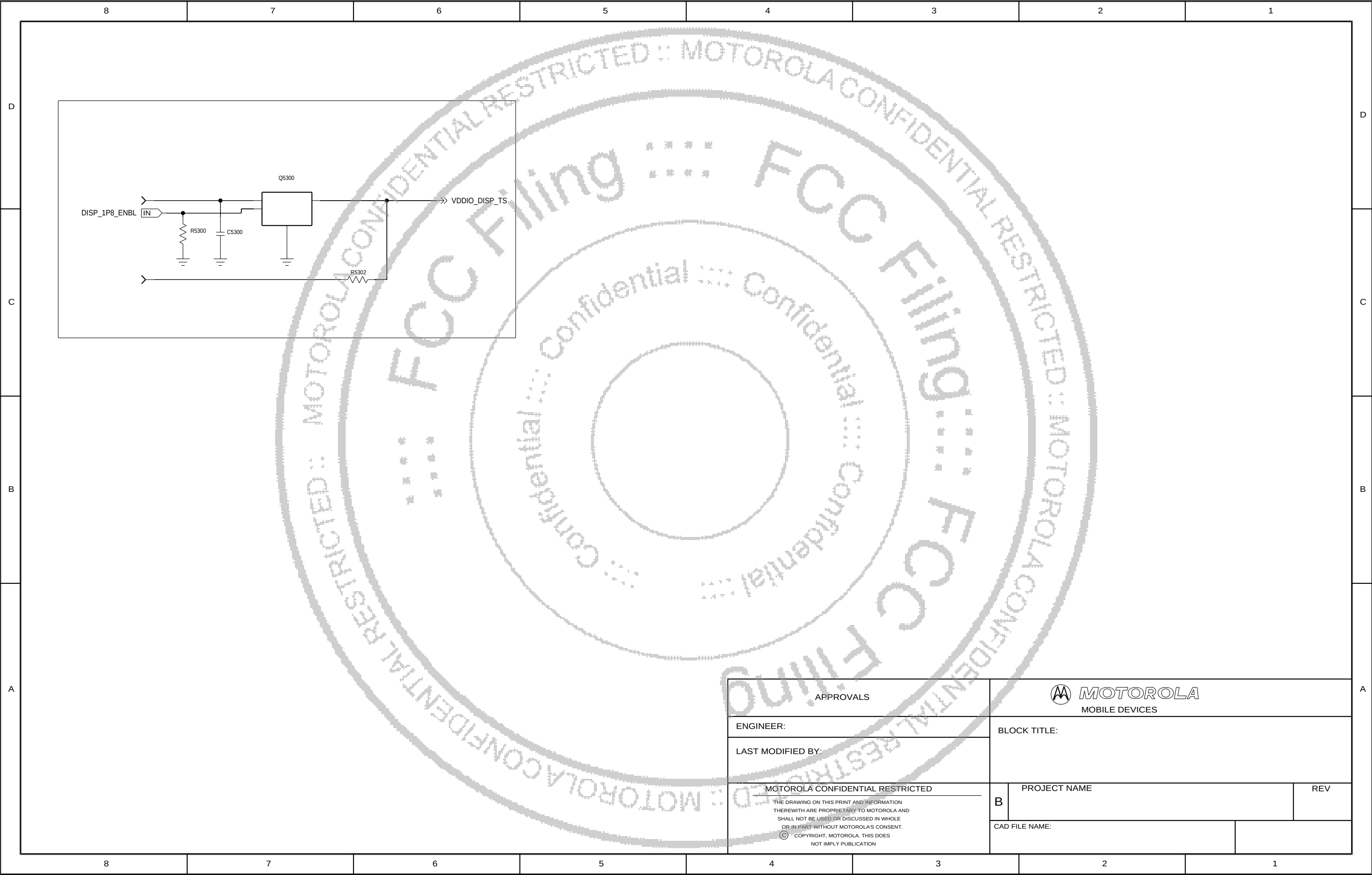


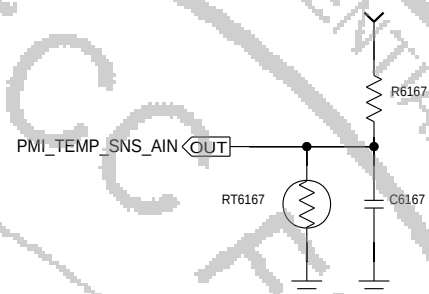




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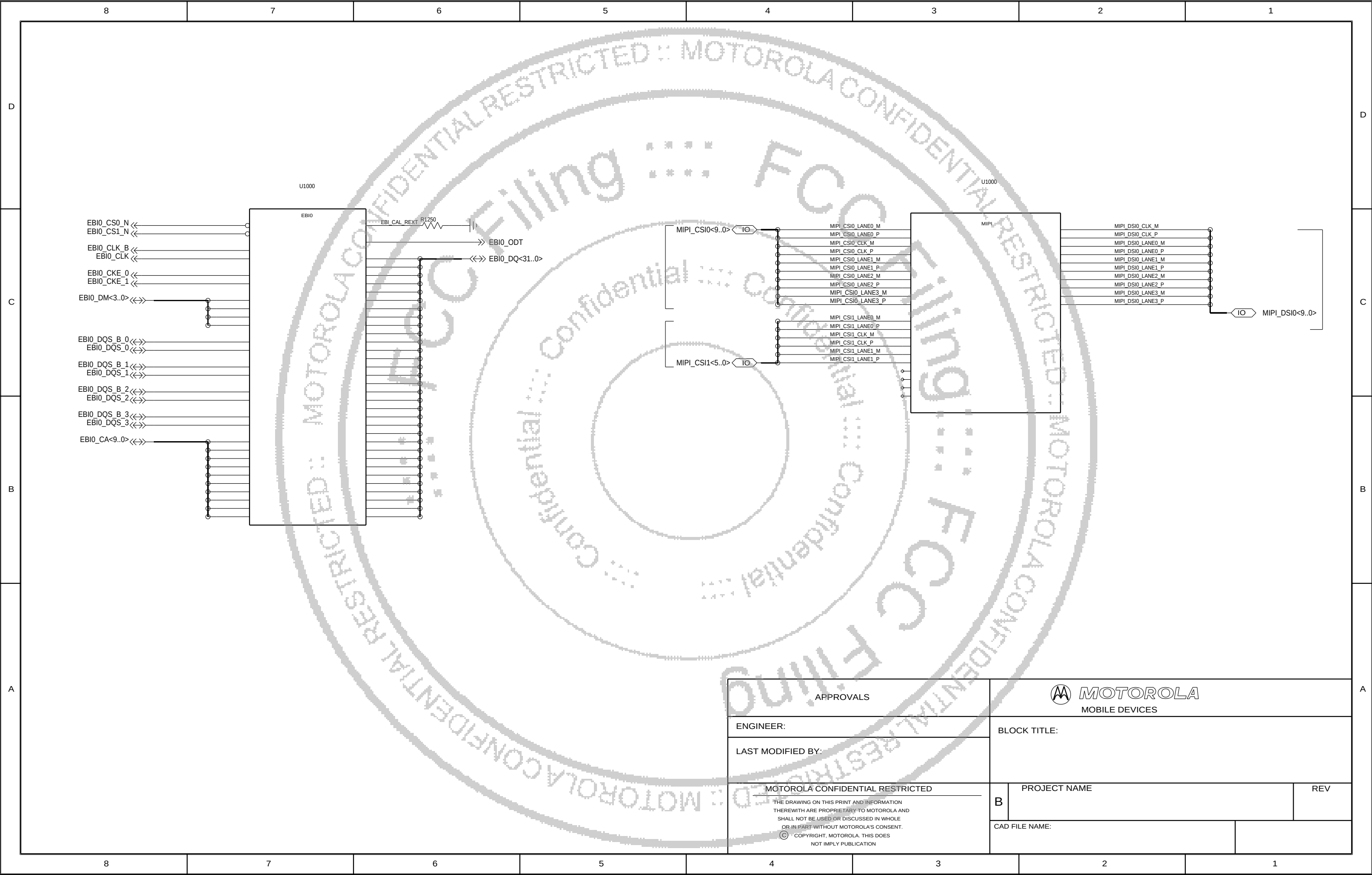
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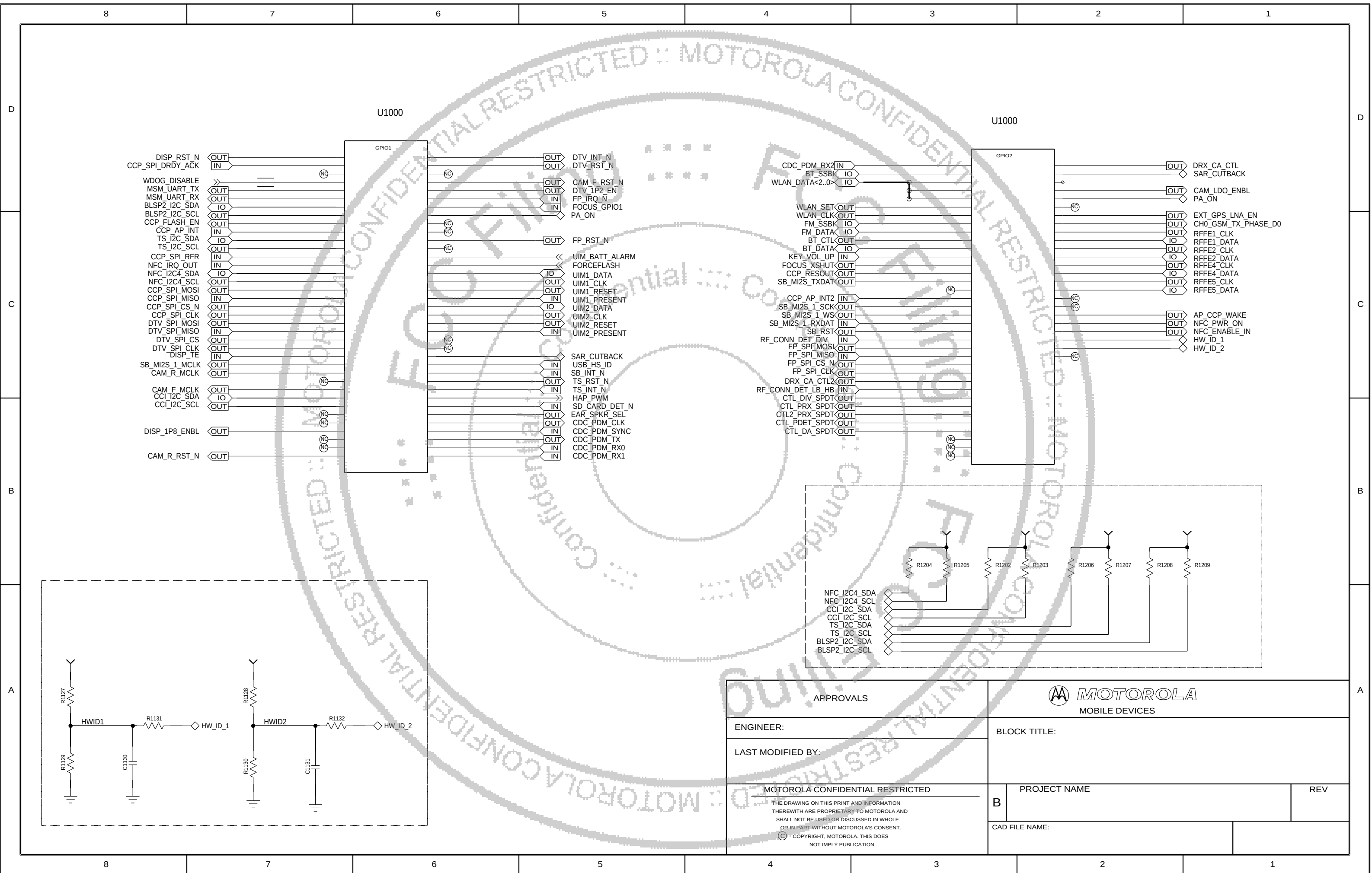
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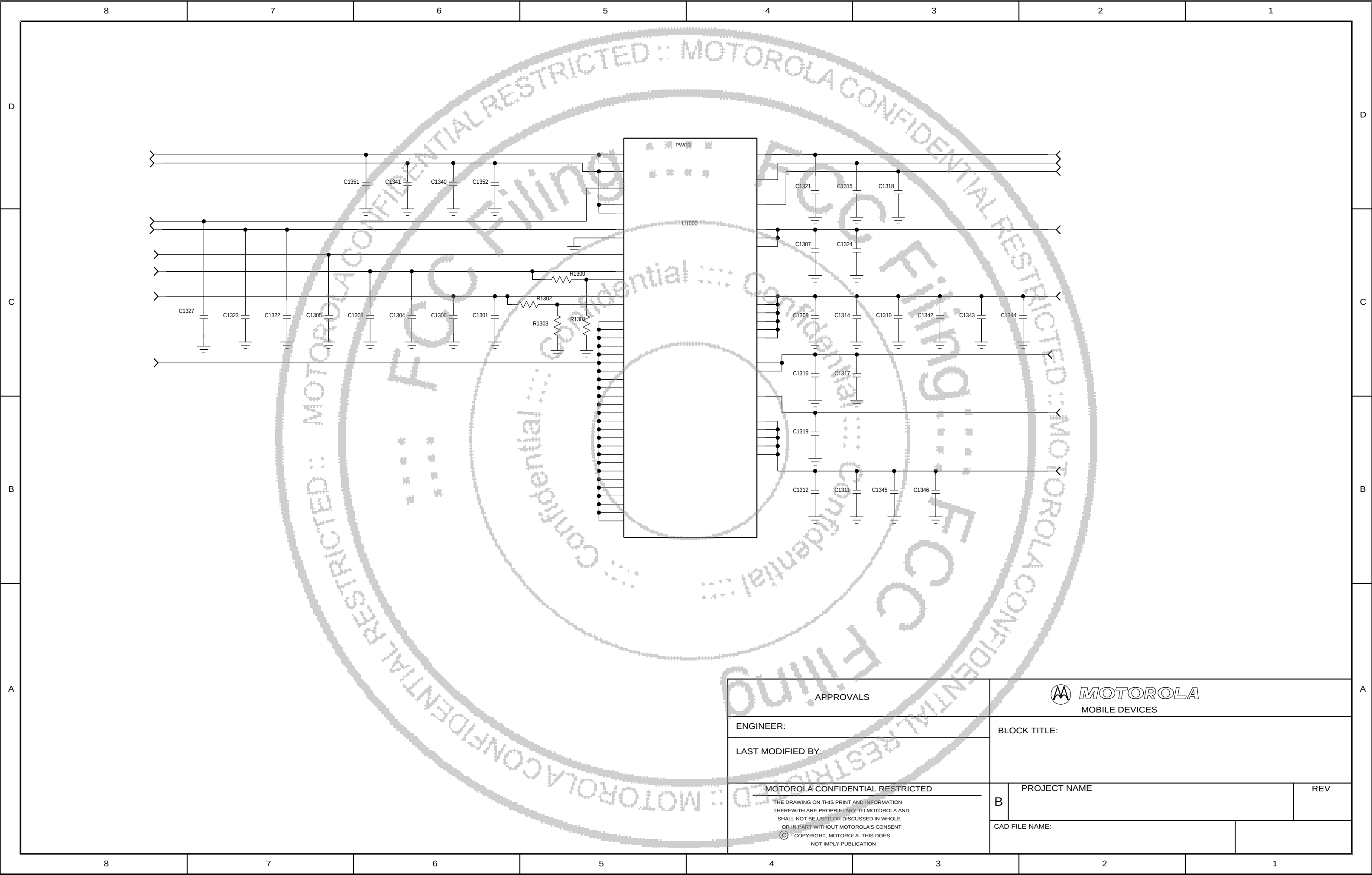
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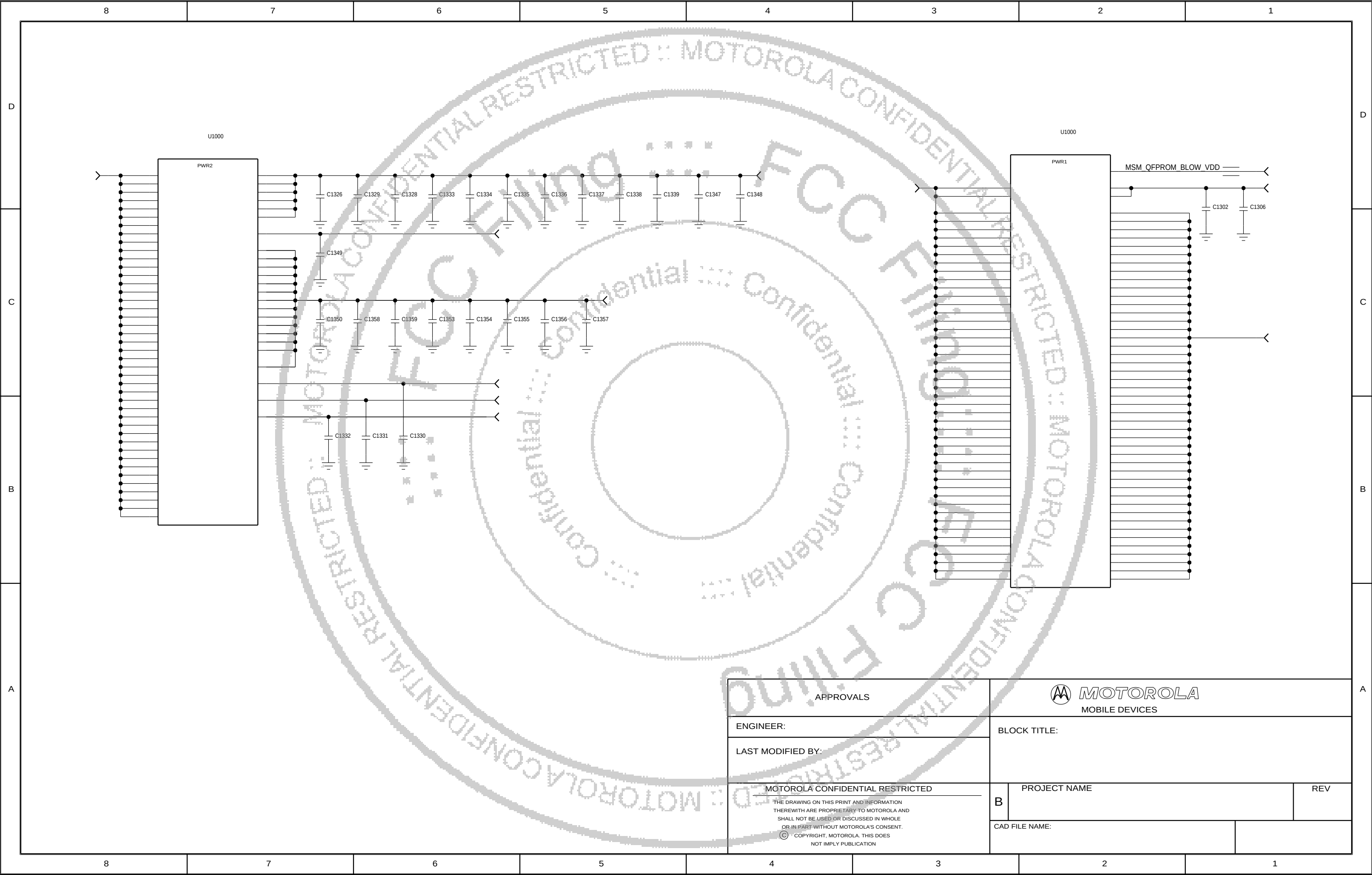
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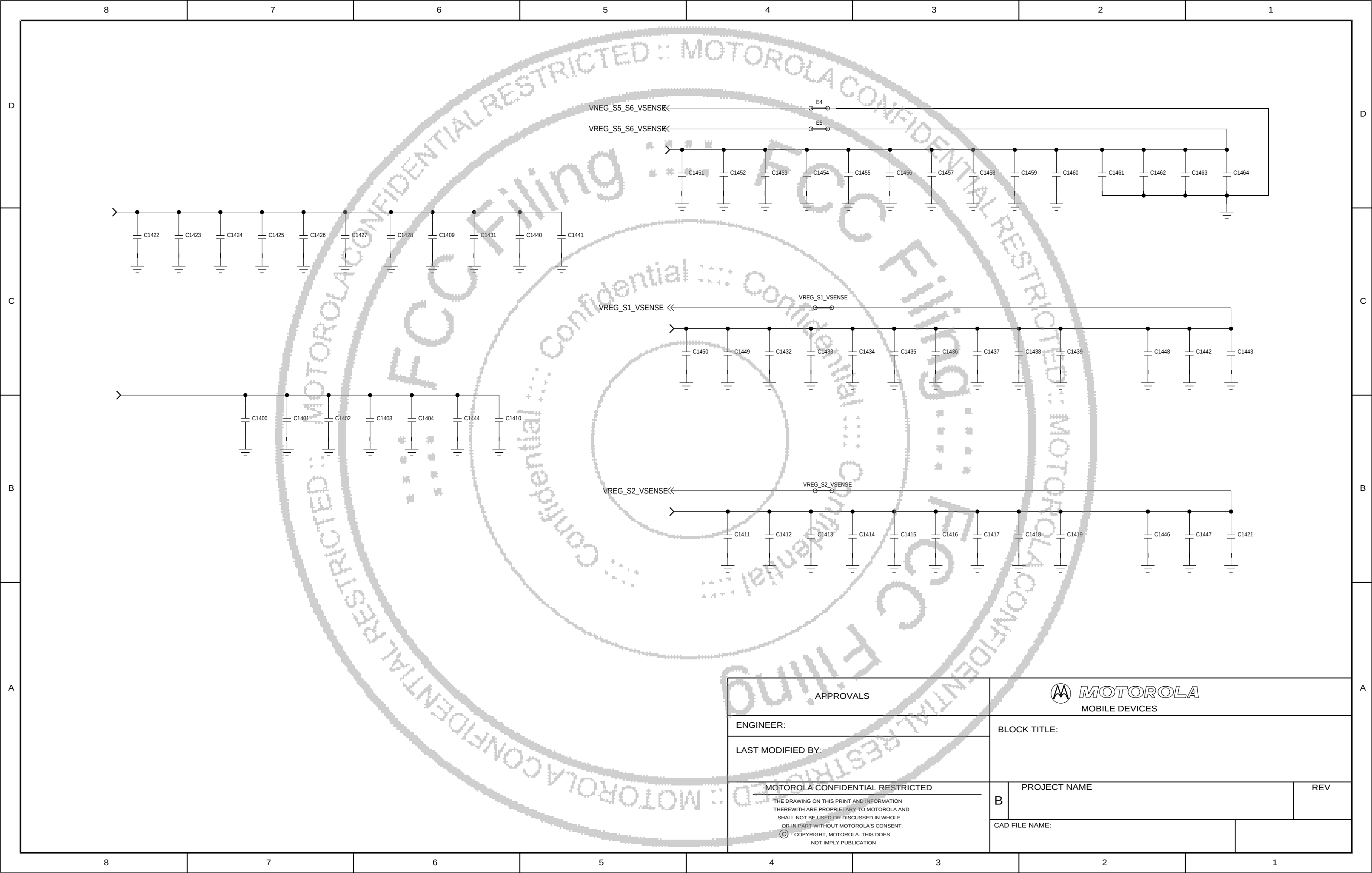


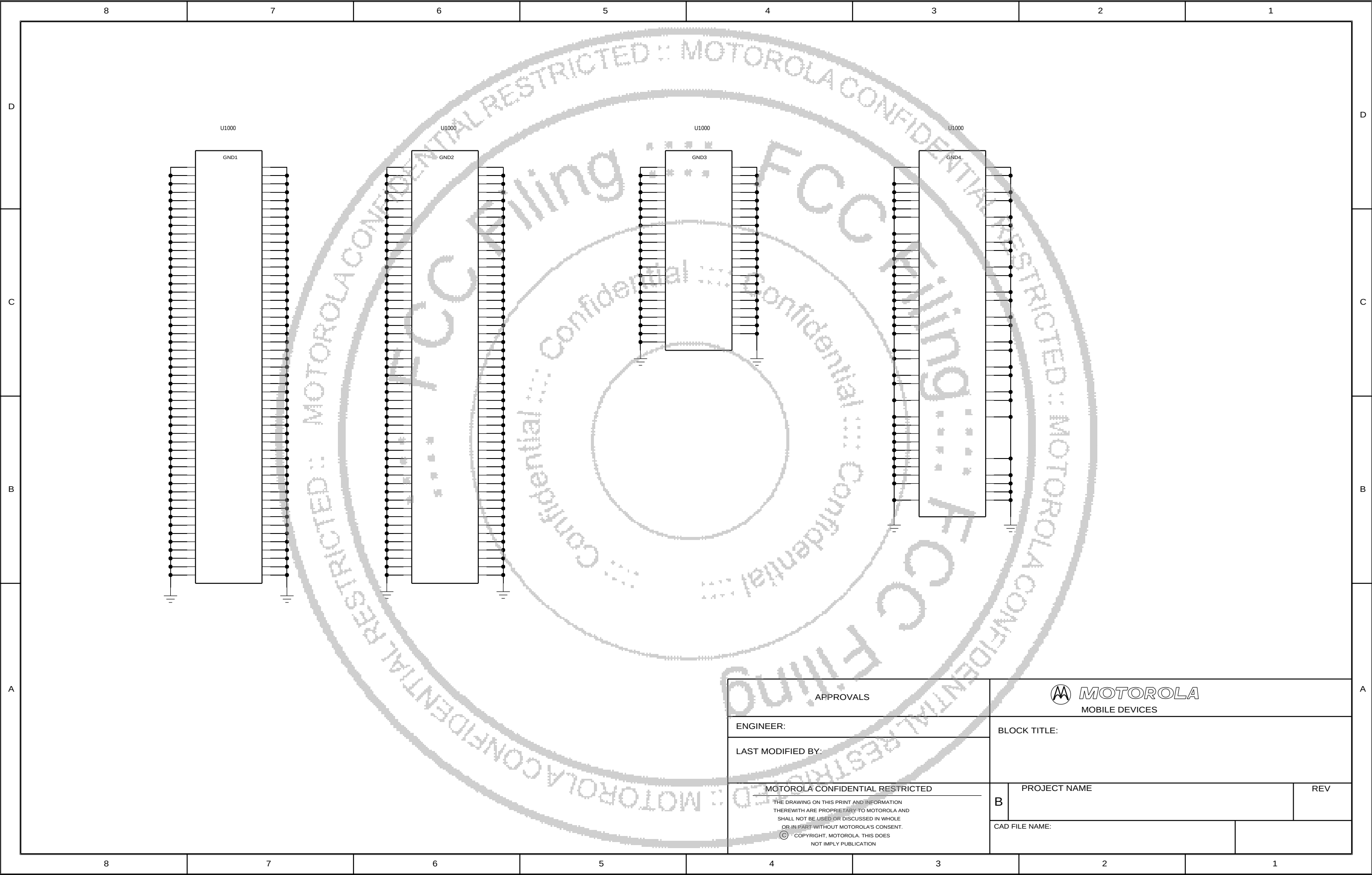


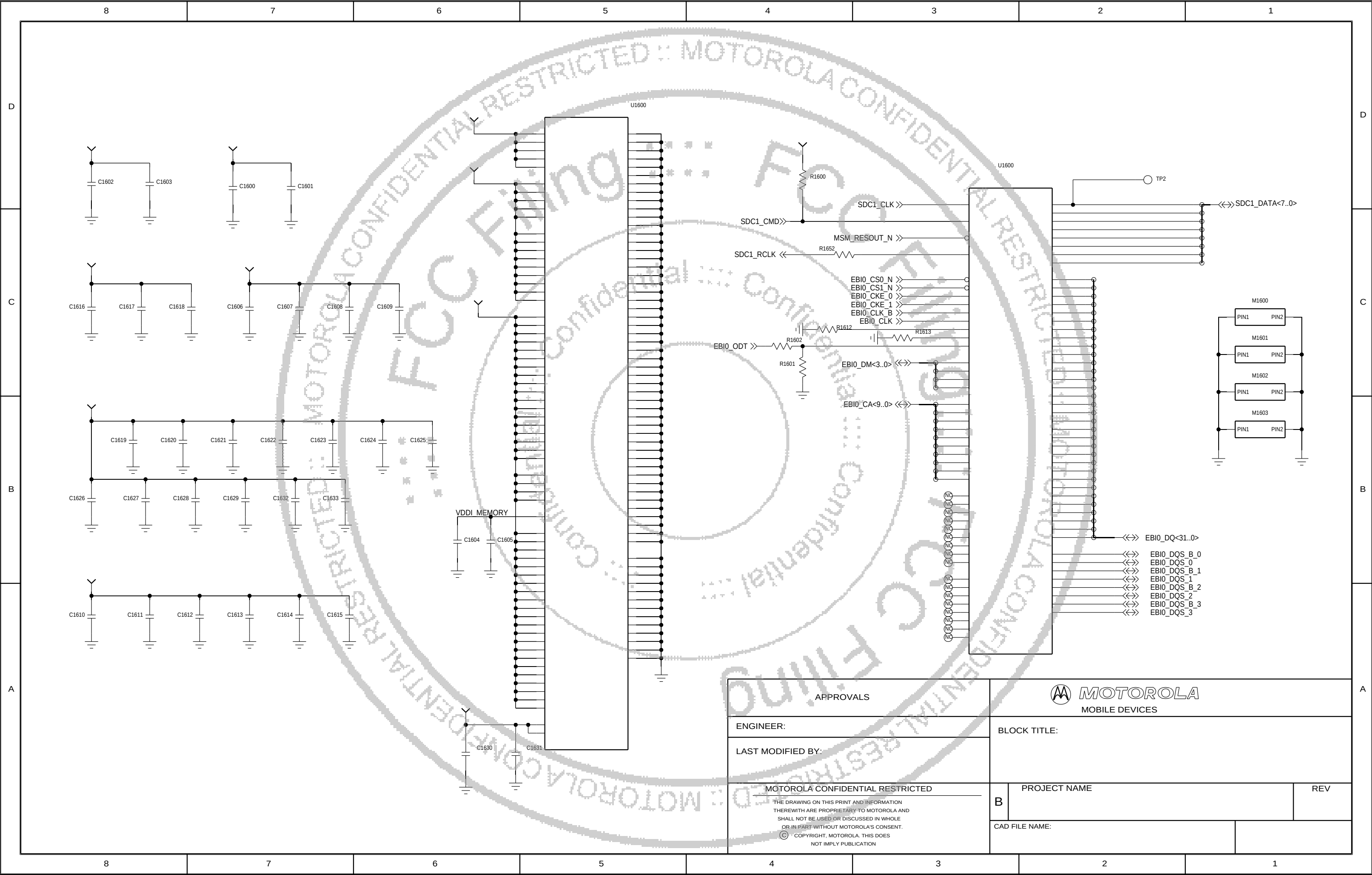


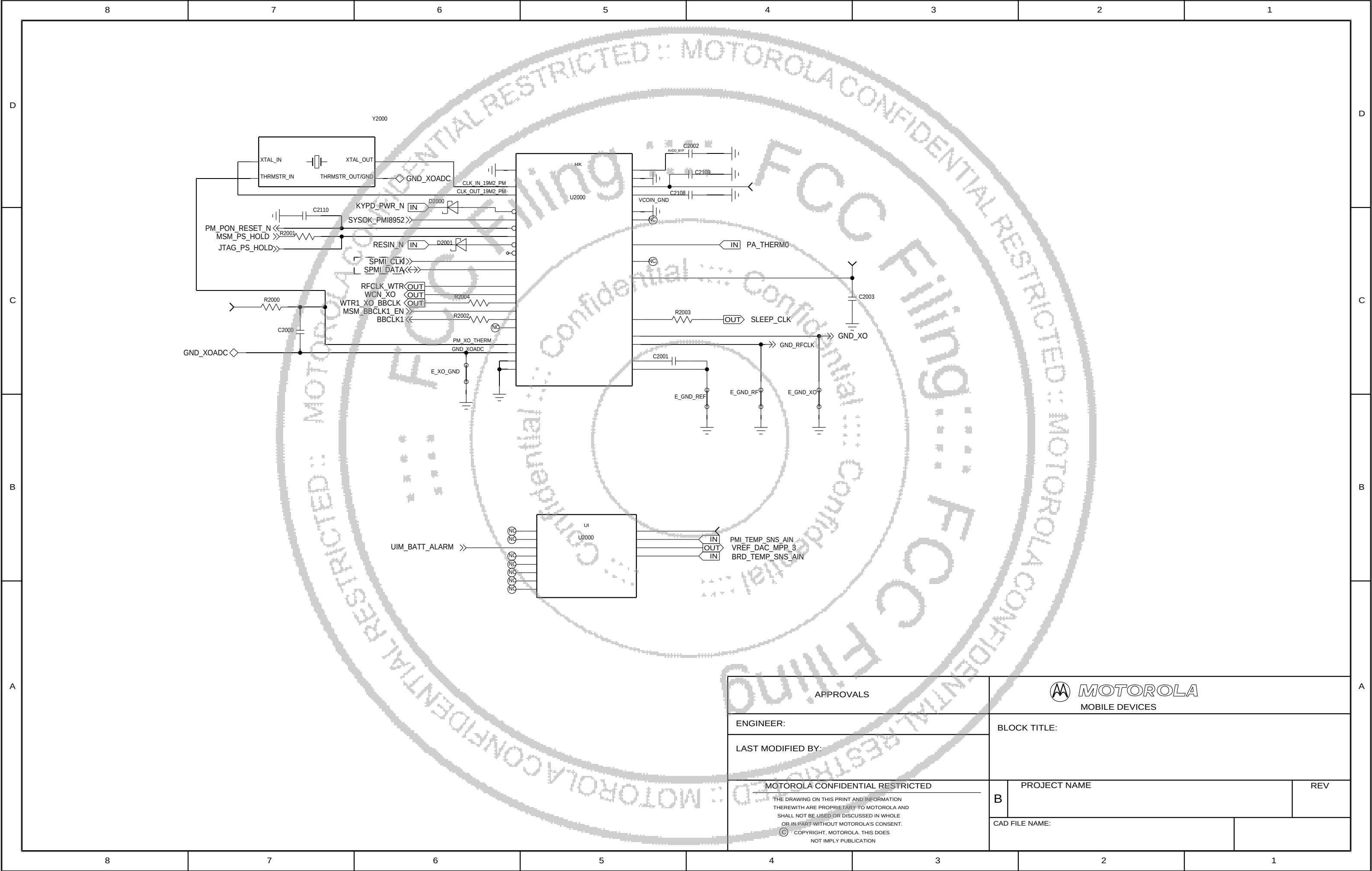


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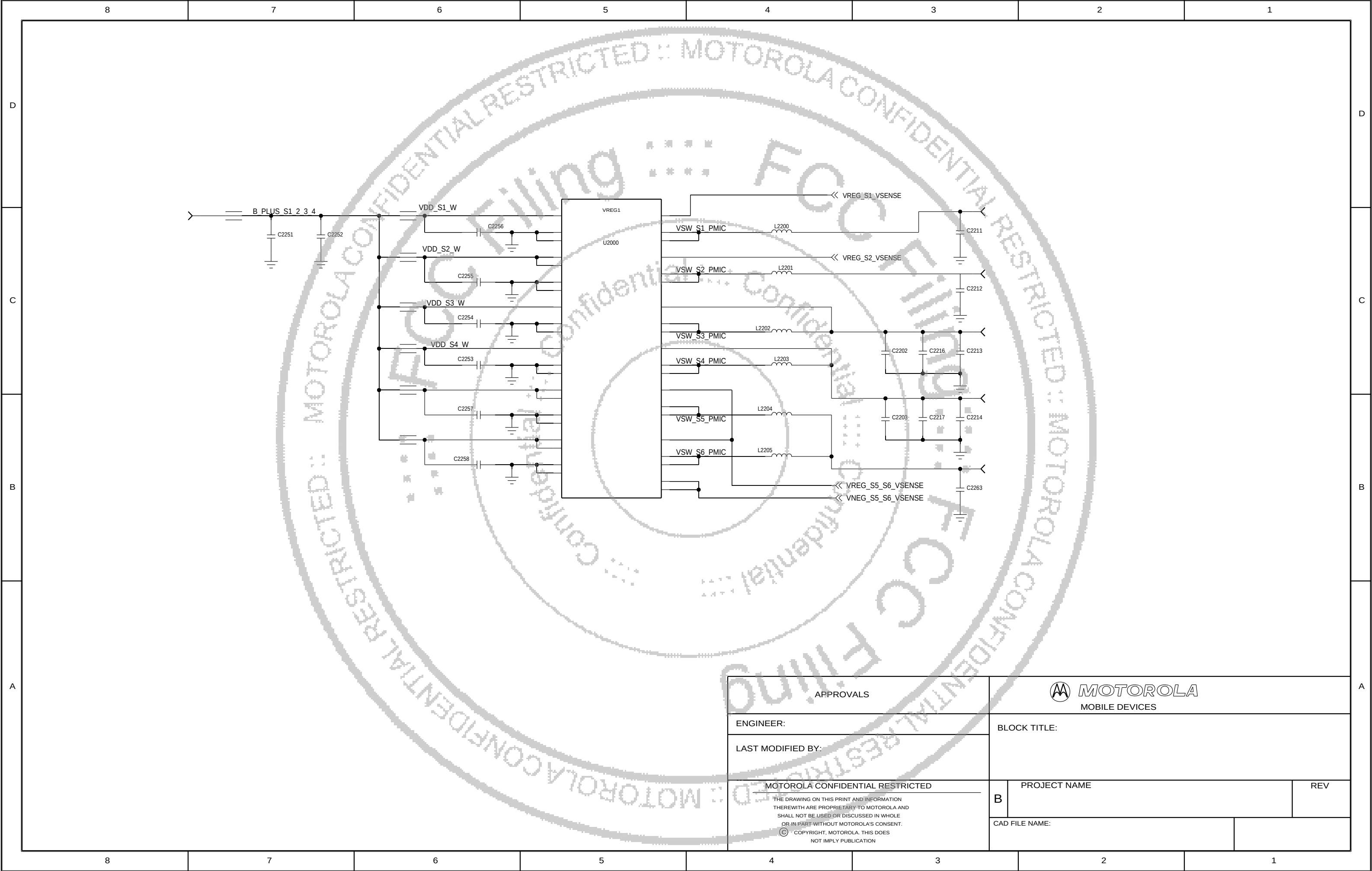


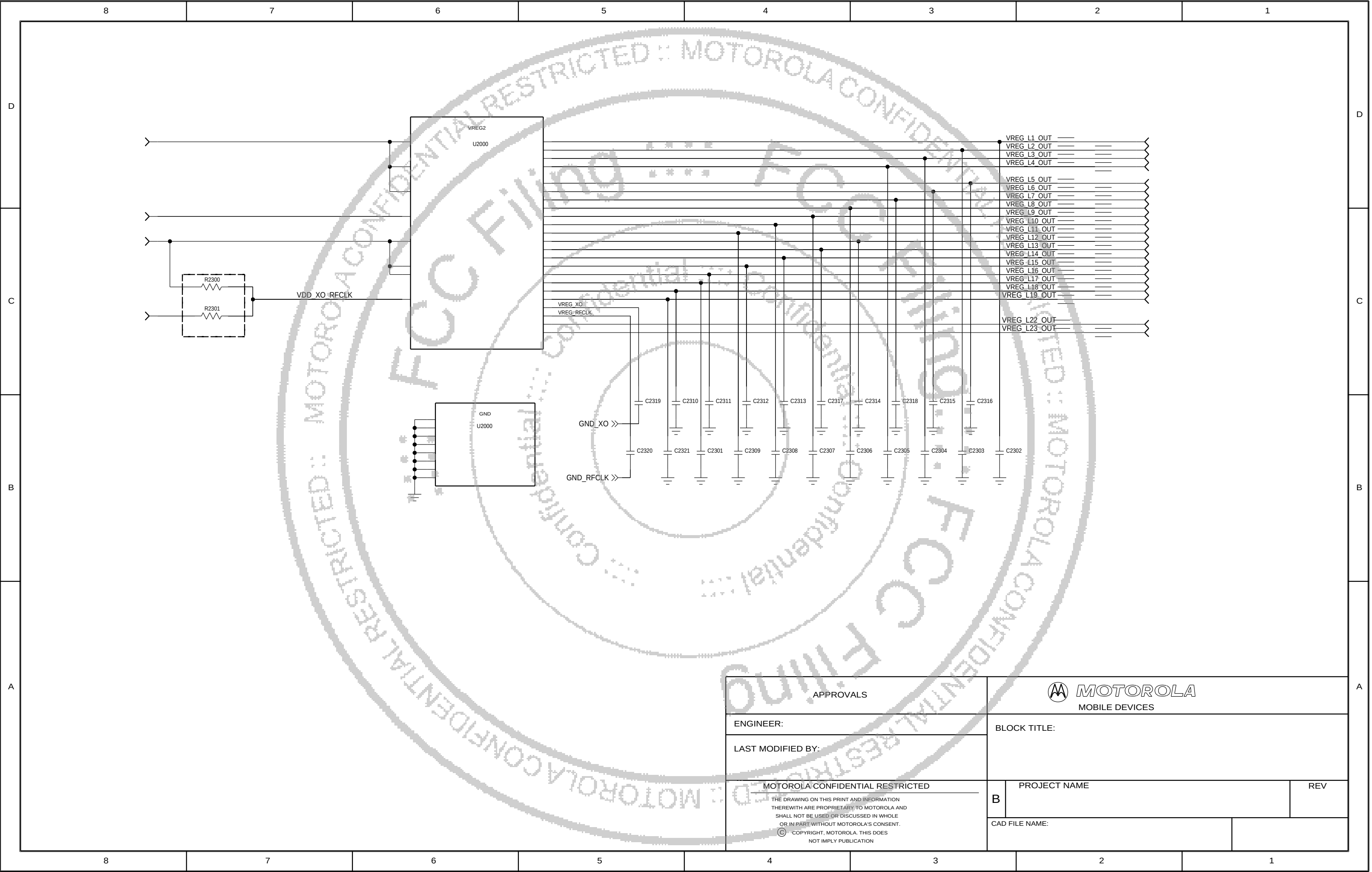


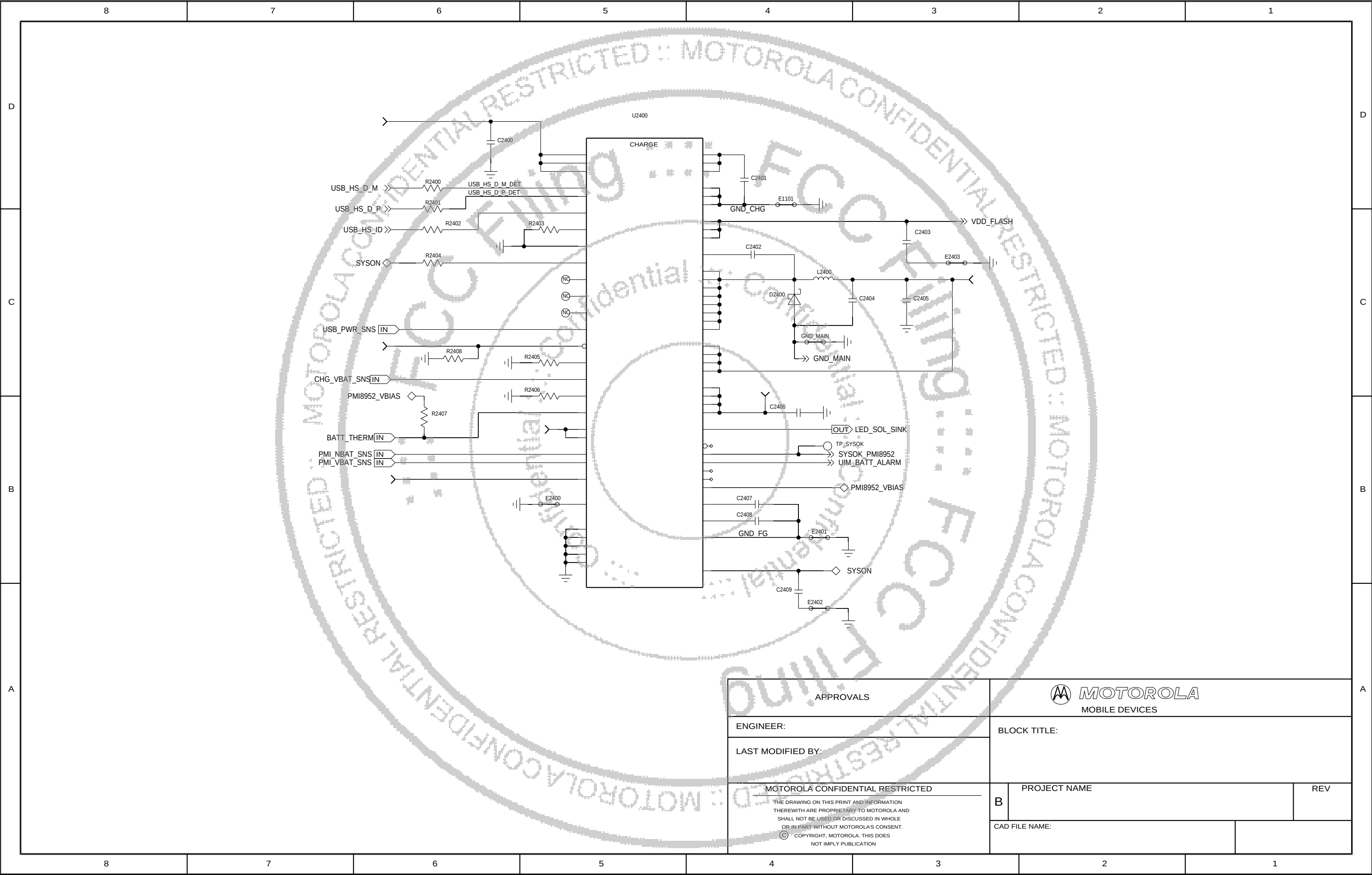




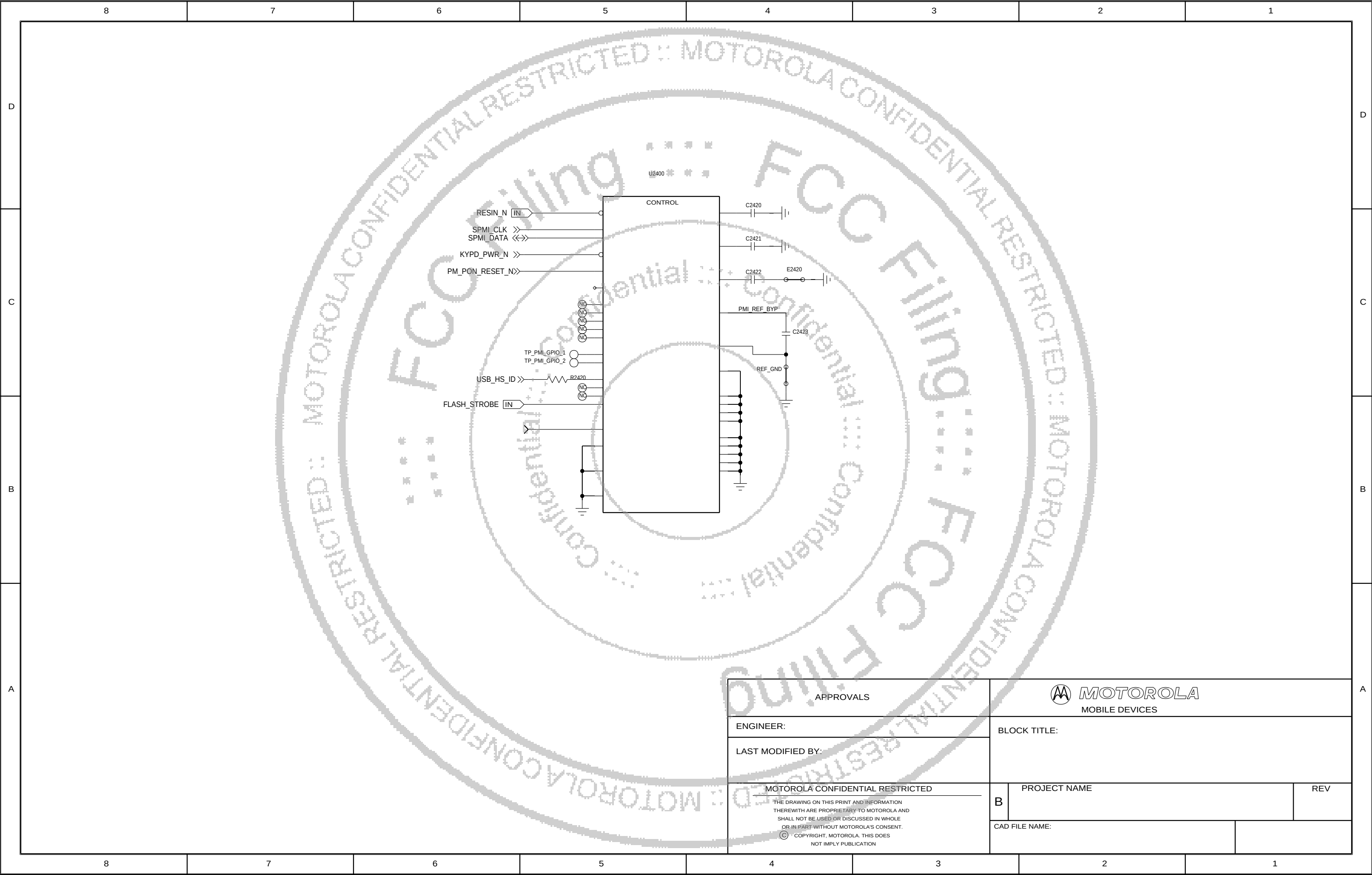
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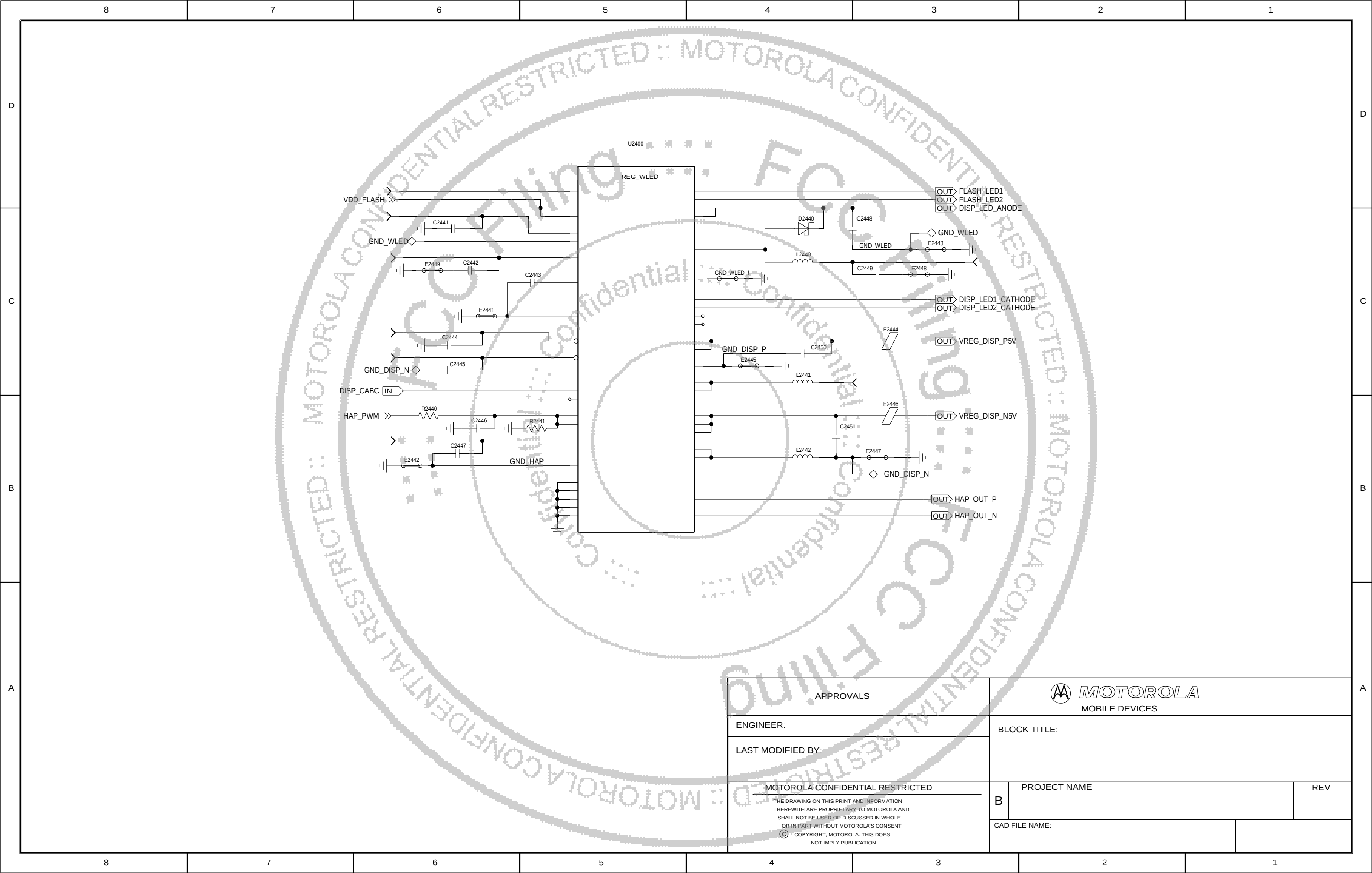




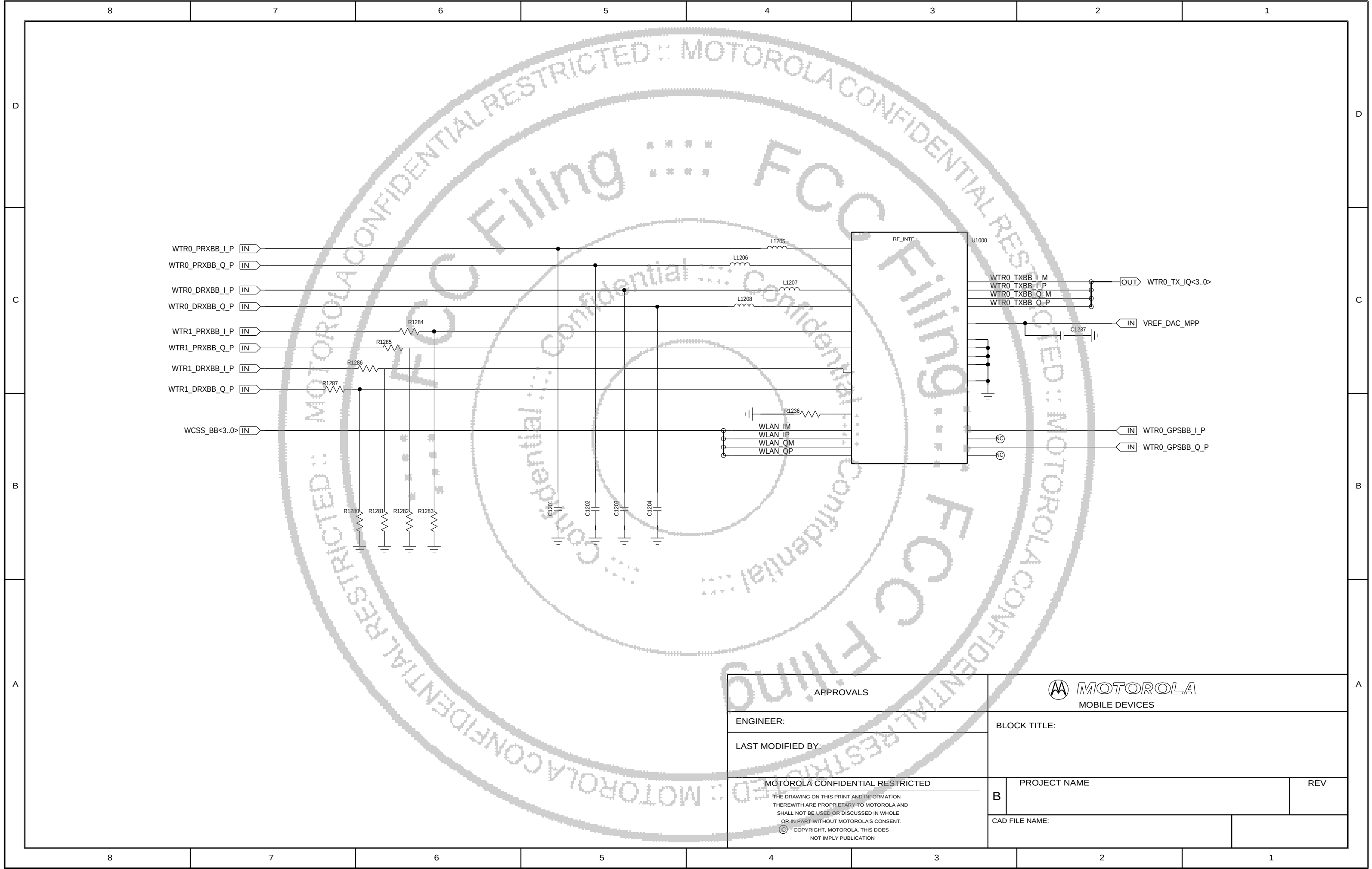


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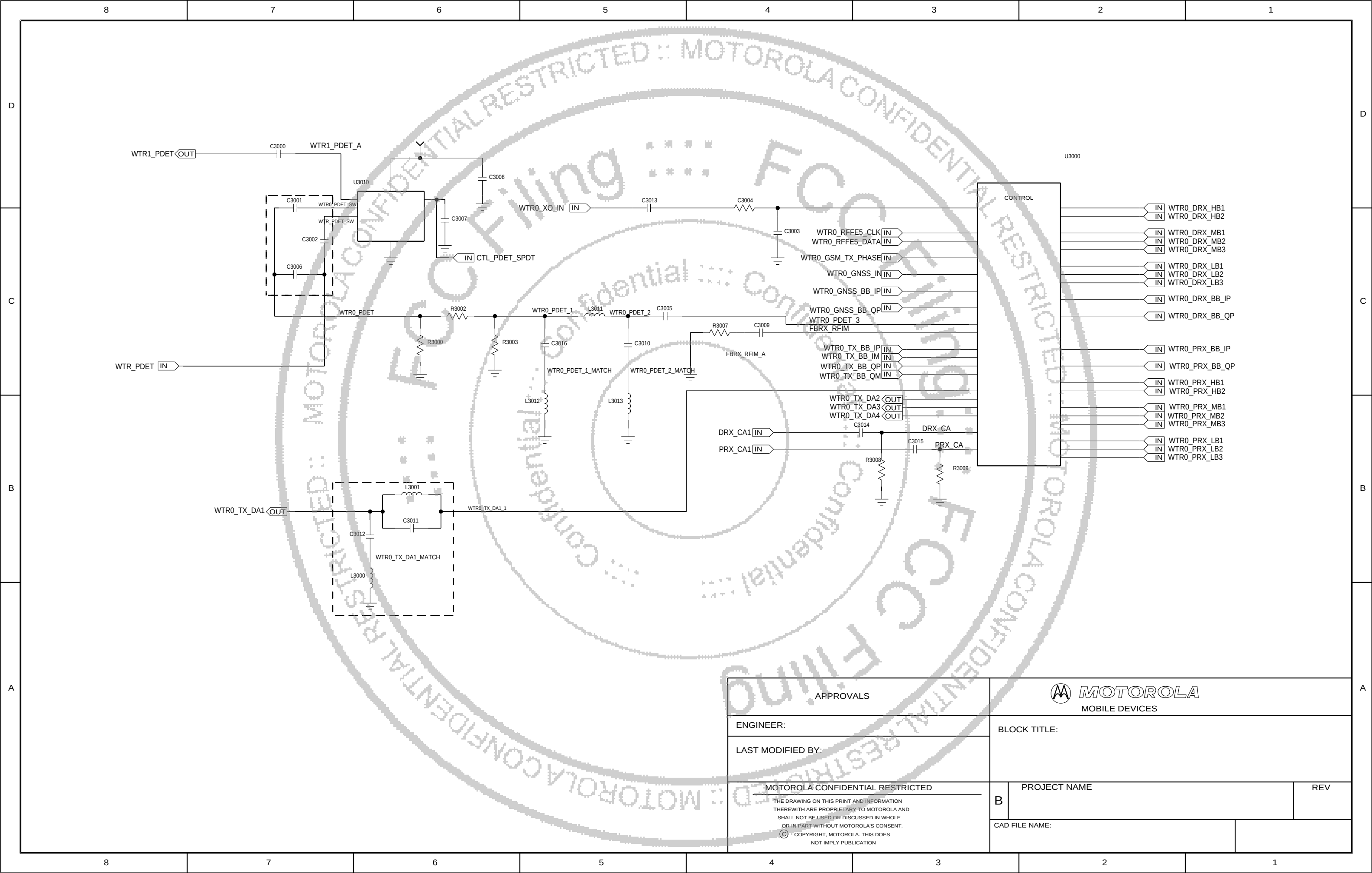




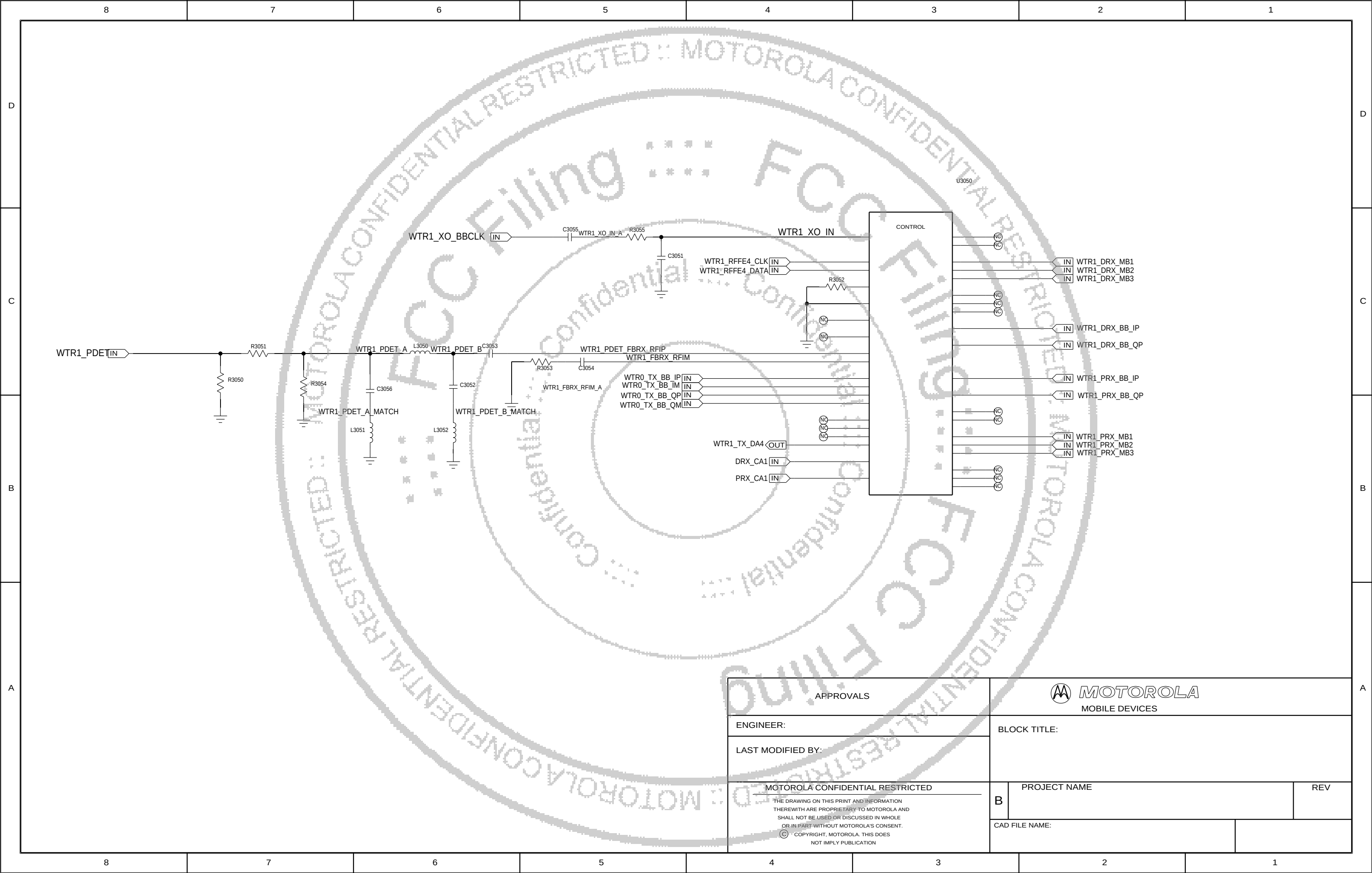
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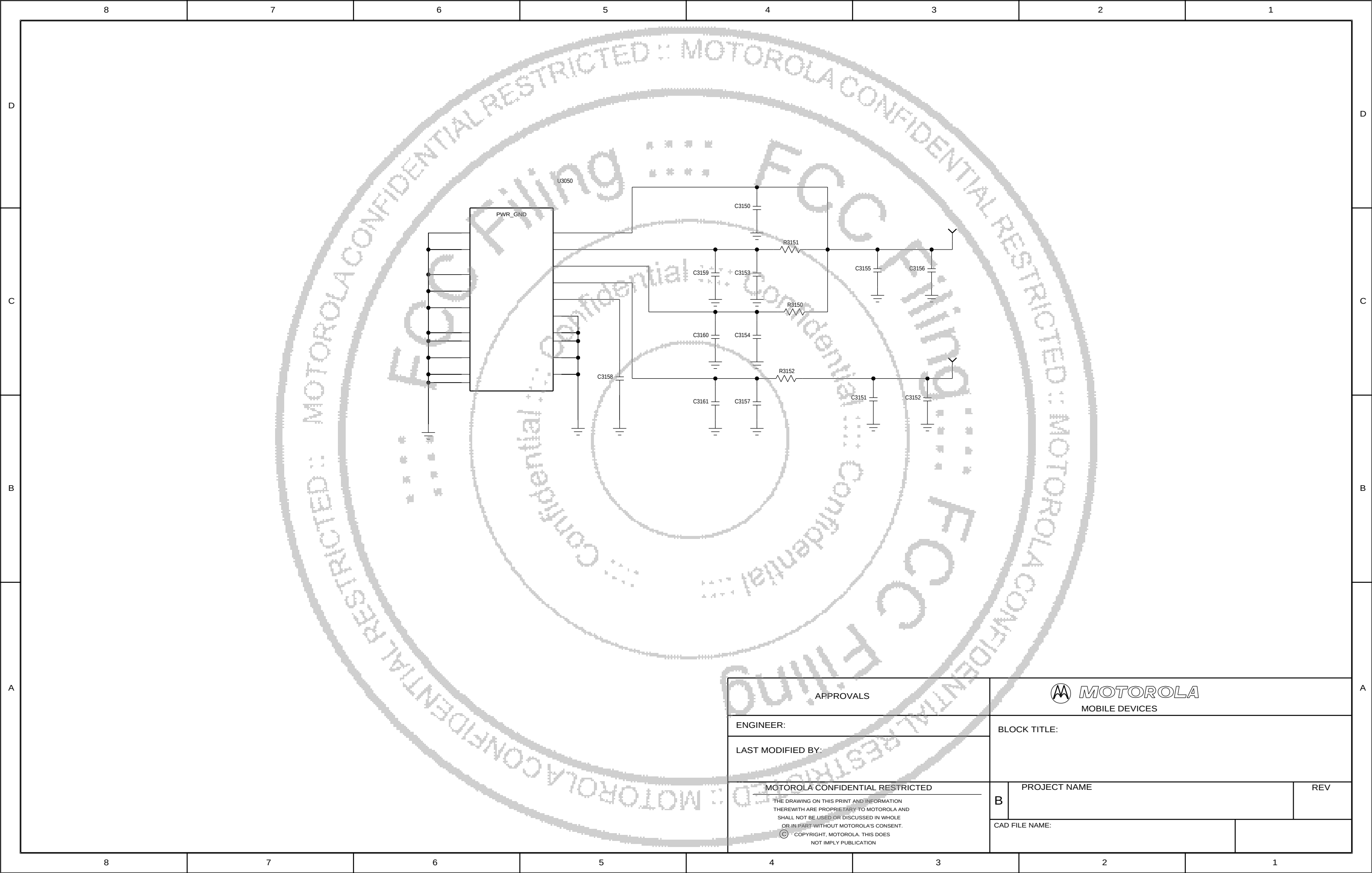


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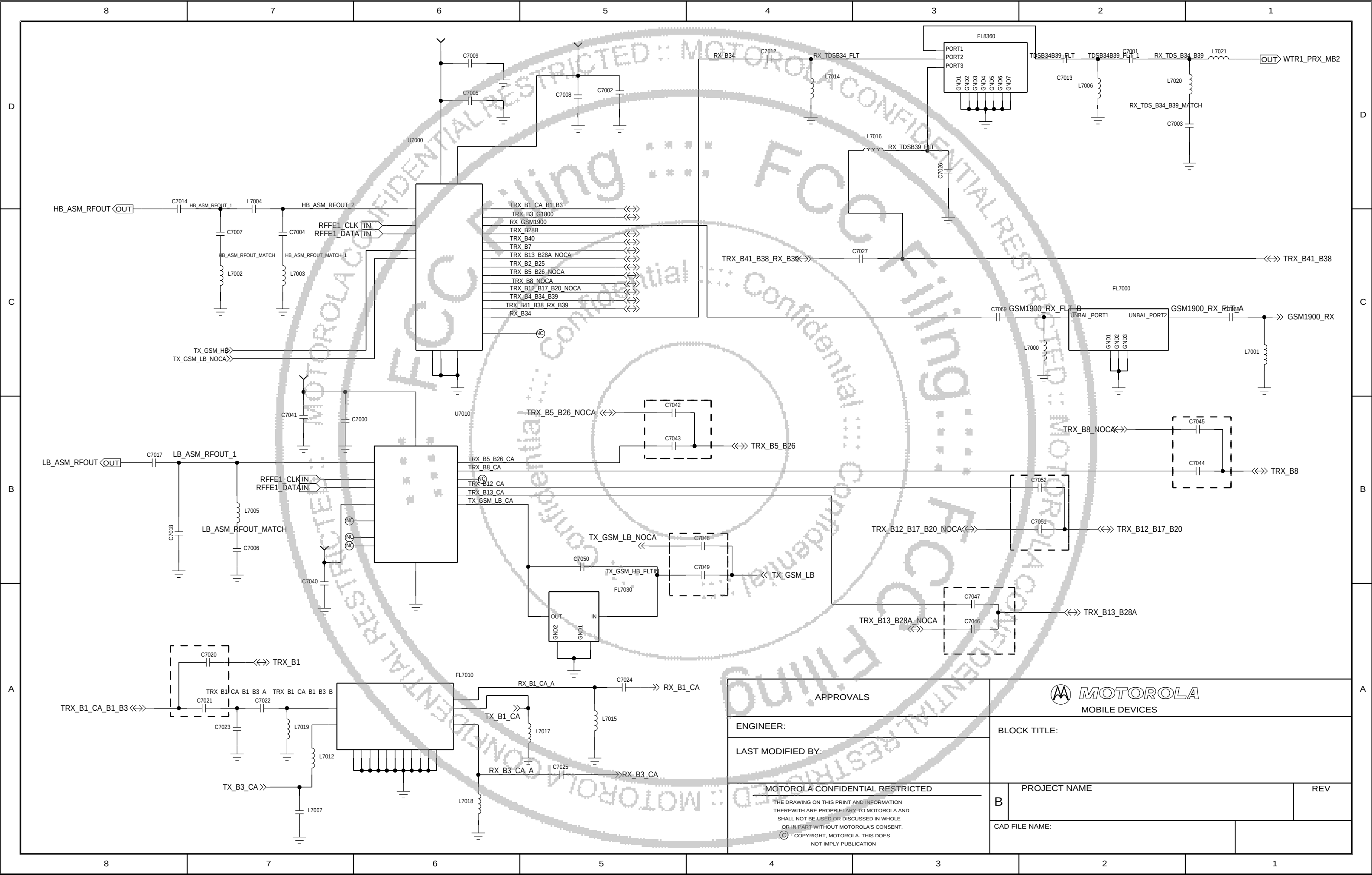


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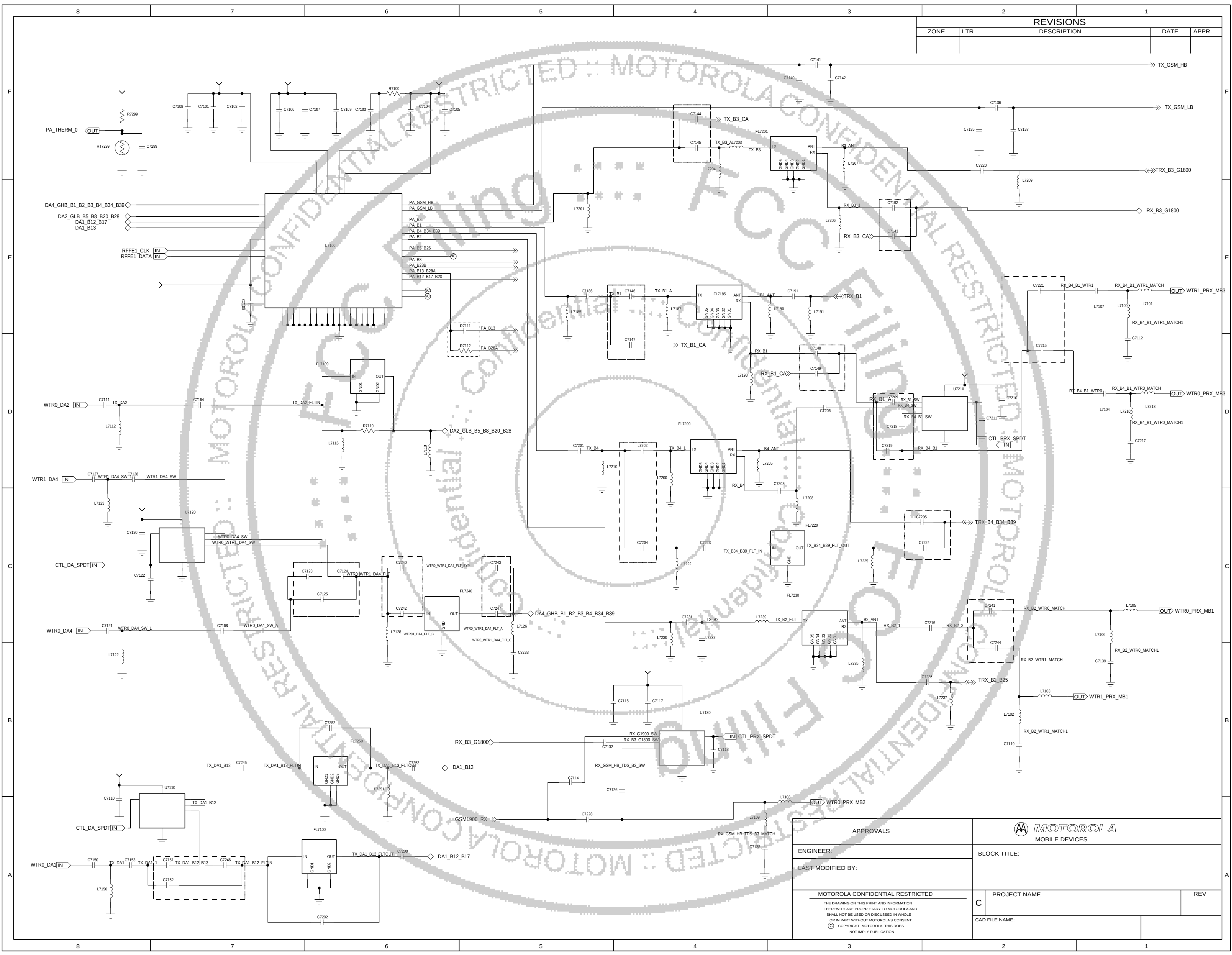




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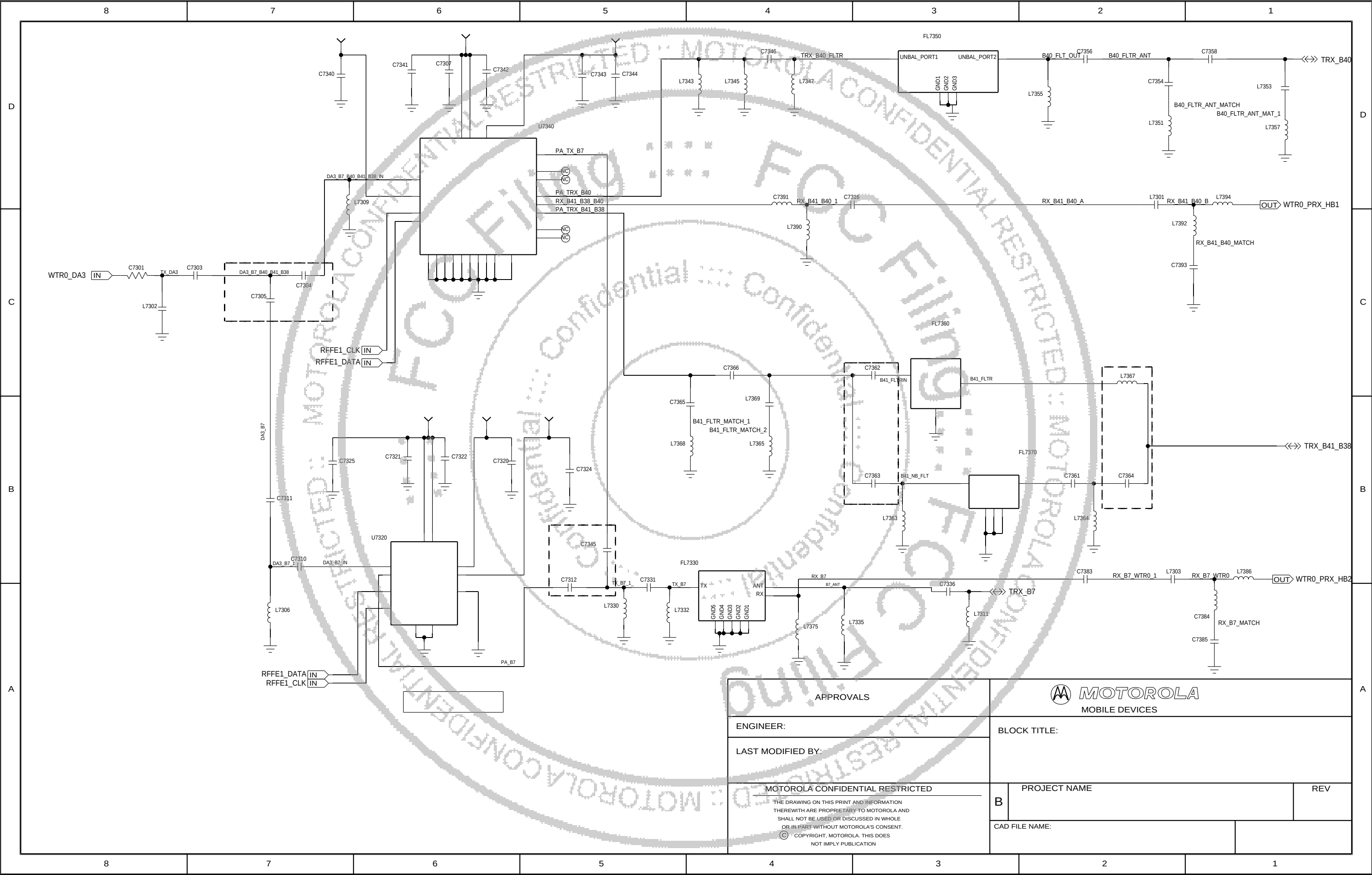


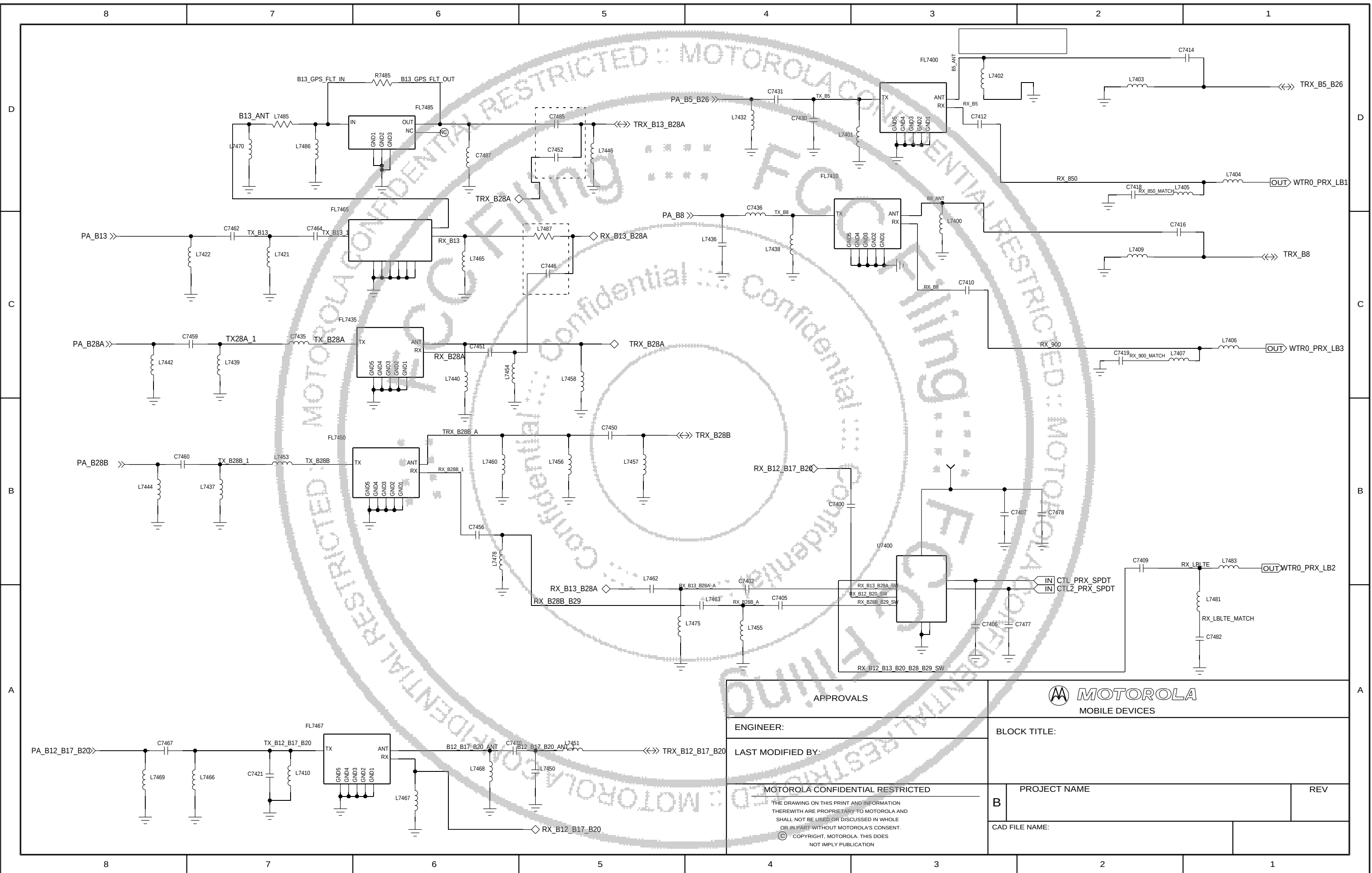
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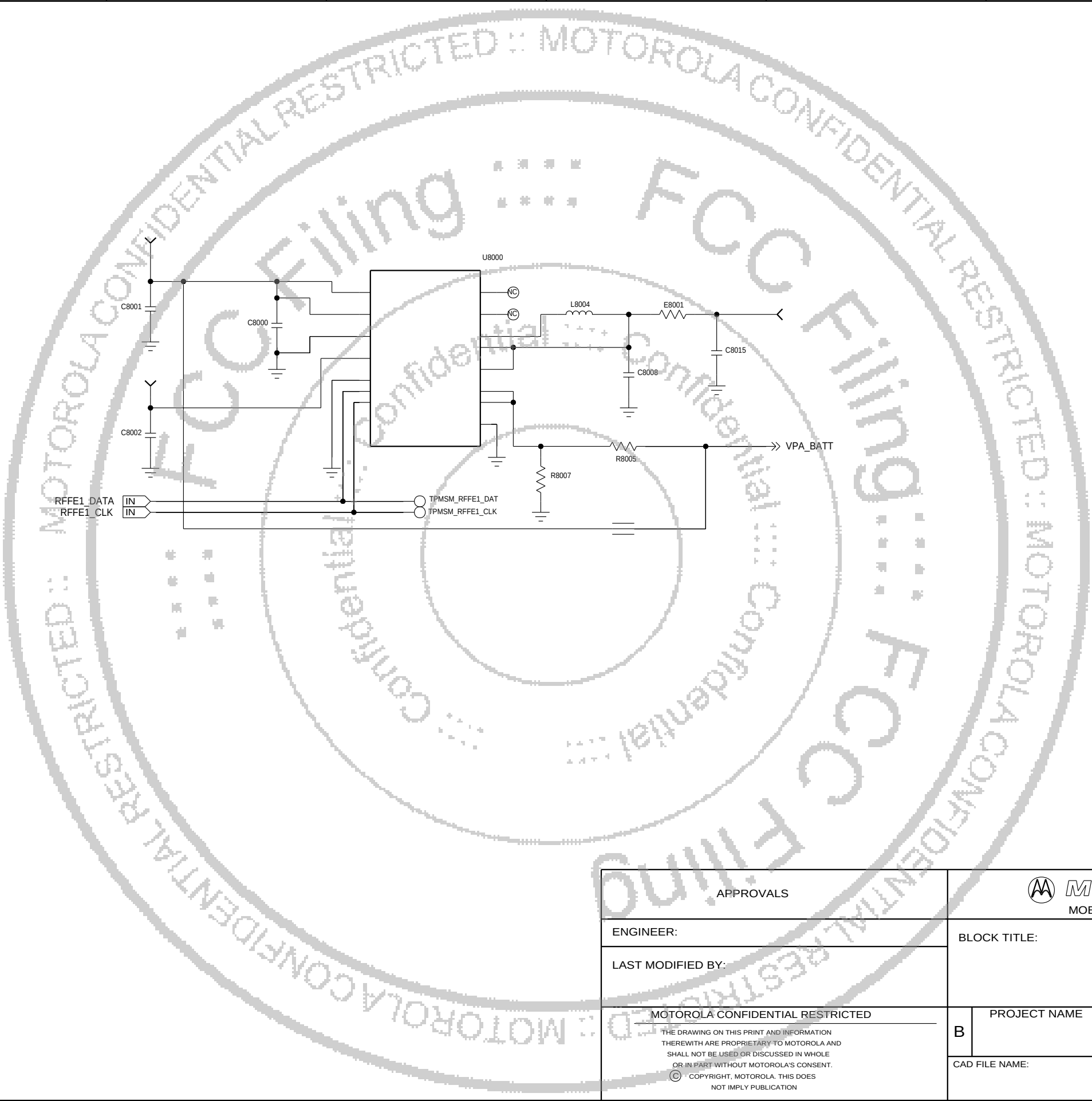


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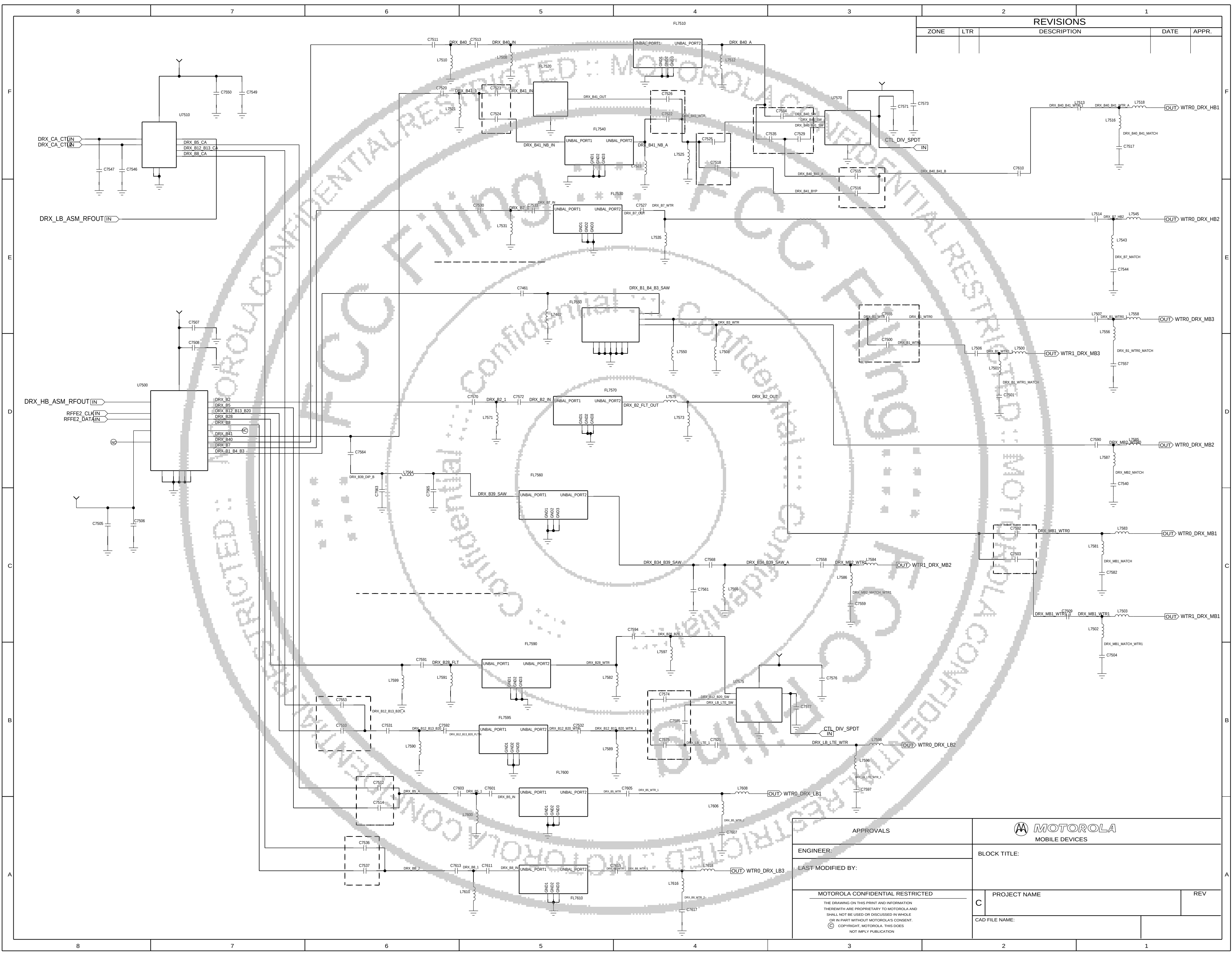
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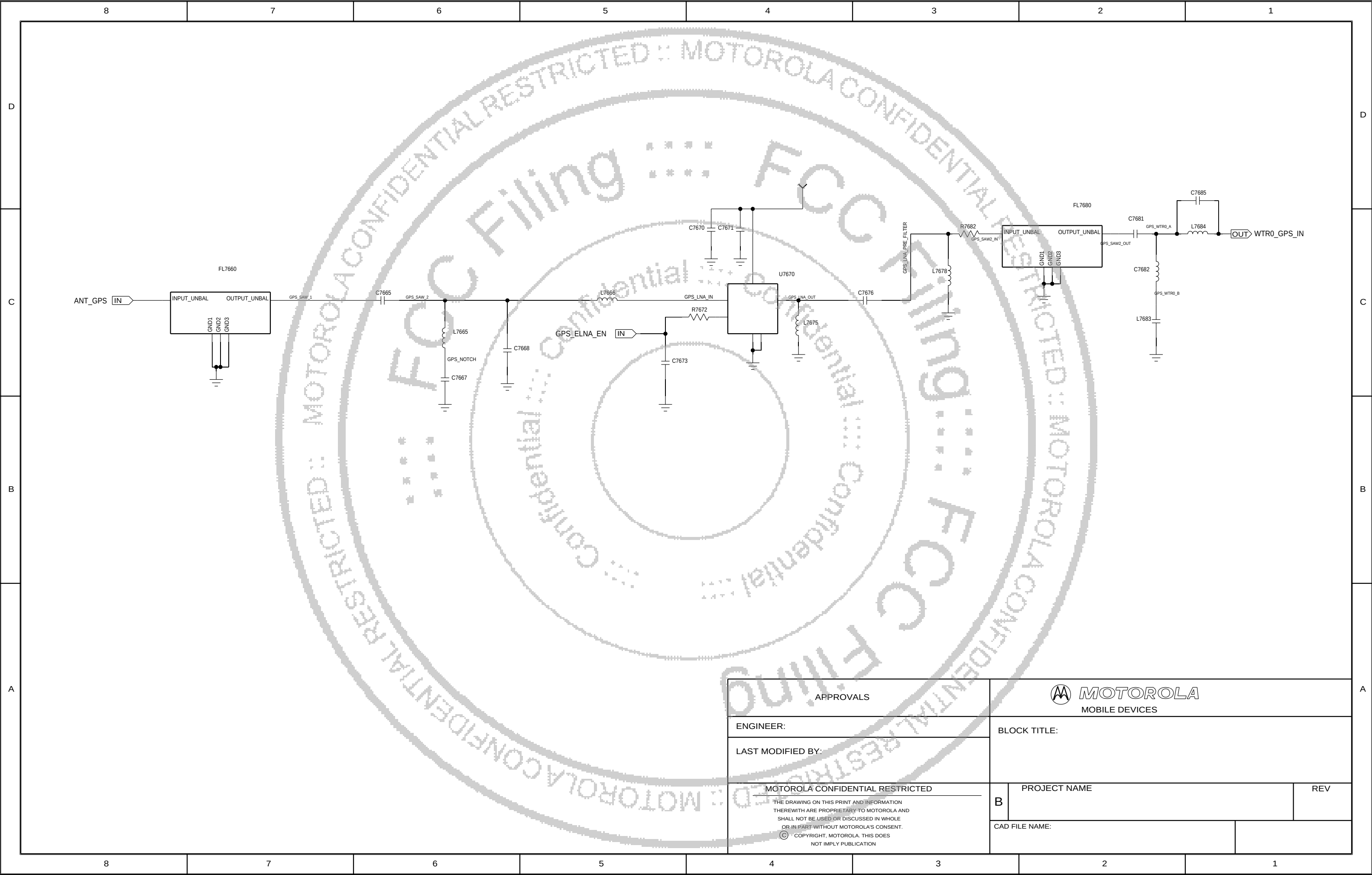


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