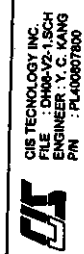
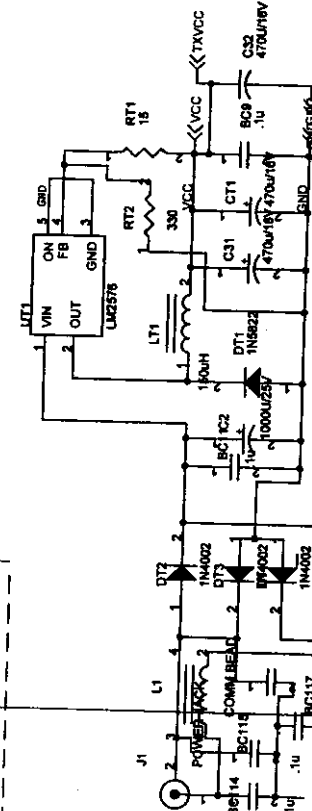
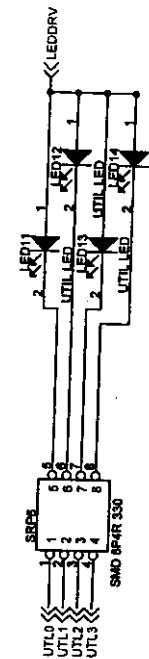
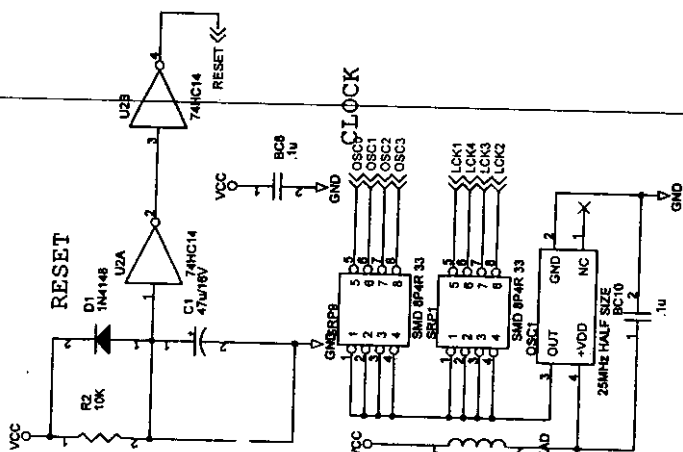
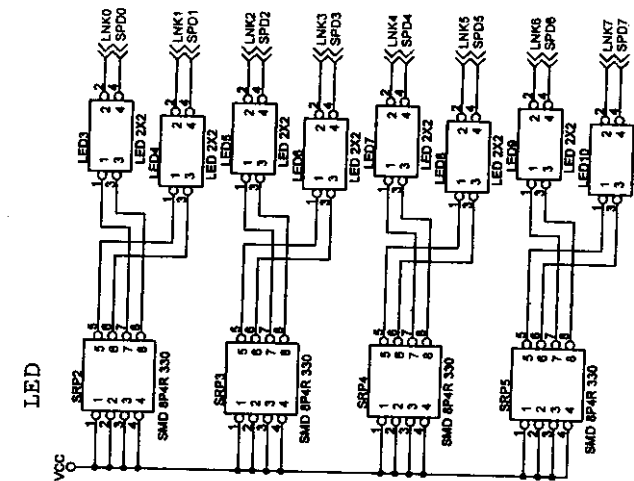
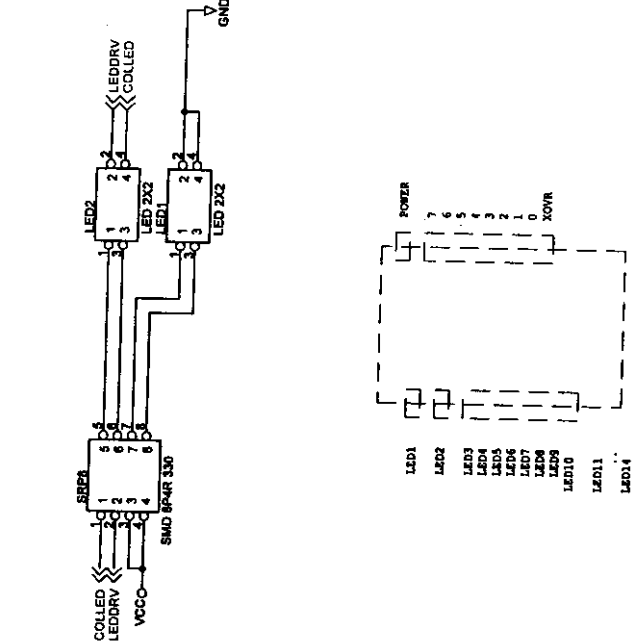


EXHIBIT D

Circuit Diagram





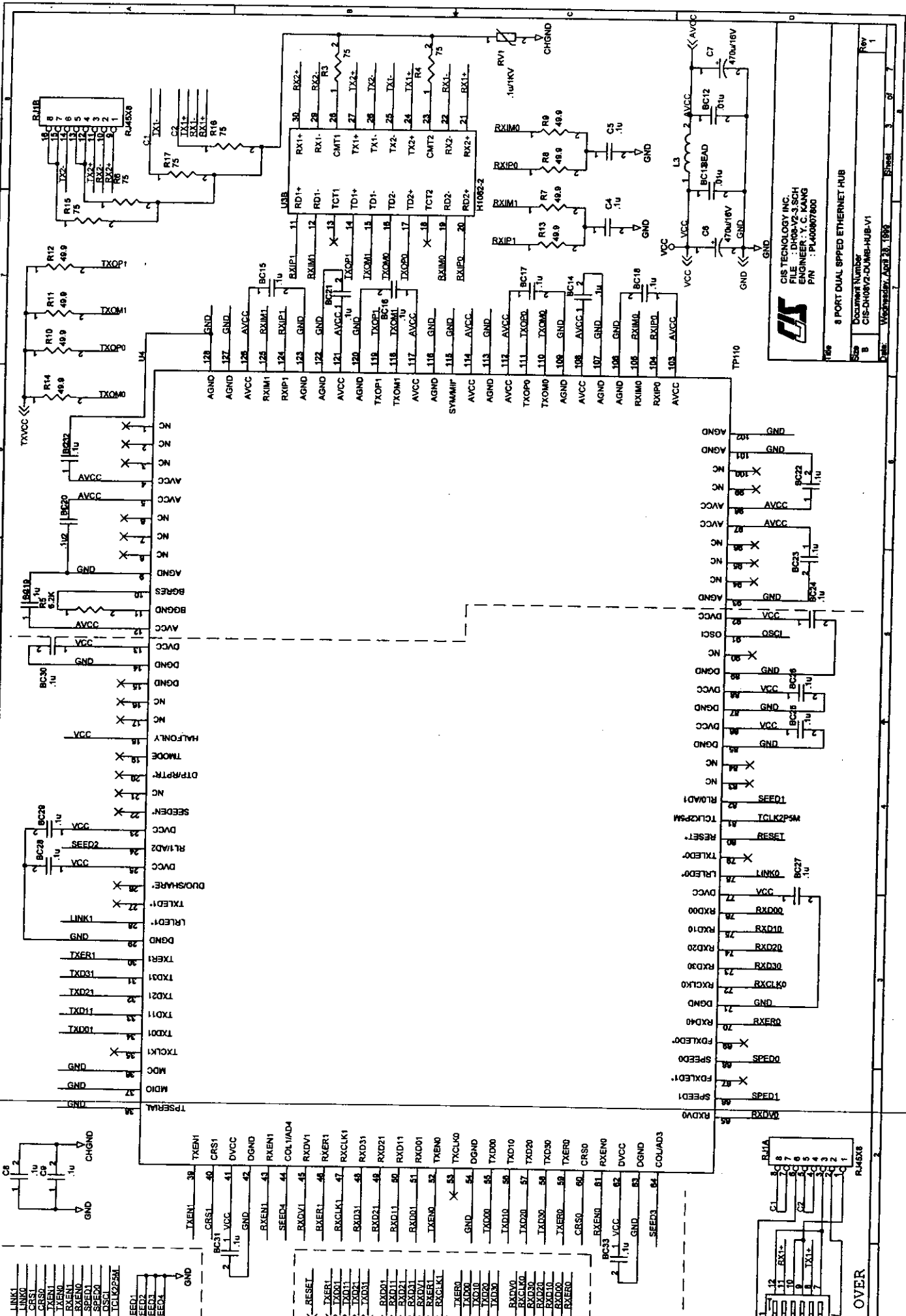
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 ENGINEER : Y. C. KONG
 P/N : PL40007600

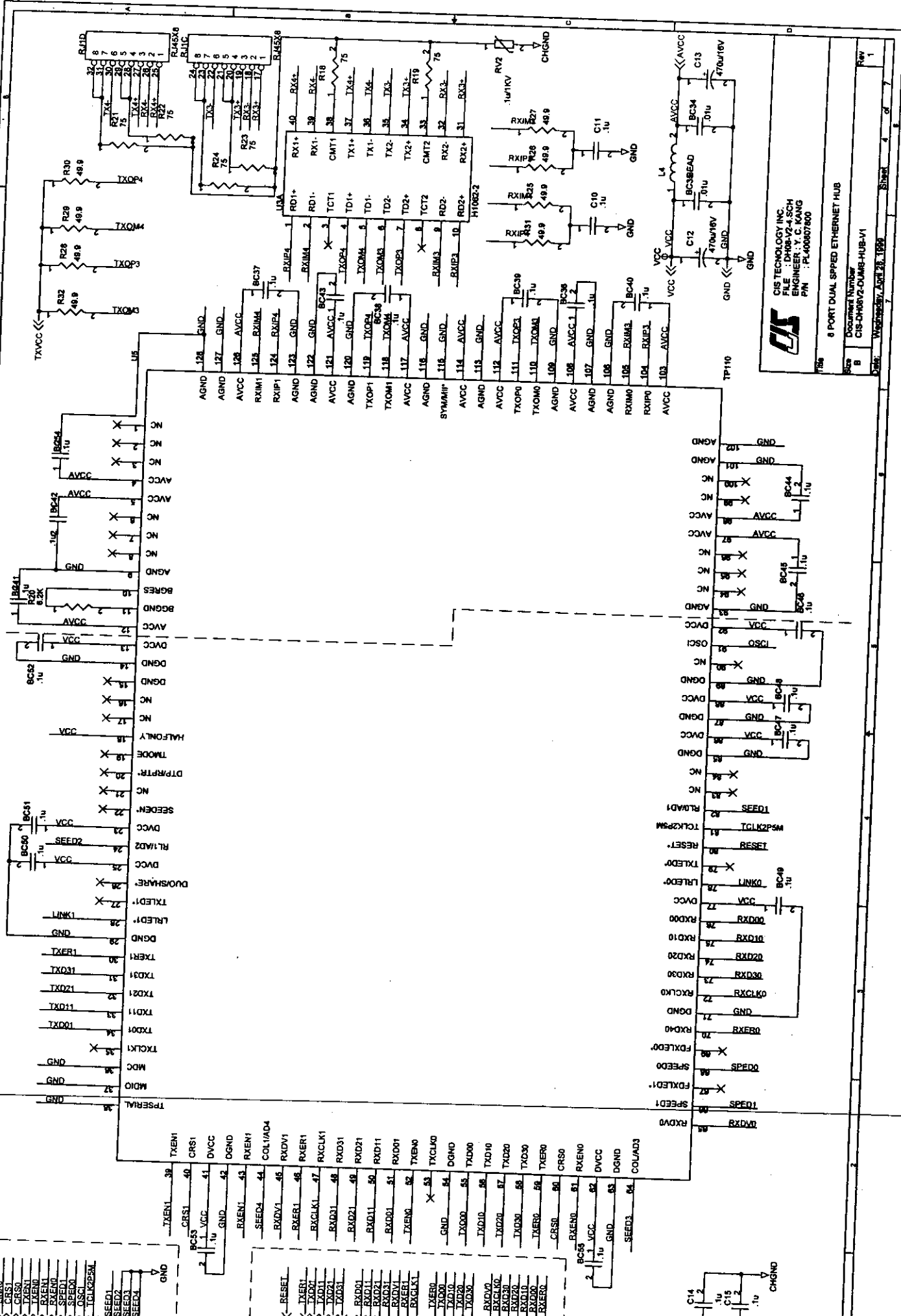
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ENGINEER : Y. C. KANG
P/N : PL400007800

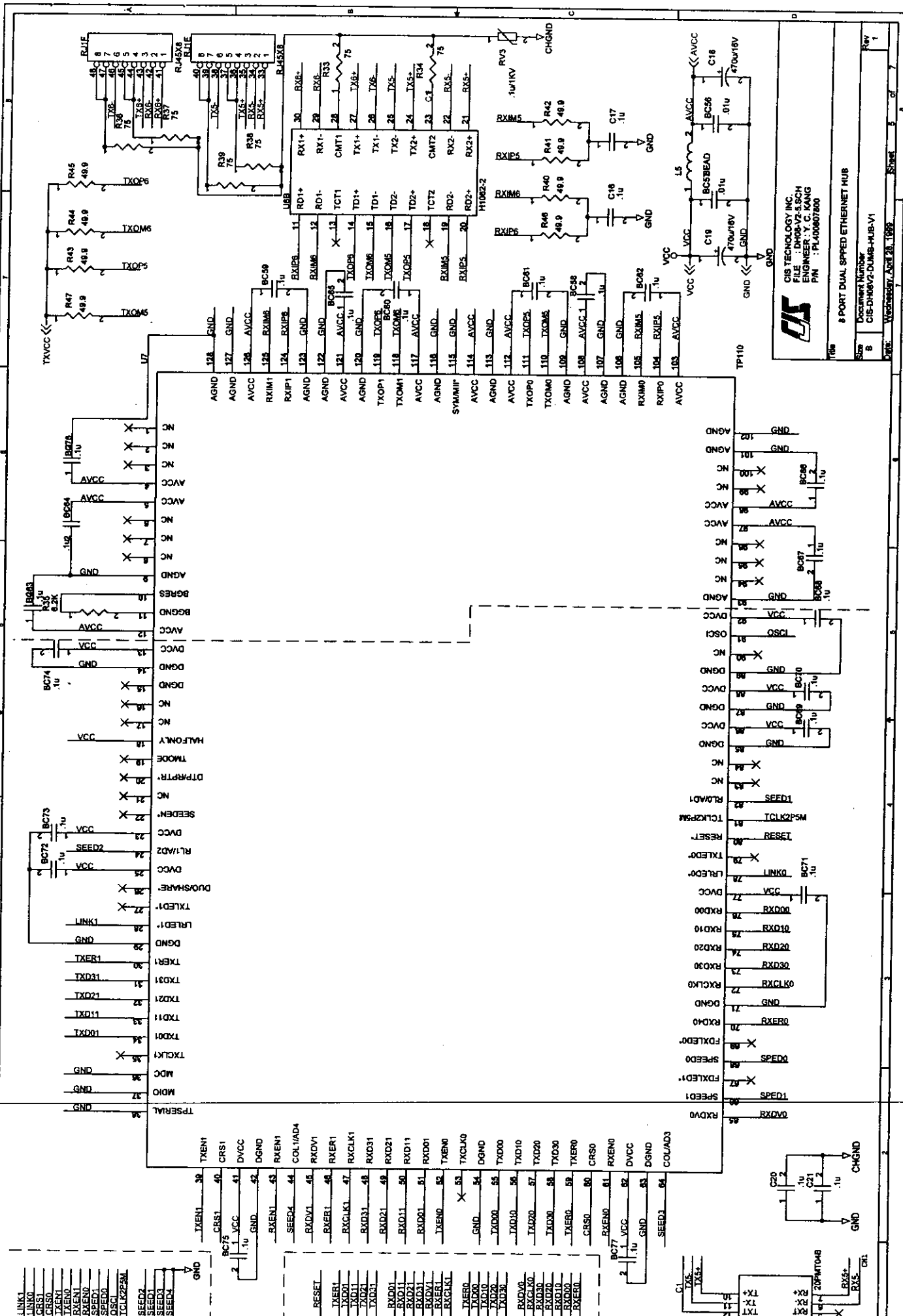
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Rev
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Date: Wednesday, April 26, 1995

Sheet 4 of 7



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 FILE : DHDV2-LSCH
 ENGINEER : Y. C. KANG
 P/N : PL400607800

8 PORT DUAL SPEED ETHERNET HUB

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 CIS-DHDV2-DUMB-HUB-V1
 Date: Wednesday, April 28, 1999

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