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graph LR; X[\"X-AXIS PHOTO SENSOR ENCODER MODULE\"] --> MC[\"MOUSE CONTROLLER\"]; Y[\"Y-AXIS PHOTO SENSOR ENCODER MODULE\"] --> MC; MS[\"MICRO-SWITCH MODULE\"] --> MC; MC <--> PI[\"PS/2 INTERFACE\"]; PI <--> HPC[\"TO HOST PC\"]; style X fill:#fff,stroke:#000; style Y fill:#fff,stroke:#000; style MS fill:#fff,stroke:#000; style MC fill:#fff,stroke:#000; style PI fill:#fff,stroke:#000; style HPC fill:#fff,stroke:#000;
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The diagram illustrates the system architecture for a mouse. It consists of the following components and connections:

- X-AXIS PHOTO SENSOR ENCODER MODULE**: Connected to the Mouse Controller.
- Y-AXIS PHOTO SENSOR ENCODER MODULE**: Connected to the Mouse Controller.
- MICRO-SWITCH MODULE**: Connected to the Mouse Controller.
- MOUSE CONTROLLER**: The central processing unit that receives input from the sensors and switch.
- PS/2 INTERFACE**: Connected to the Mouse Controller and the Host PC.
- TO HOST PC**: The destination for the processed data.

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PROJECT UM510P PS/2 MOUSE V.A		TITLE BLOCK DIAGRAM	
APPROVE		DWG NO	
CHECK		SHEET	1/1
DESIGN		SIZE	A4
DATE	12/14/98	FILE NAME	5701100
		REV	D/A
		DESCRIPTION	