

FACSIMILE COVER PAGE**To :** MR. SCOTT**From :** ANTONY**Sent :** 3/24/99 at 3:40:16 PM**Pages :** 4 (including Cover)**Subject :**

Scott,

I only have the information attached and below.

Bit 0 = 1

Bit 1 = 2

Bit 2 = 3

Bit 3 = 4

Bit 0 is LSB.

The 1,2,3,4 are the ID selection at the back of the receiver & transmitter. } *THE ENGINEER USED 4*
Moreover, the engineer used 4 bits only of 8 bits for the ID selection. } *OF THE 8 BITS FOR THE TX.*

If you have any problems, please feel free to contact me. Thanks!!!

Regards,

Antony Chung

P.S. : We still waiting for your quotation of 2.4 GHz A/V Tx system.

```

graph TD
    Start([Power - on]) --> Standby[Stand-by mode]
    Standby --> DataIn{Data in ?}
    DataIn -- No --> Standby
    DataIn -- Yes --> AddressMatch{8 bits Address Matched ?}
    AddressMatch -- No --> DisableVT[Disable VT & ignore the rest of this word]
    AddressMatch -- Yes --> StoreData[Store 4 bits Data]
    StoreData --> MatchPrev{Match Previous stored Data ?}
    MatchPrev -- No --> DisableVT
    MatchPrev -- Yes --> TripleCheck{Triple Check ? Completed}
    TripleCheck -- No --> StoreData
    TripleCheck -- Yes --> LatchData[Latch 4 bits Data to Output & Activate VT]
    LatchData --> Standby
    DisableVT --> Standby

```

The timing diagram illustrates the operation of the HT-12E encoder. It includes the following signals:

- Oscillator Clock:** A periodic square wave signal.
- Data Out (HT-12E):** A signal that outputs data words. The diagram shows four words: "1 0 1", "0", "1 0", and "0". The first word is labeled "1st Word" and the fourth word is labeled "4th Word".
- Valid:** A signal that indicates when the data output is valid. It is shown as a high-level pulse.
- Transmission:** A signal that indicates when the data is being transmitted. It is shown as a high-level pulse.
- Data Output (HT-12D):** A signal that outputs the decoded data. It is shown as a high-level pulse.

DUTY CYCLE PLOT OF FUNDAMENTAL = 57%

MKR 100.0 msec

hp REF 100.0 dB μ V ATTEN 10 dB

86.50 dB μ V

10 dB/

12.14

11.43

11.43

11.43

10.00

56.43%

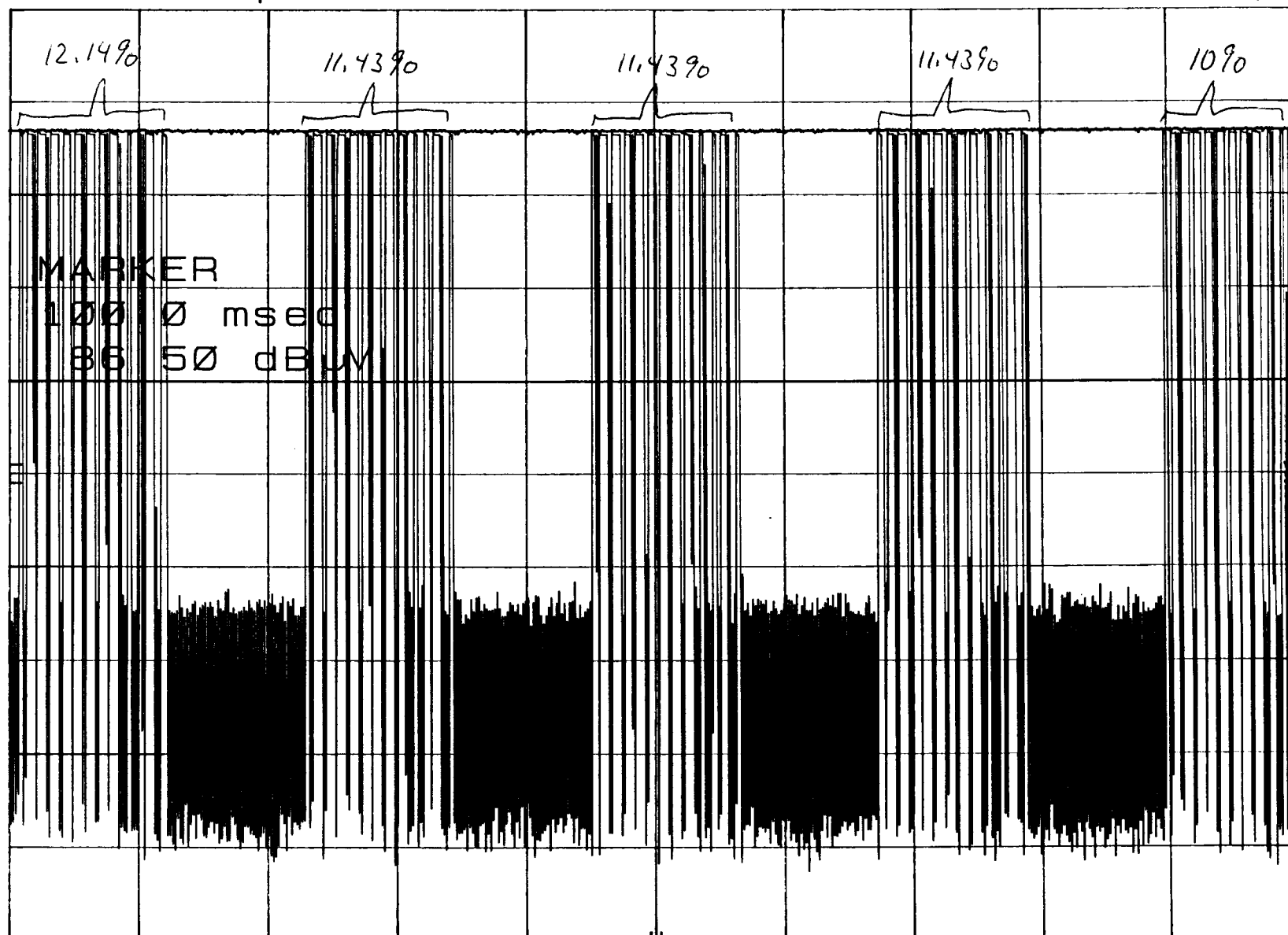
DL

60.0

dB μ V

ROUNDED UP
TO NEAREST % =
57%

CORR'D



CENTER 305.732 010 MHz

RES BW 1 MHz

VBW 1 MHz

SPAN 0 Hz

SWP 100 msec