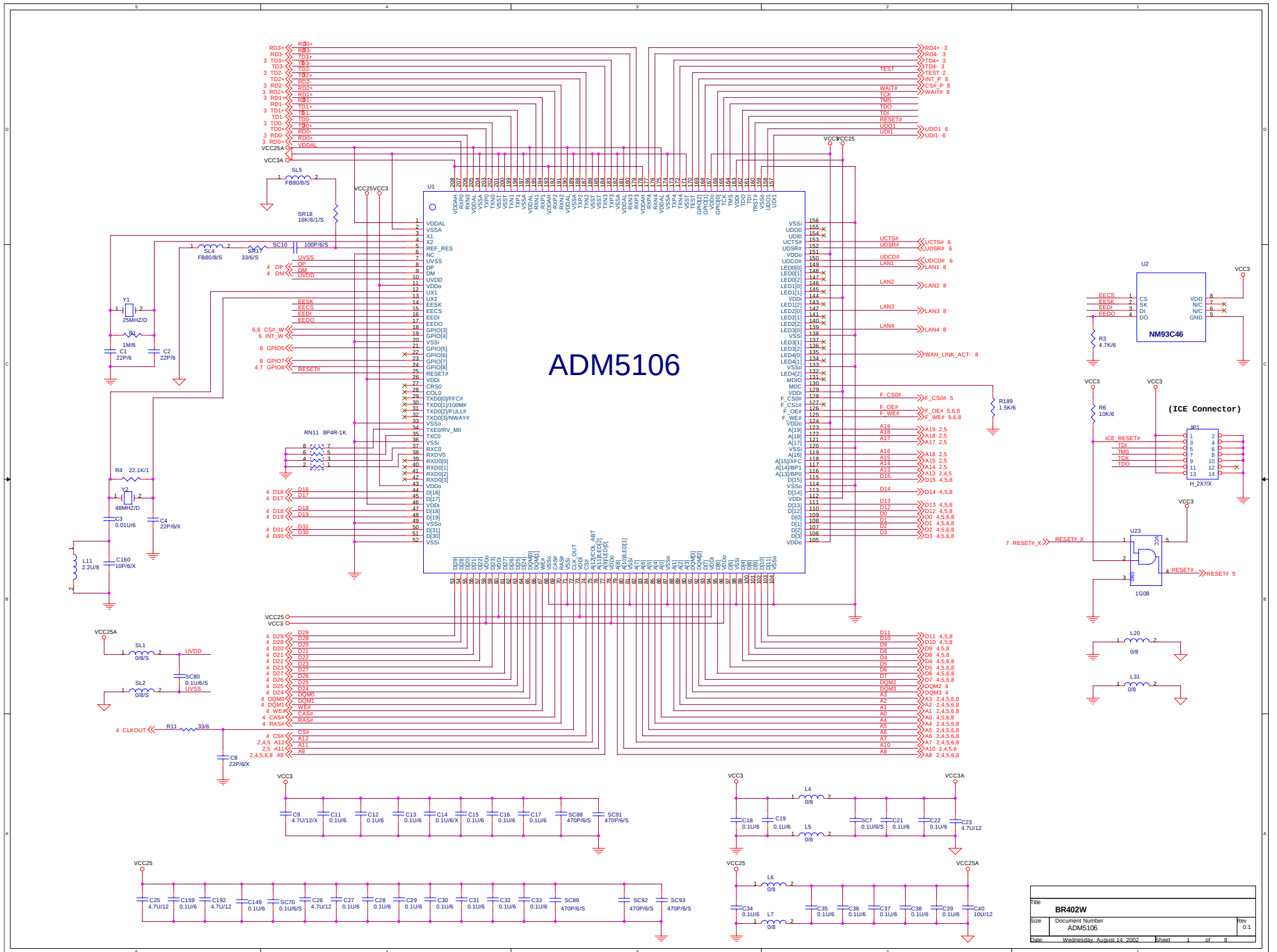
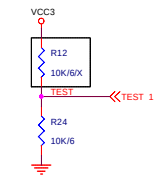
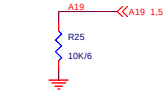


ADM5106

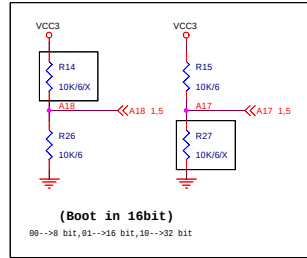




(TEST MODE)

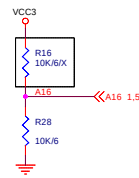


(Little Endian)

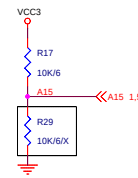


(Boot in 16bit)

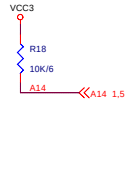
00-->8 bit, 01-->16 bit, 10-->32 bit



(5107 MII1#/GPIO)

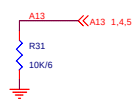


Enable
802.3x
flow
control



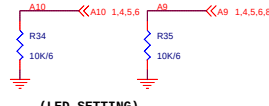
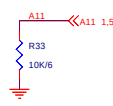
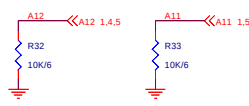
(BP1)

All jam back-pressure(SW
SETTING)

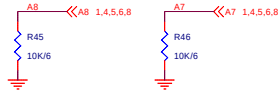


(BP0)

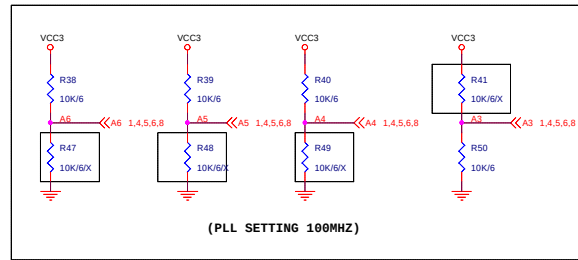
(COL_ABT)will not abort
packet when meet 16
consecutive collisions



(LED SETTING)



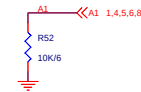
(ARM/USB OBSERVED MODE)



(PLL SETTING 100MHZ)

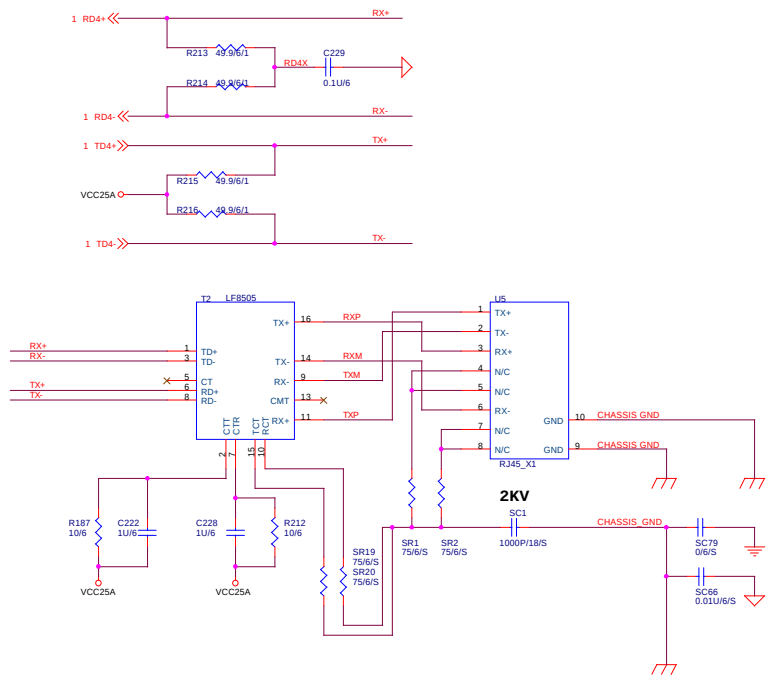
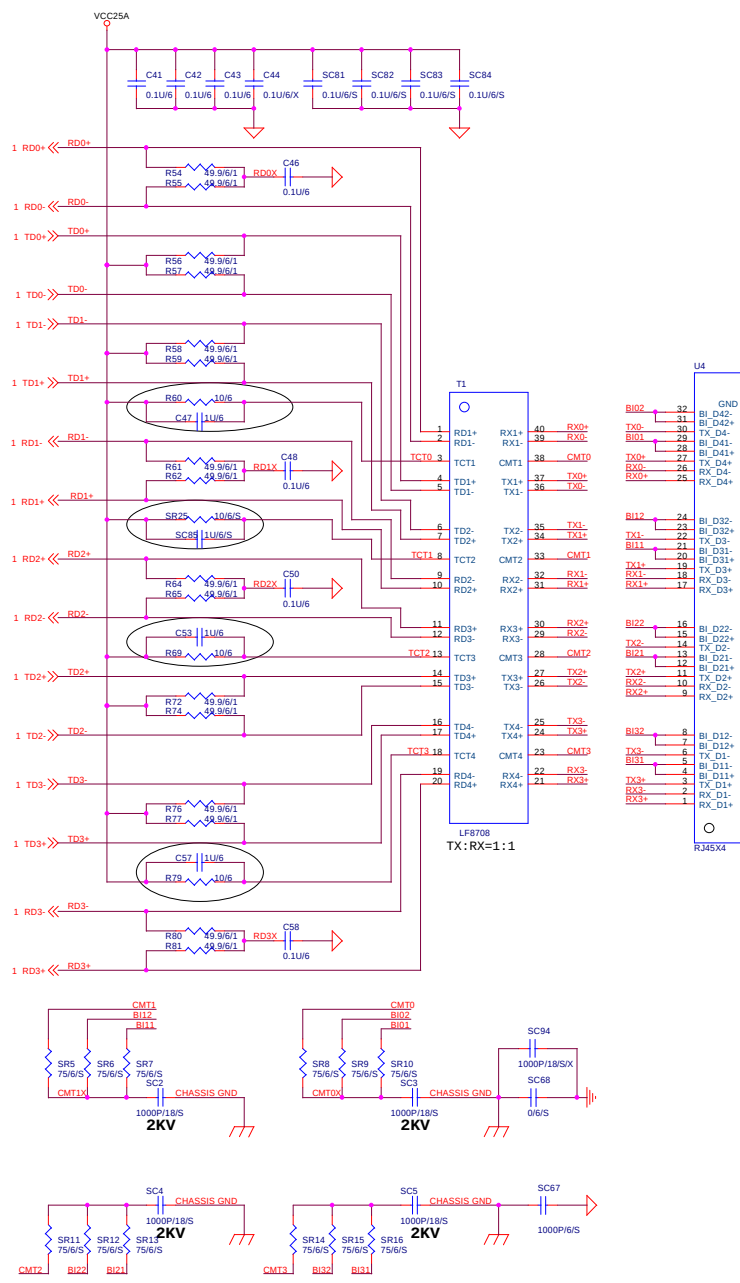


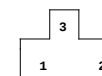
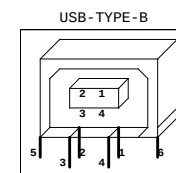
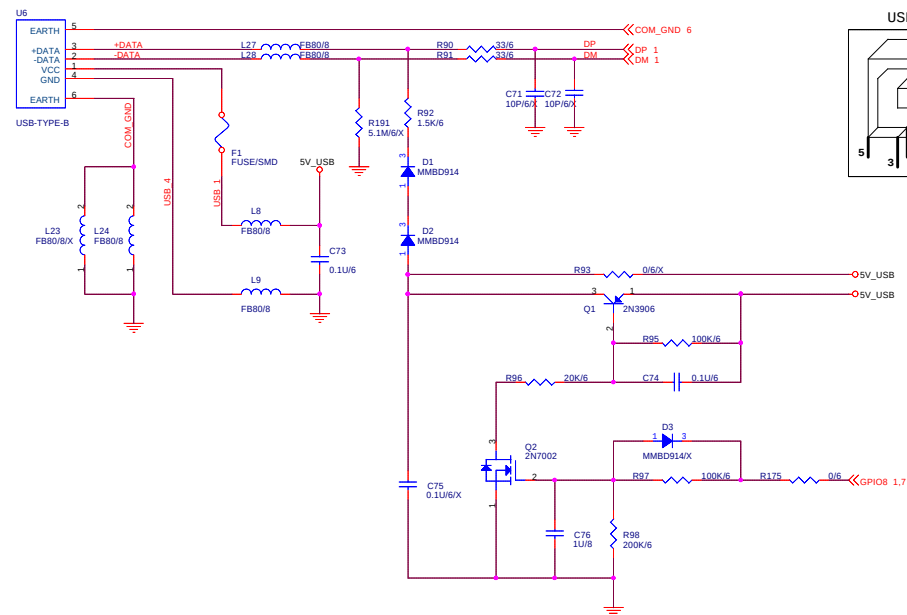
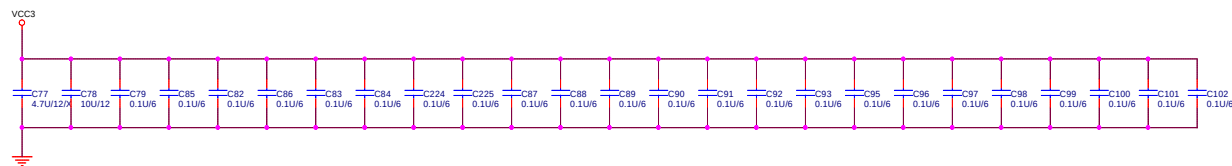
(USB CXVR BYPASS)



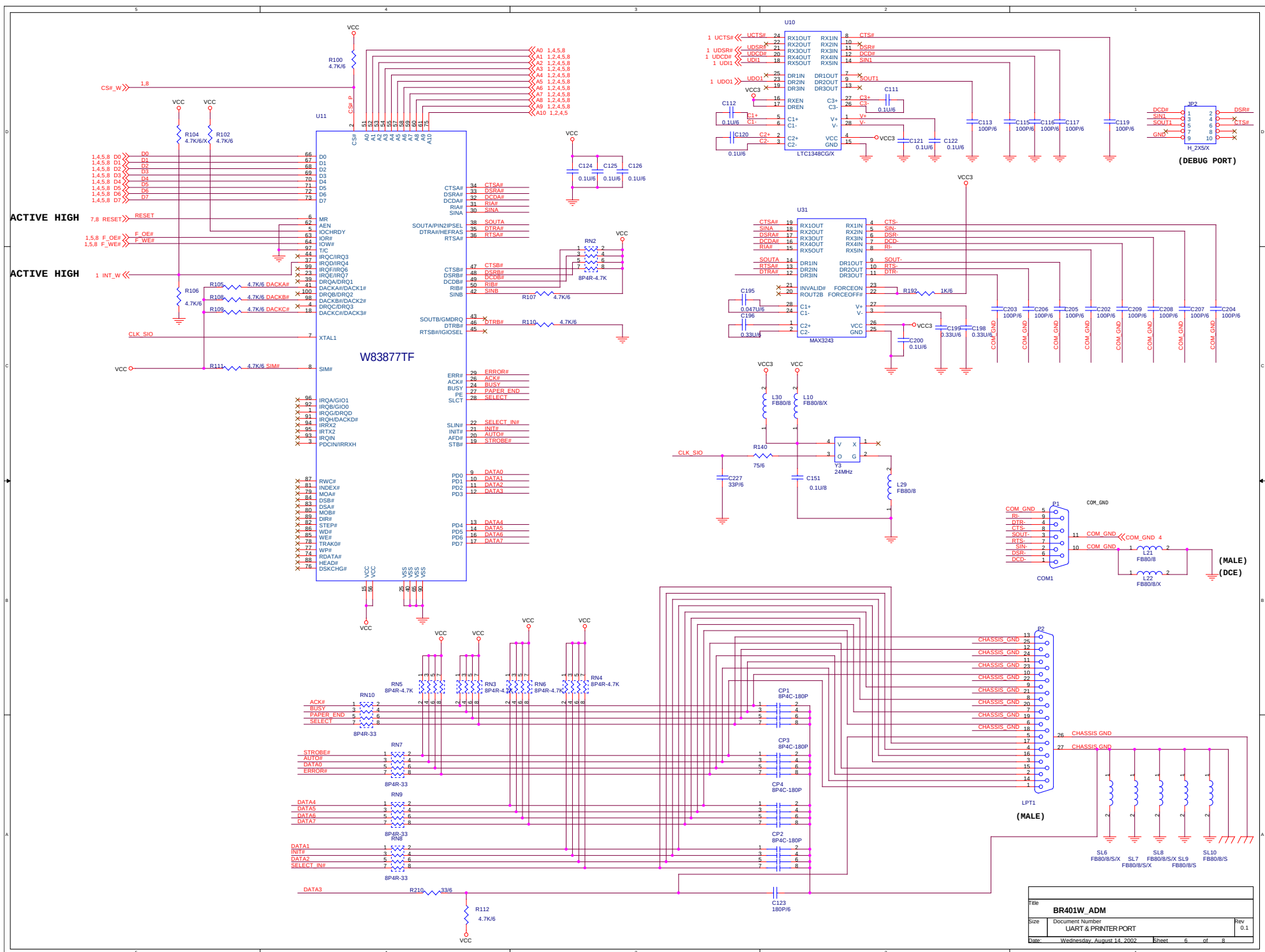
(SIMULATION MODE)

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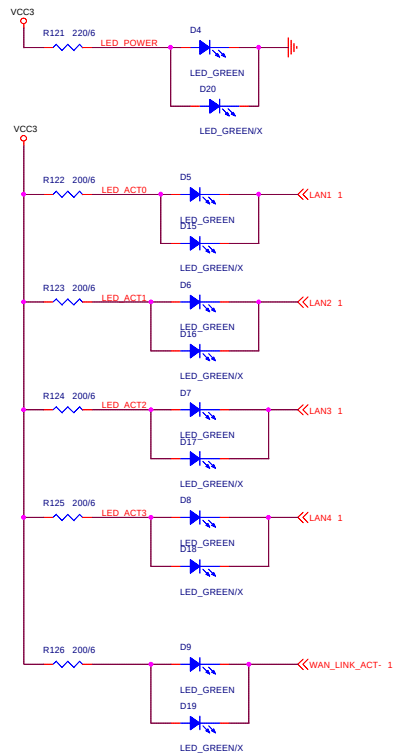


NO

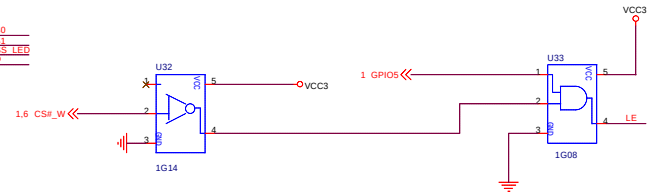
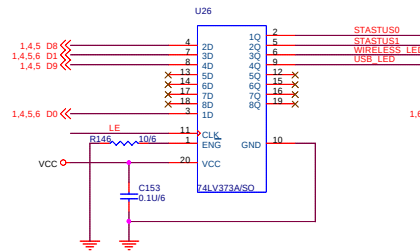
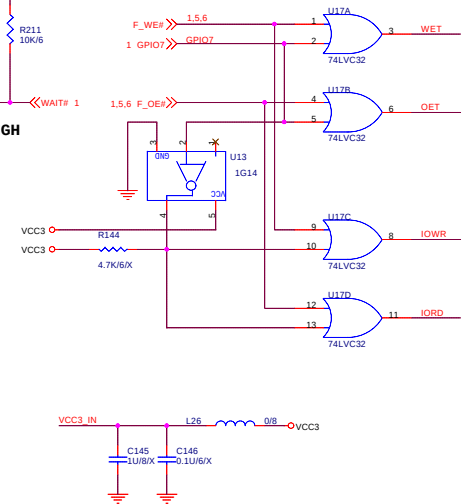
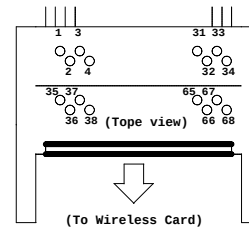
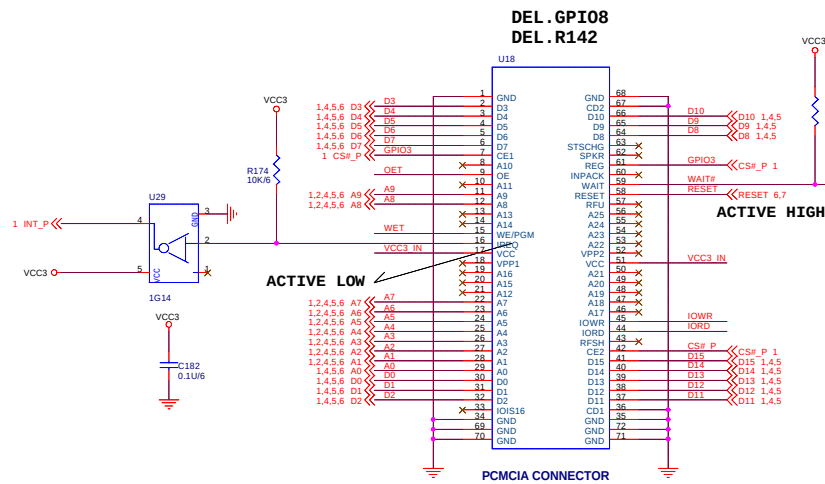
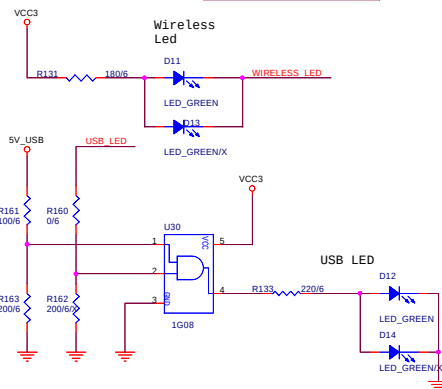
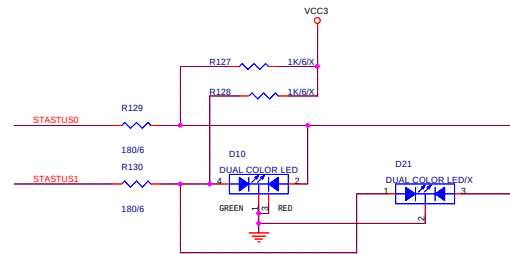


The diagram shows a pushbutton switch (PUSHBUTTON) with two terminals. One terminal is connected to a pull-up resistor (R145, 10K/5) which is connected to VCC3. The other terminal is connected to a decoupling capacitor (C158, 0.01u/6) which is connected to SW GND. The output of the switch is connected to a resistor (R143, 1K/6) which is connected to GPIOB 1.4.

Title			
BR401W_ADM			
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STATUS LED



K1
K2
K3
K4
K5
K6
K7
K8
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K10
K11
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K14
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K100

File	BR401W_ADM		
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