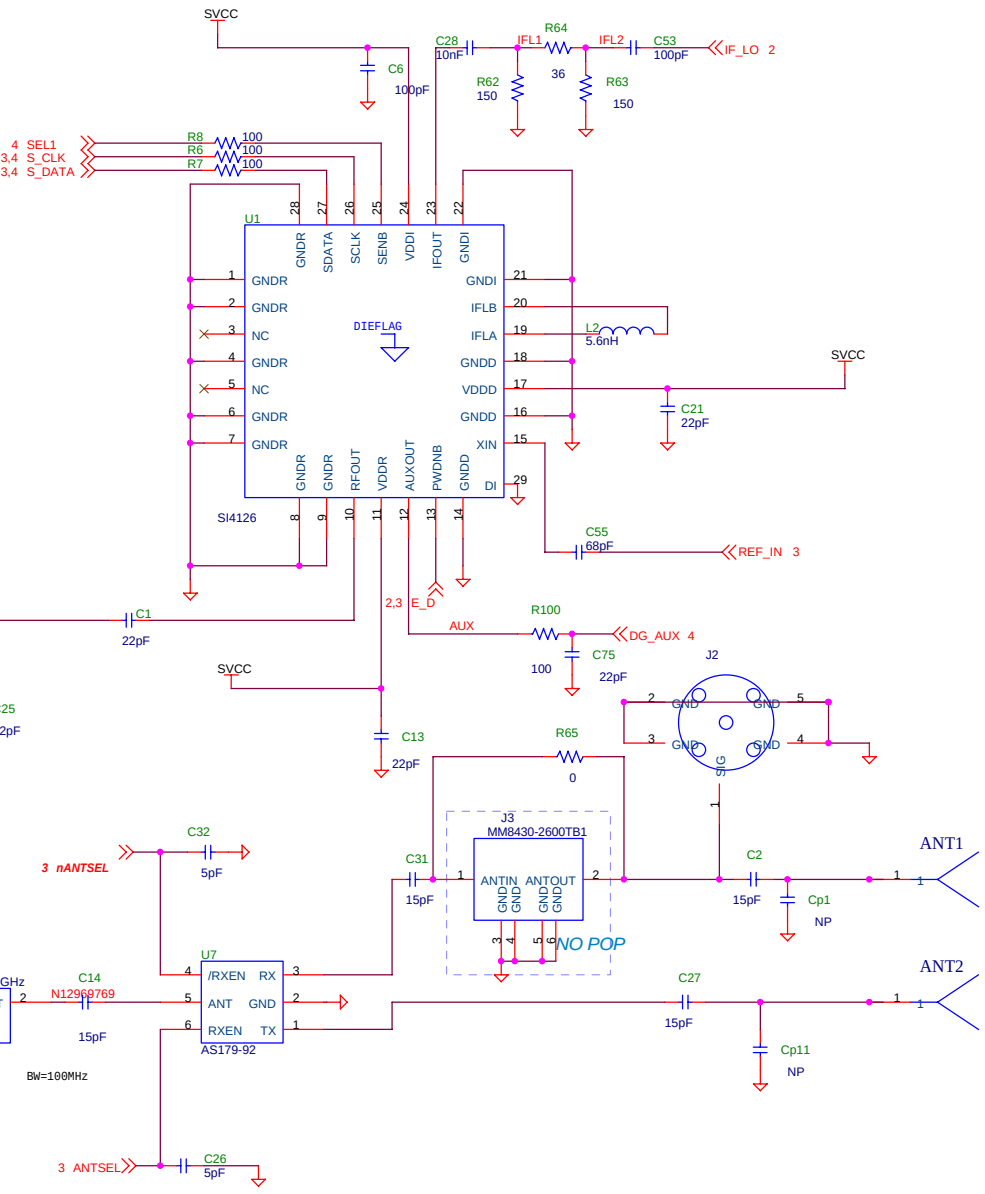
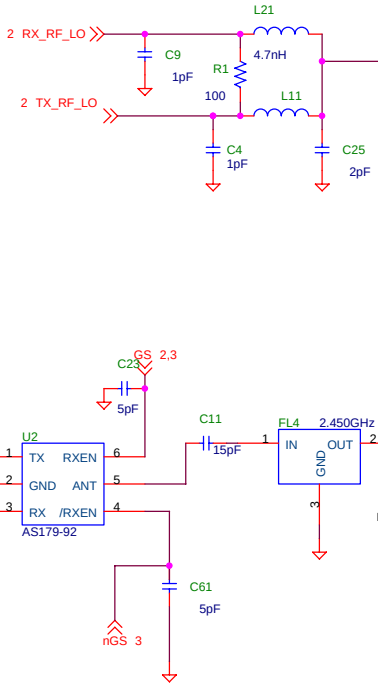
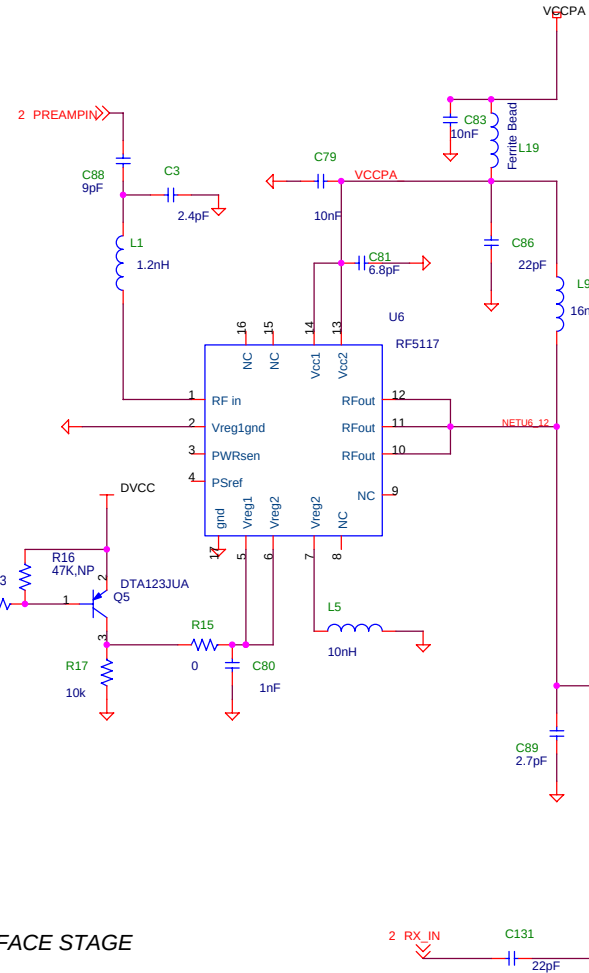
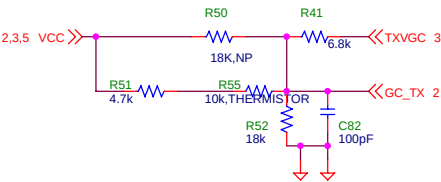
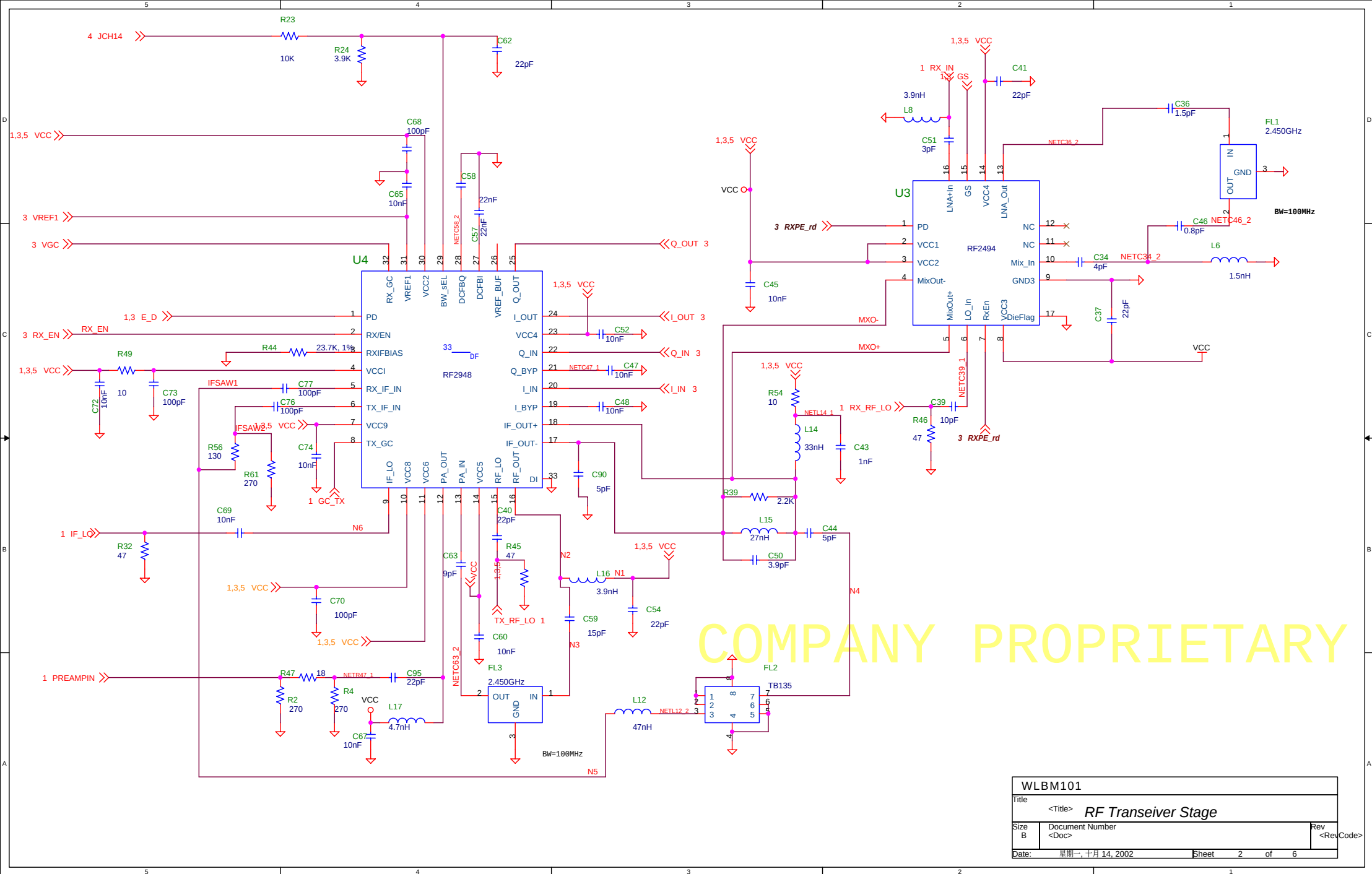


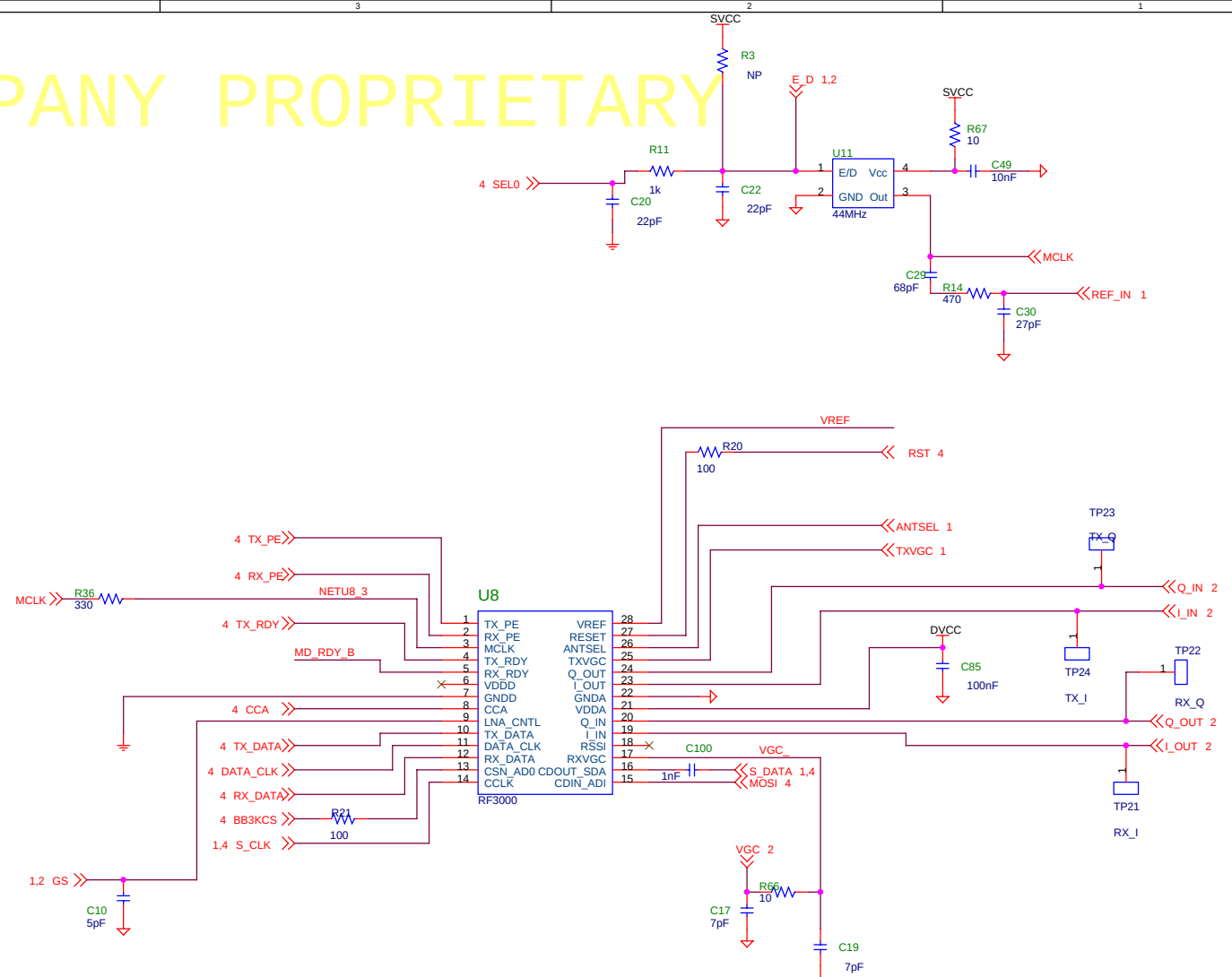
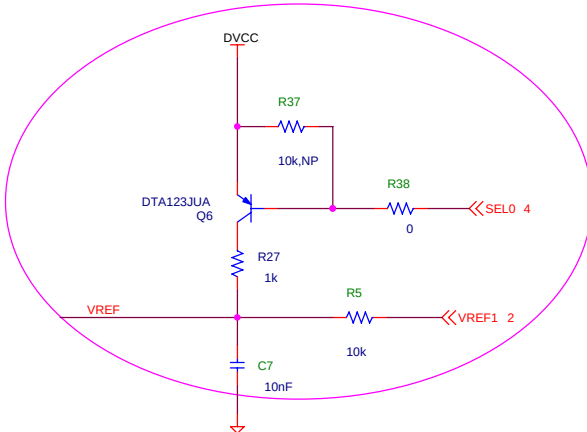
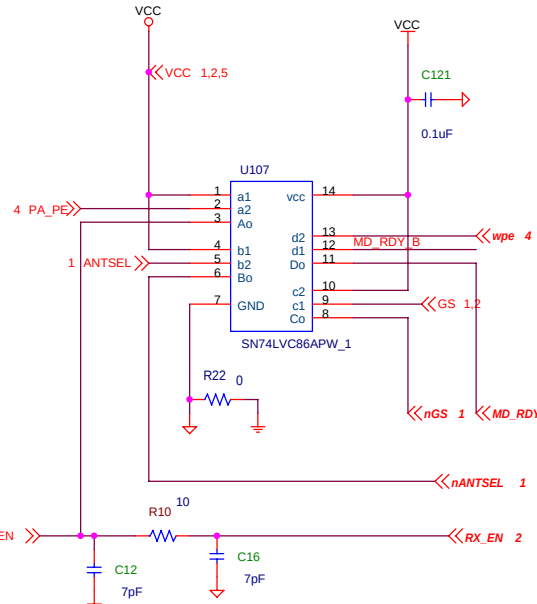
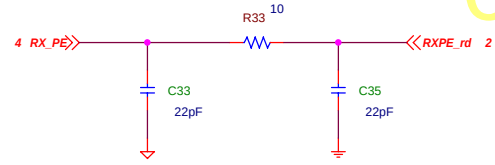


INTERFACE STAGE

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| Size                                        | Document Number Custom | Rev <RevCode> |
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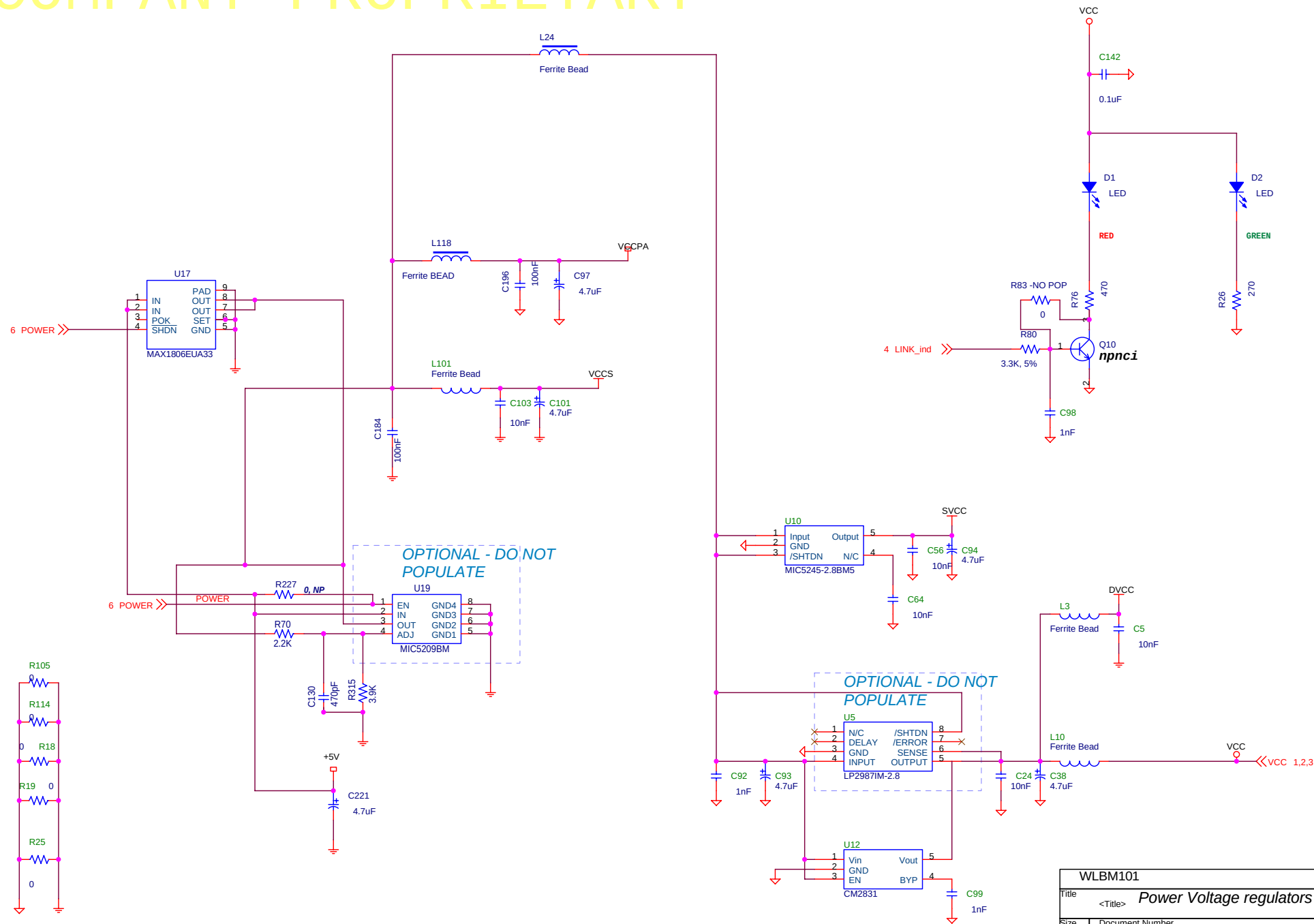


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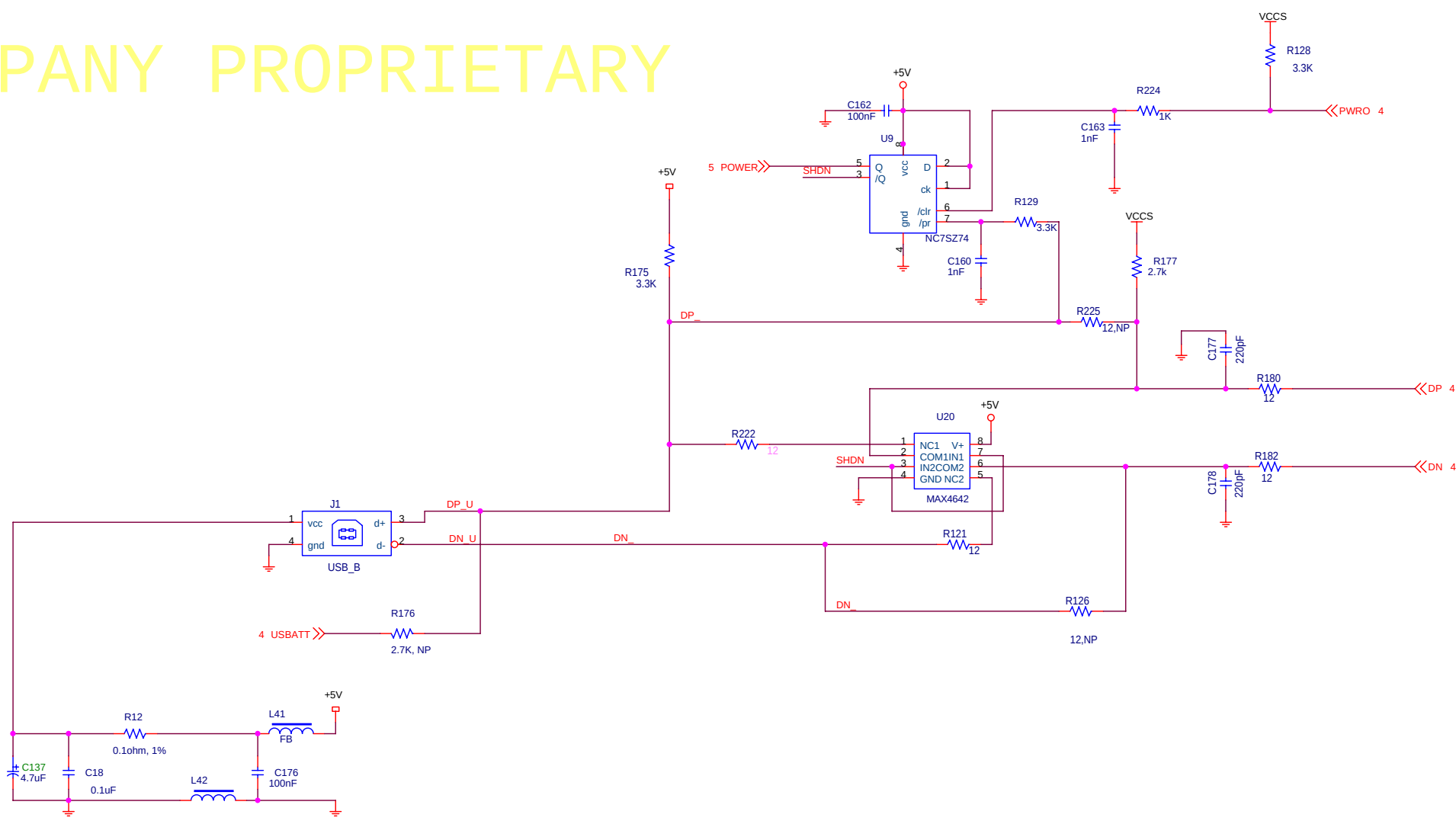


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| Date:                               | 星期一, 十月 14, 2002 | Sheet 3 of 6 |                |





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| Title  | <Title> Power Voltage regulators |              |
| Size   | Document Number                  | Rev          |
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| Date:  | 星期一, 十月 14, 2002                 | Sheet 5 of 6 |



|                                         |                  |              |
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| Title                                   |                  |              |
| <Title> SUSPEND CIRCUIT & USB INTERFACE |                  |              |
| Size                                    | Document Number  | Rev          |
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| Date:                                   | 星期一, 十月 14, 2002 | Sheet 6 of 6 |