

ZEBRA

CHEETAH BASED FLAT PANEL MONITOR CONTROLLER

REFRESH RATE : 70Hz(Max) DCLK : 75MHz(Max)

REV 0.6

08 FEB 1998

ROAD MAP

DESCRIPTION	SHEET NO.
THIS SHEET	1
MICROCONTROLLER INTERFACE	2
CHEETAH - FLAT PANEL MONITOR CONTROLLER	3
VRAM INTERFACE	4
FLAT PANEL INTERFACE	5
ANALOG DIGITAL CONVERTER INTERFACE & GENLOCK PLL	6
VIDEO AMPLIFIER AND DAC FOR DIGITAL CONTROL	7
POWER SUPPLY AND DECOUPLING CAPACITORS	8

REVISION HISTORY

REV.	DESCRIPTION	BY	LAST UPDATE
0.0	PRELIMINARY DESIGN DERIVED FROM CHEETAH r2.2	KARANTH	08 DEC '97
0.1	CHANGES FROM INTERNAL ENCG REVIEW	KARANTH	09 DEC '97
0.2	CHANGED MEMORY DECODE LOGIC DELETED R14	KARANTH	10 DEC '97
0.3	GATE SWAP ON U15,U17 & U18	KARANTH	11 DEC '97
0.4	CHANGED C125,C140 CONNECTIONS DELETED L4, RENAMED X1,X2	KARANTH	11 DEC '97
0.5	ADDED MOUNTING HOLES CHANGED BKLT POWER TO +12VIN FROM +12V	KARANTH	18 DEC '97
0.6	MODIFIED LM1203 POWER SUPPLY CONNECTIONS ADDED COMPENSATION CAPS FOR U27(MAX4129)	KARANTH	08 FEB '98

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Title ZEBRA

Size Document Number
ZEBRA 001

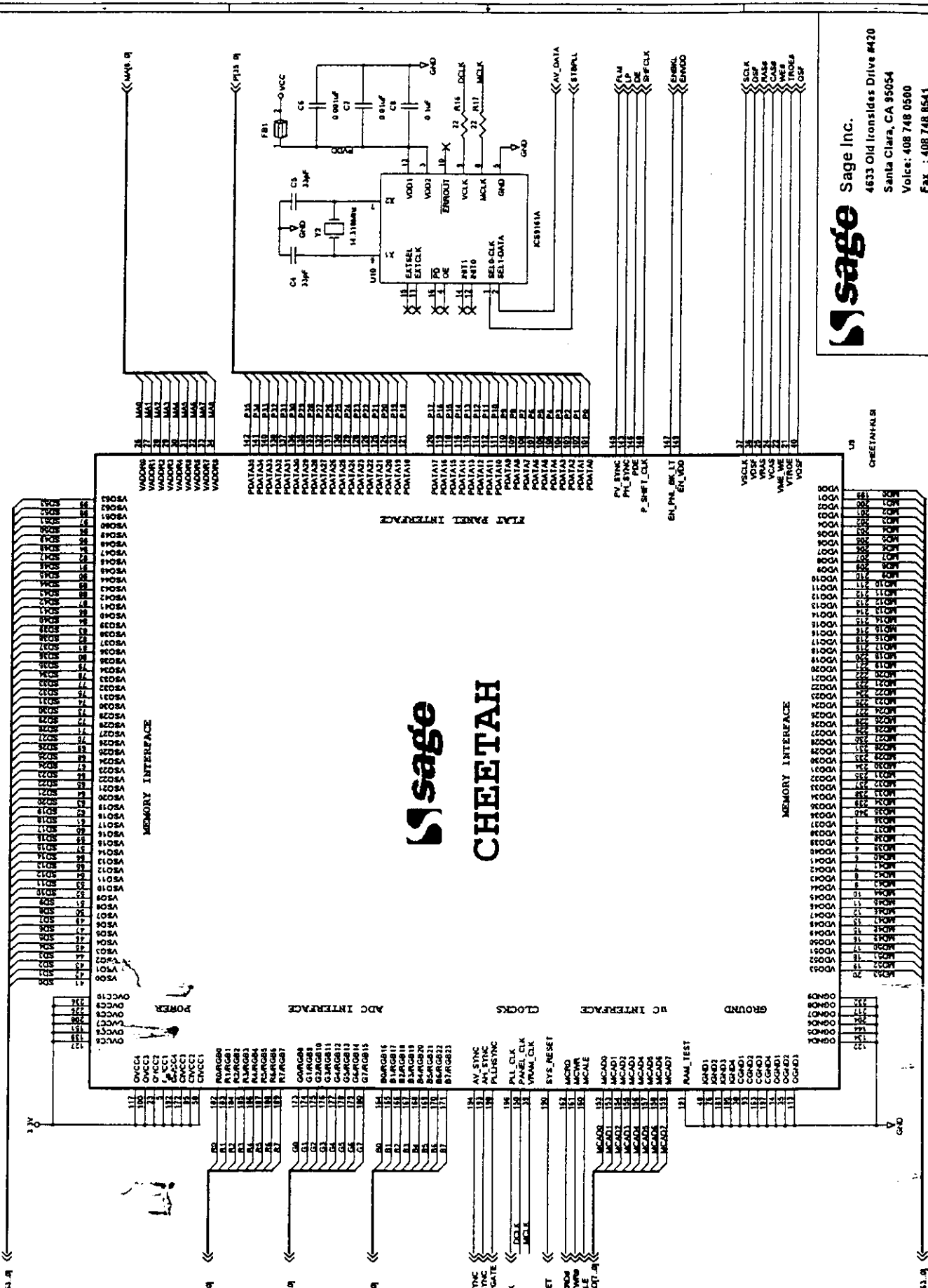
Date 11/01/98 Rev 0.6

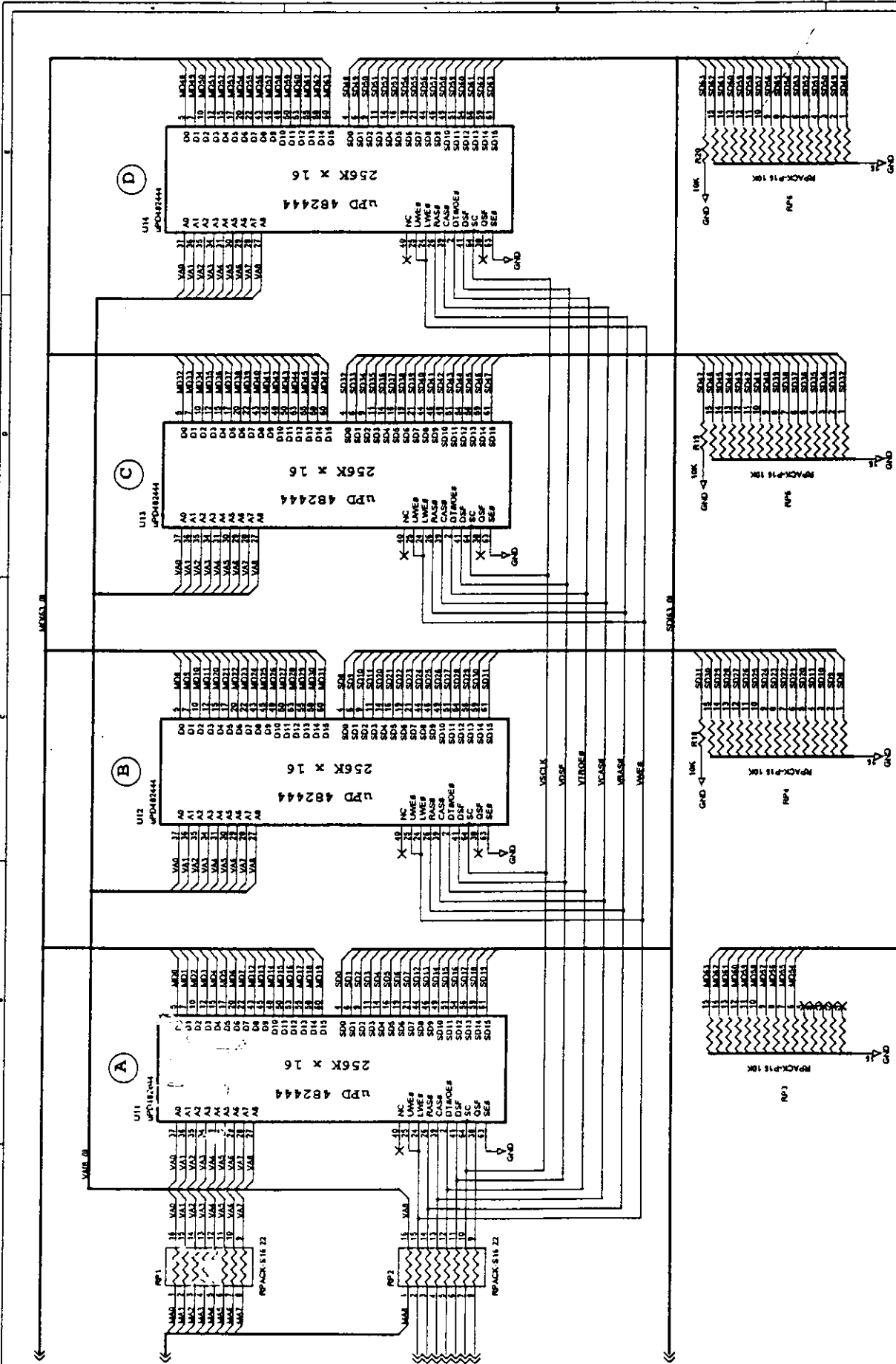
CHEETAH

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ZEBRA - CHEETAH ASIC INTERFACE
 DocuPrint Name
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Date: 1994, February 13, 1994
 Rev: 0.0



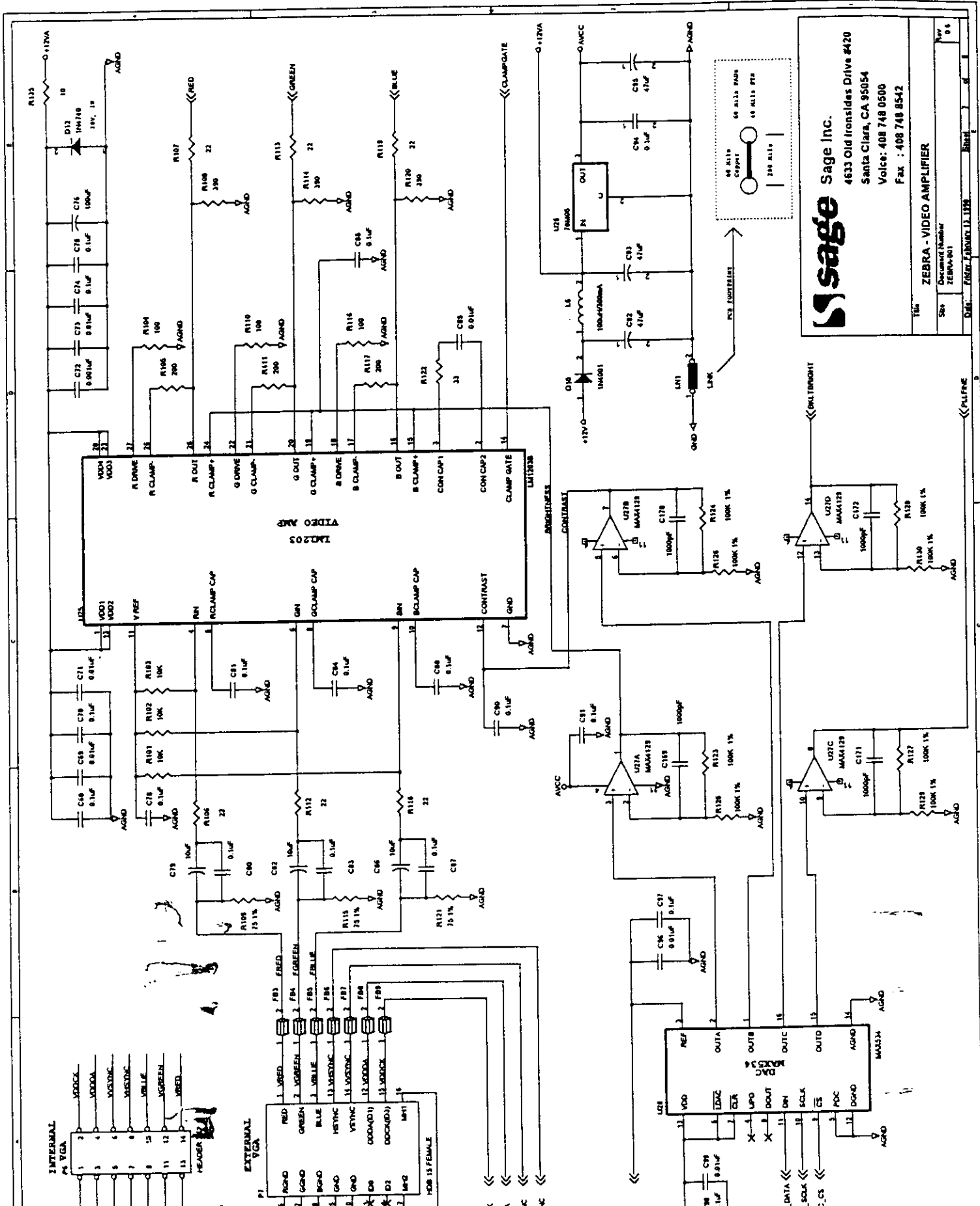


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ZEBRA - MEMORY INTERFACE

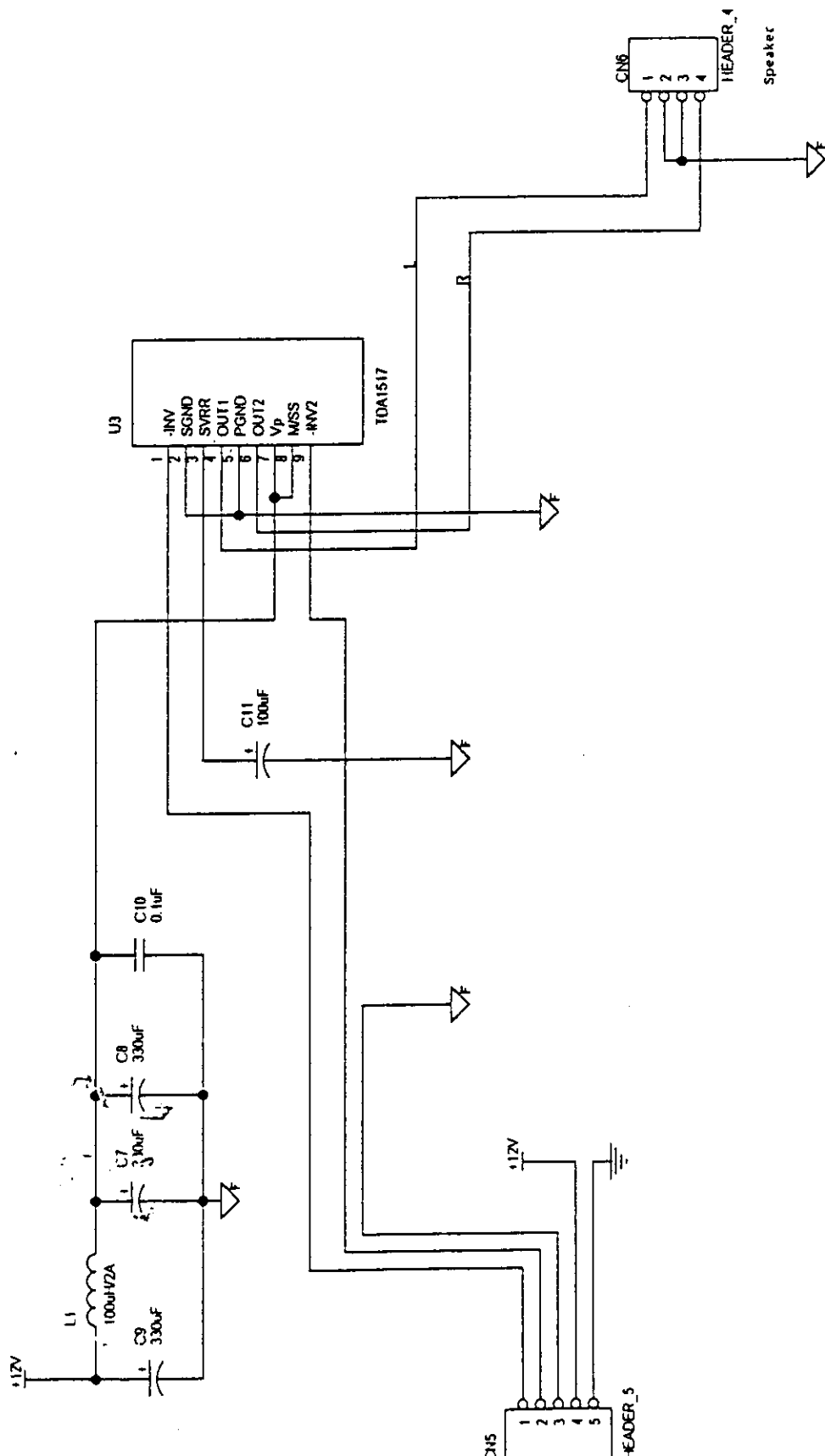
Rev 0.1

MEMORY POPULATION OPTIONS		
PANEL TYPE	PRESENT	ABSENT
ALL TFT	VRAMS A, B, C AND D	RP4, RP5 & RP6 R18, R19 & R20
STN 16 BIT 2-2/3 PIX/CLK	VRAM A ONLY RP4, RP5 & RP6 R18, R19 & R20	VRAMS B, C, D
STN 24 BIT 4 PIX/CLK	VRAMS A AND B ONLY RP5 & RP6 R19 & R20	VRAMS C AND D RP4 R18



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ZEBRA - VIDEO AMPLIFIER	
Title ZEBRA-001	Date: 10/10/83
Size 8 1/2 x 11	Sheet 1 of 1



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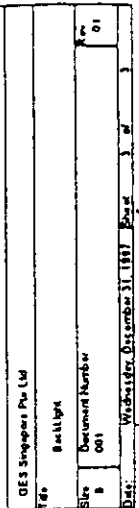
Title AUDIO

Size A Document Number 001

Rev 01

Date: Tuesday, January 06, 1998

Sheet 2 of 3



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LCD SWITCH & PANEL CONNECTOR

DRAWN:	DATED:				REV:
CHECKED:	DATED:	CODE:	SIZE:	DRAWING NO:	
QUALITY CONTROL:	DATED:				
RELEASED:	DATED:	SCALE:			SHEET: OF