

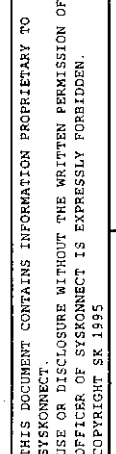
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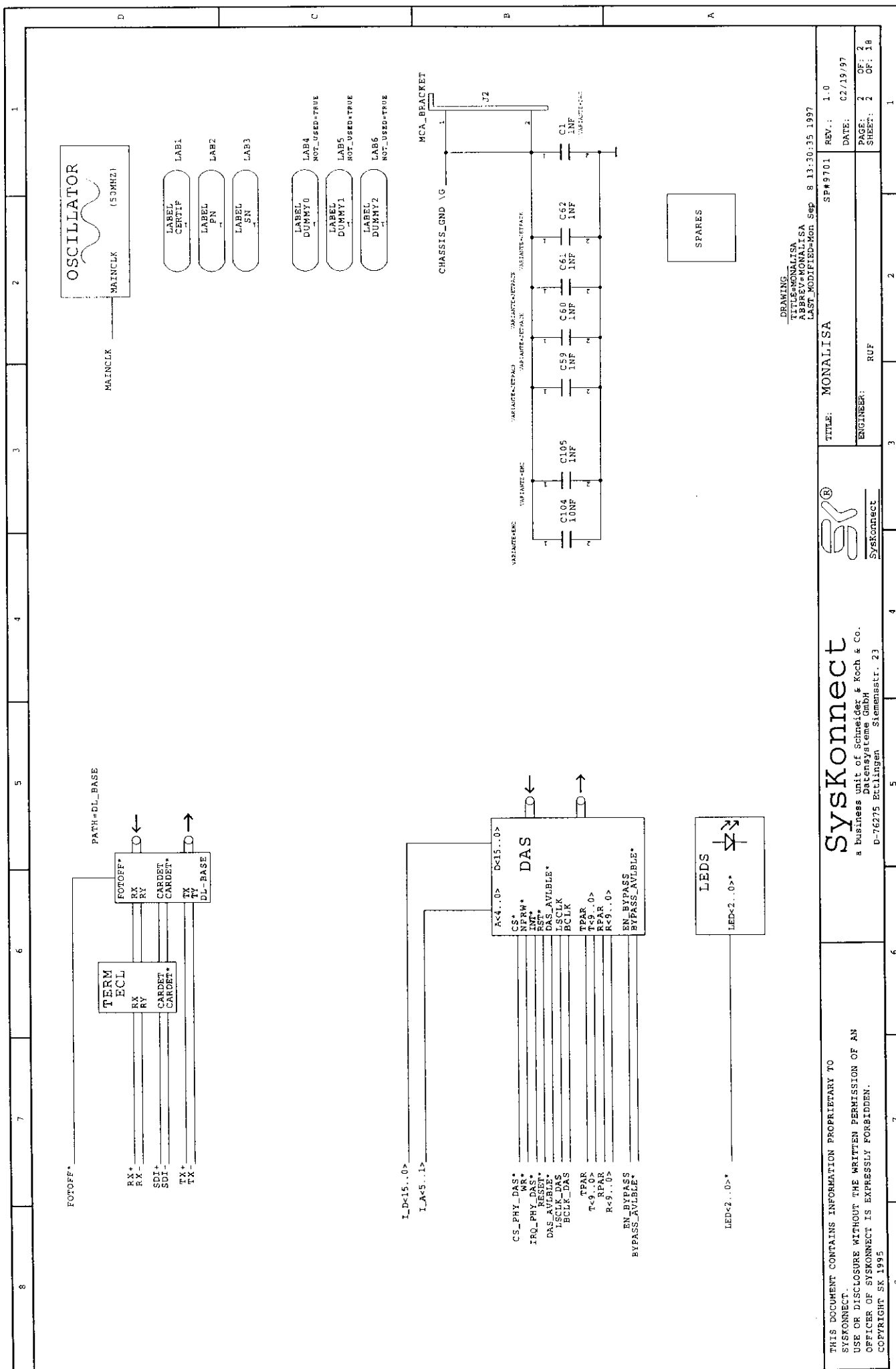
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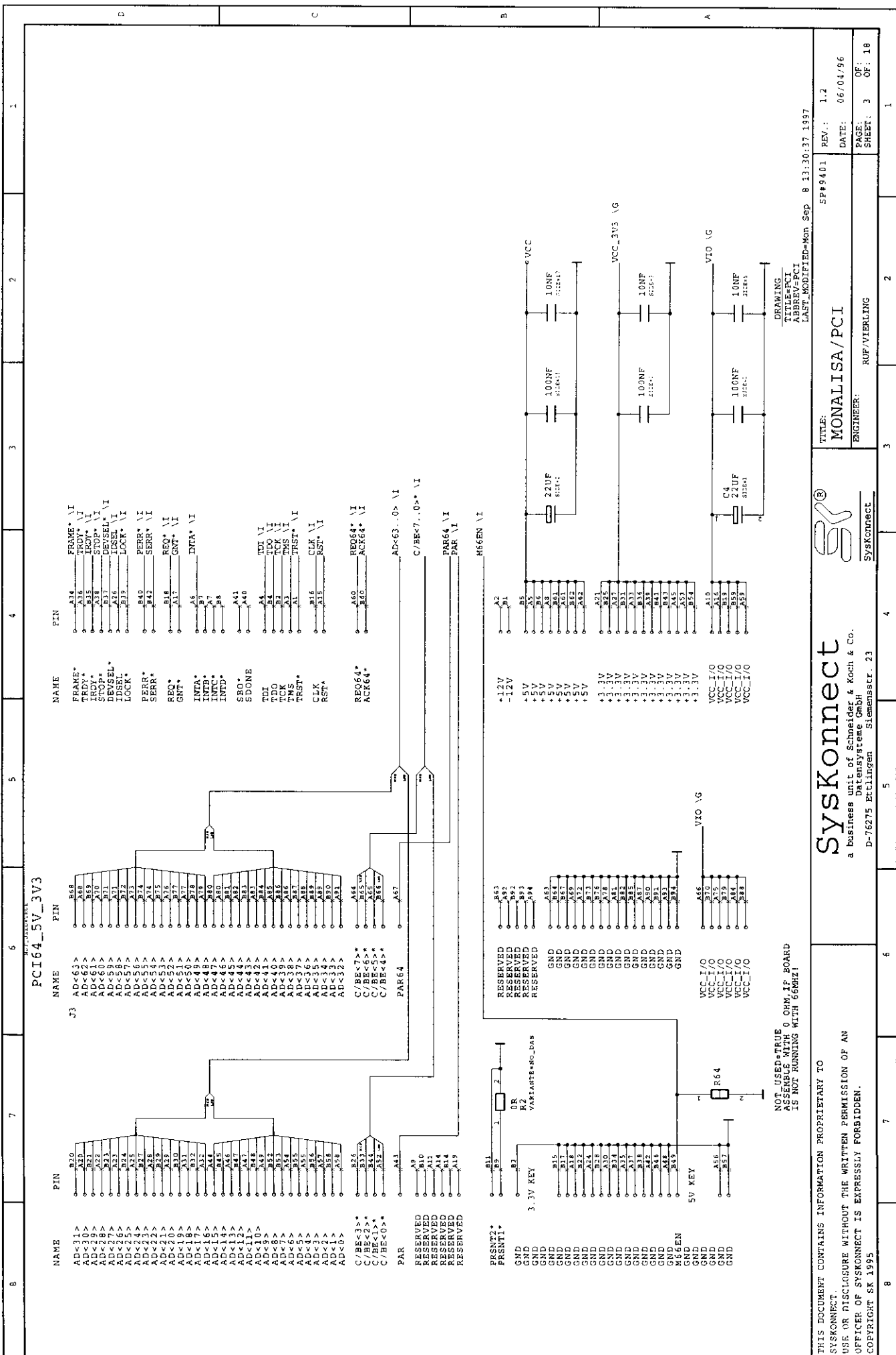
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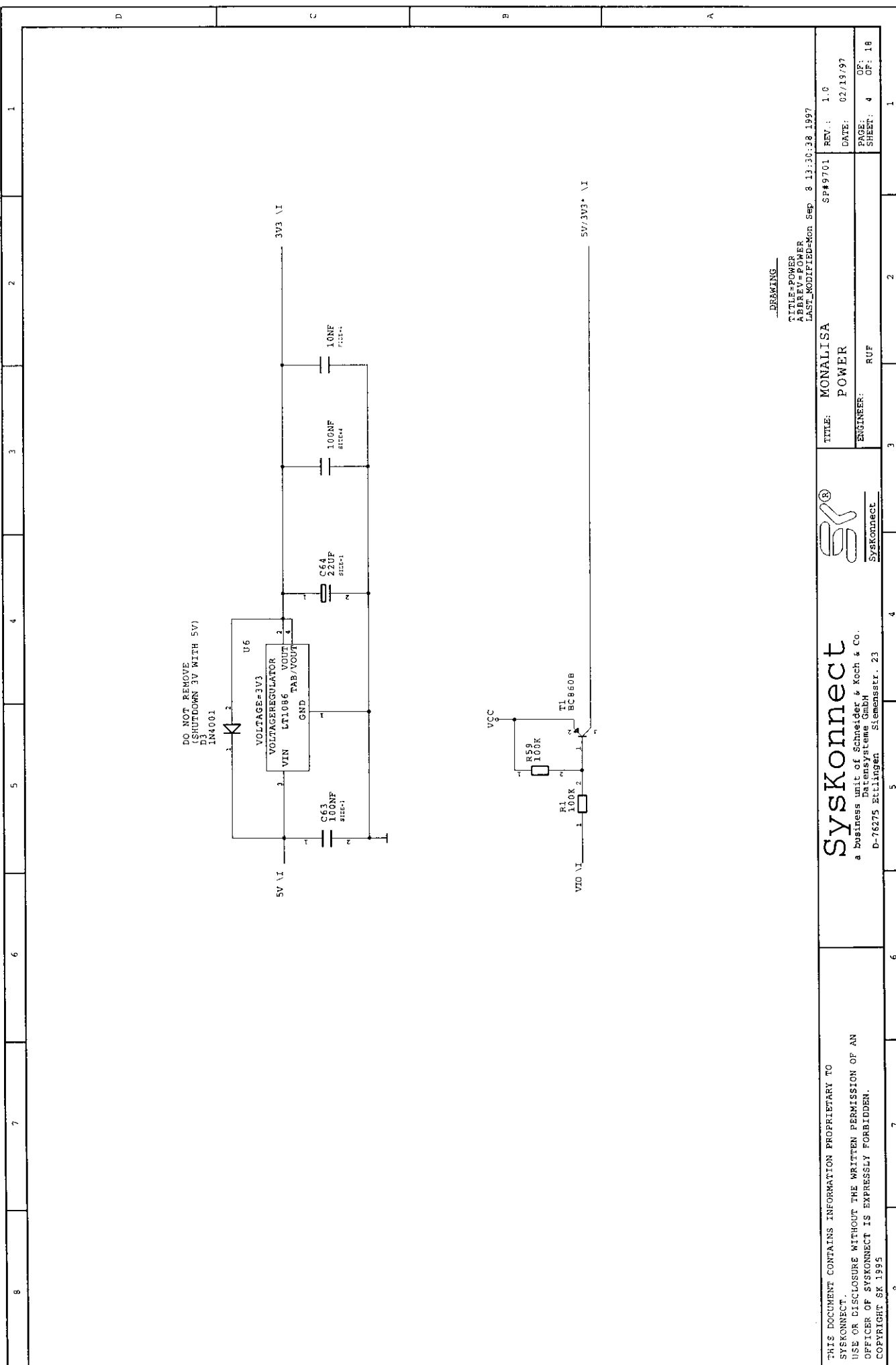
- Schematics -

18 pages without cover sheet









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TITLE: MONALISA
POWER

ENGINEER: RUF

REV: 1.0
DATE: 02/13/97
PAGE: 4
SHEET: 4 OF: 18

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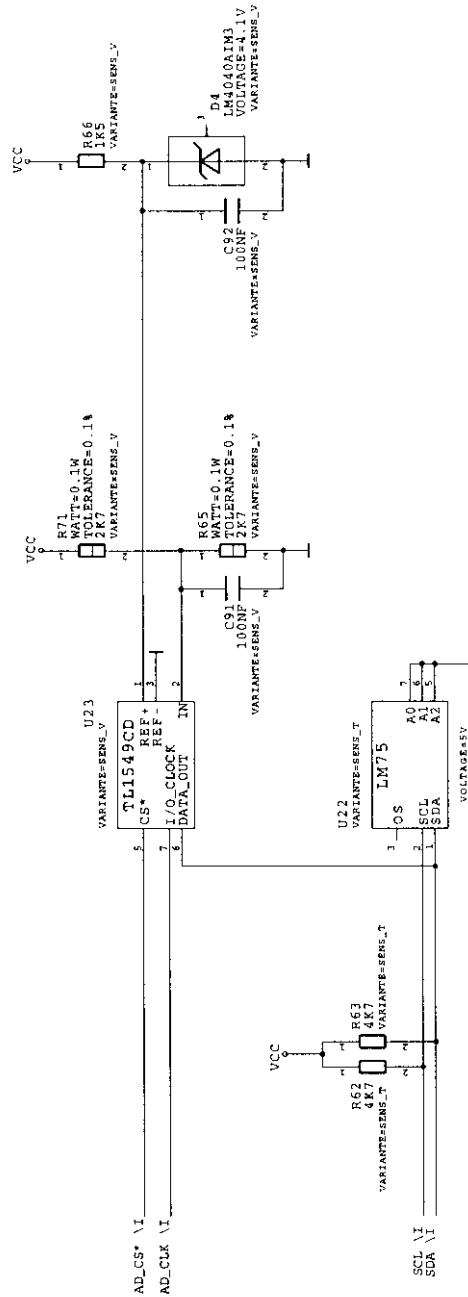
LAST MODIFIED: Mon Sep 8 13:30:38 1997

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TITLE: SENSORS
 PART: SENSORS
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U4 MEMORIEFELD MIT ANSCHLÜSSEN				AM29F040 528Kb FLASH MEMORY		D<7.0>VI	
A<16.0>VI WE* \I OE* \I CS* \I				Diagram showing connections to the AM29F040 Flash Memory chip. The chip is a rectangular component with pins on the left and right. Wires connect the chip to the labels A<16.0>VI, WE* \I, OE* \I, and CS* \I. A label D<7.0>VI is connected to the top of the chip.		SP#9701 REV.: 1.0	
LAST_MODIFIED=Mon Sep 8 13:30:36 1997				TITLE: MONALISA BOOT-EPROM		DATE: 02/19/97	
ENGINEER: RUF				PAGE: 7		OP: 18	
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BOOT-EPROM

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PAGE: 7

OP: 18



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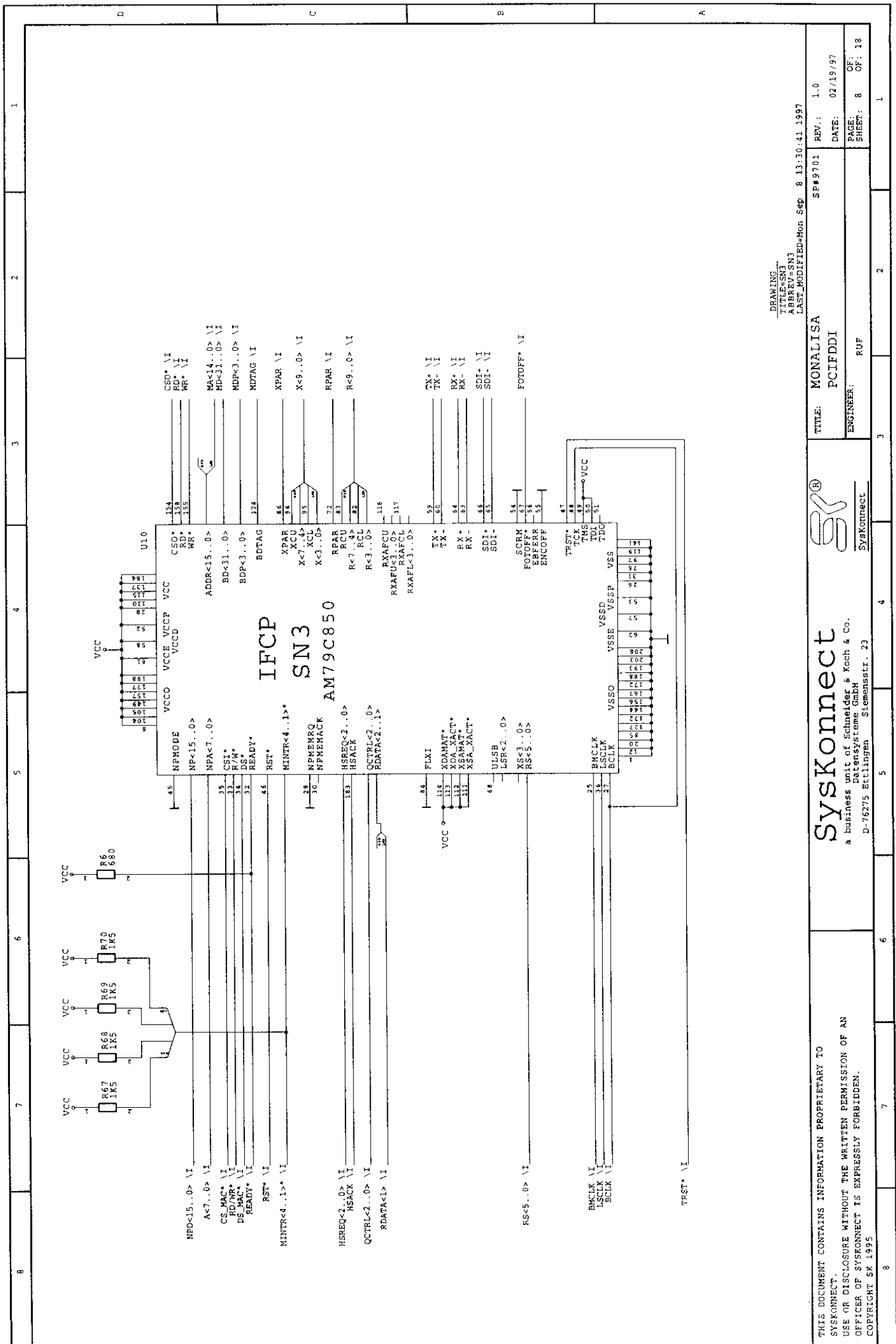
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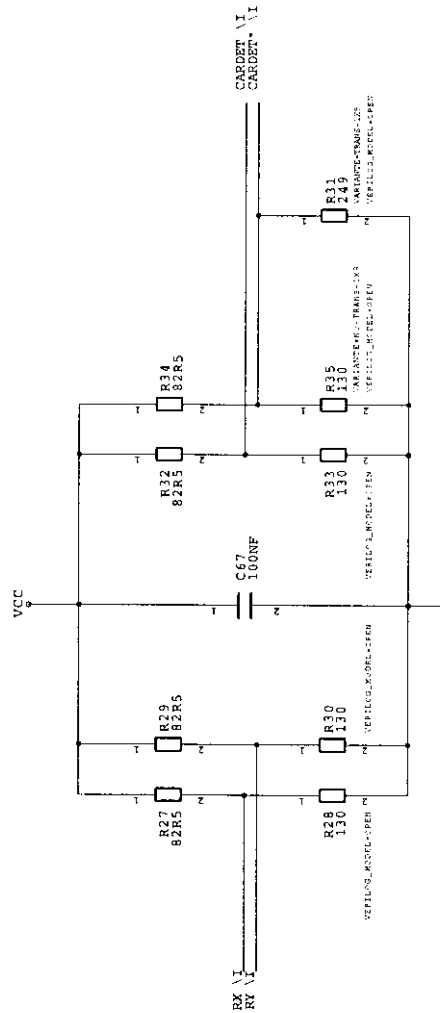




TITLE: MONALISA
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REV.:	1.0
DATE:	02/10/2010

19/97



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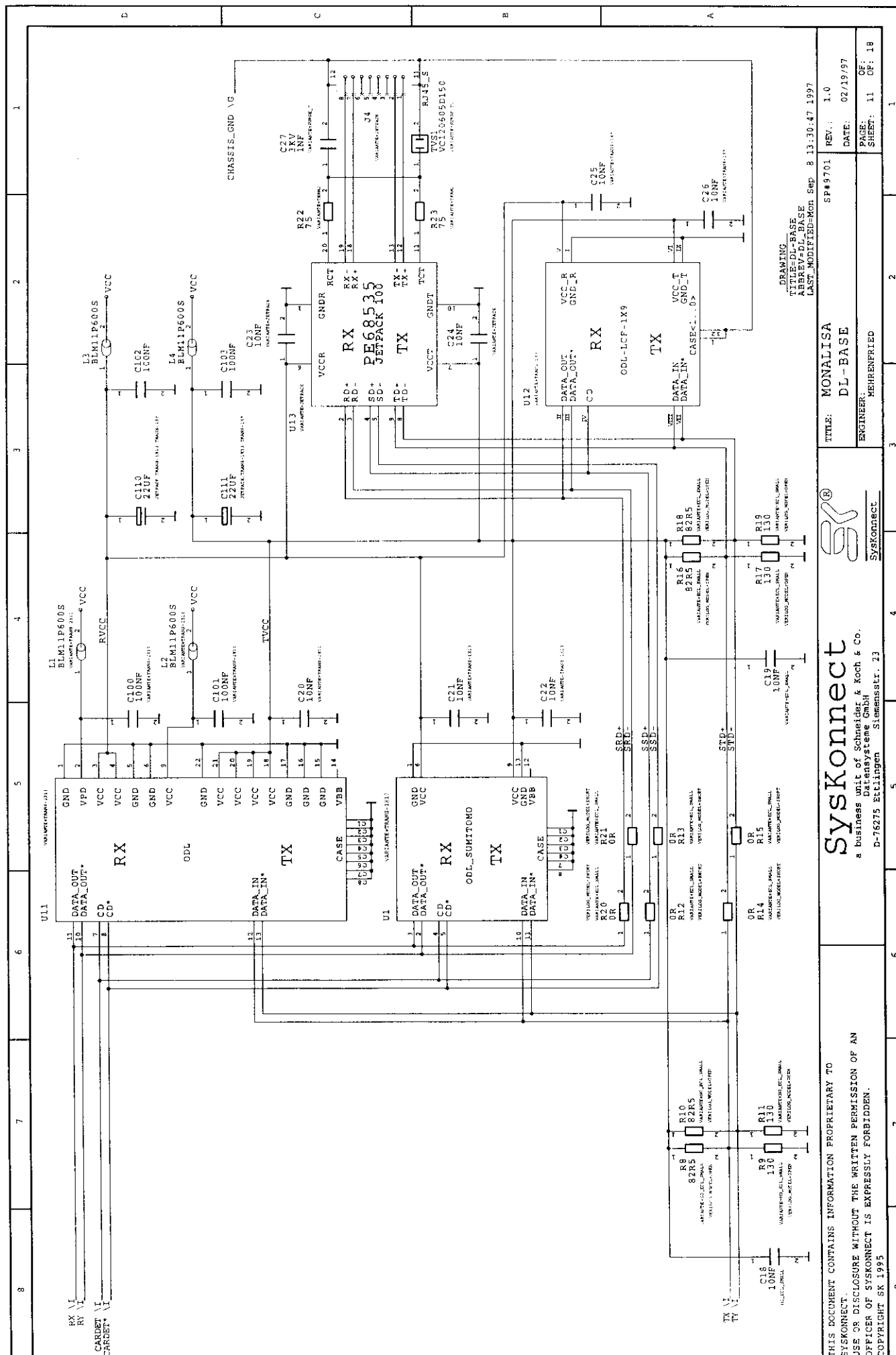
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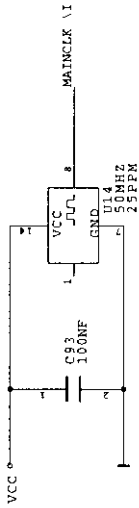
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TITLE: DL-BASE
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ABBREV=OSCILLATOR
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TITLE: MONALISA
OSCILLATOR



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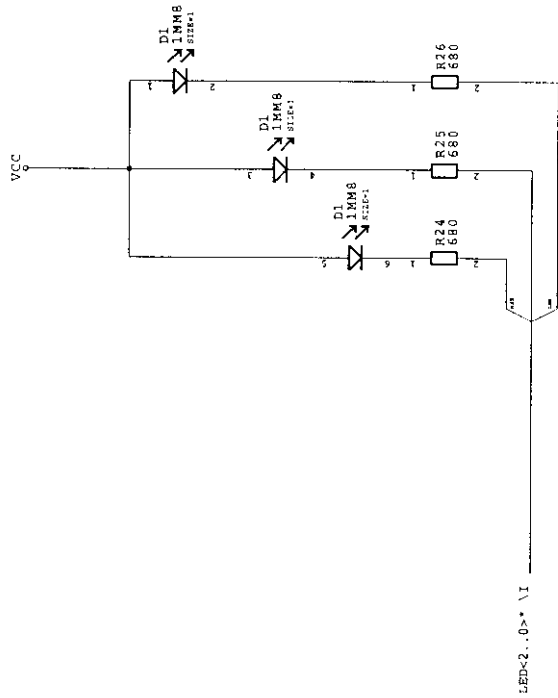
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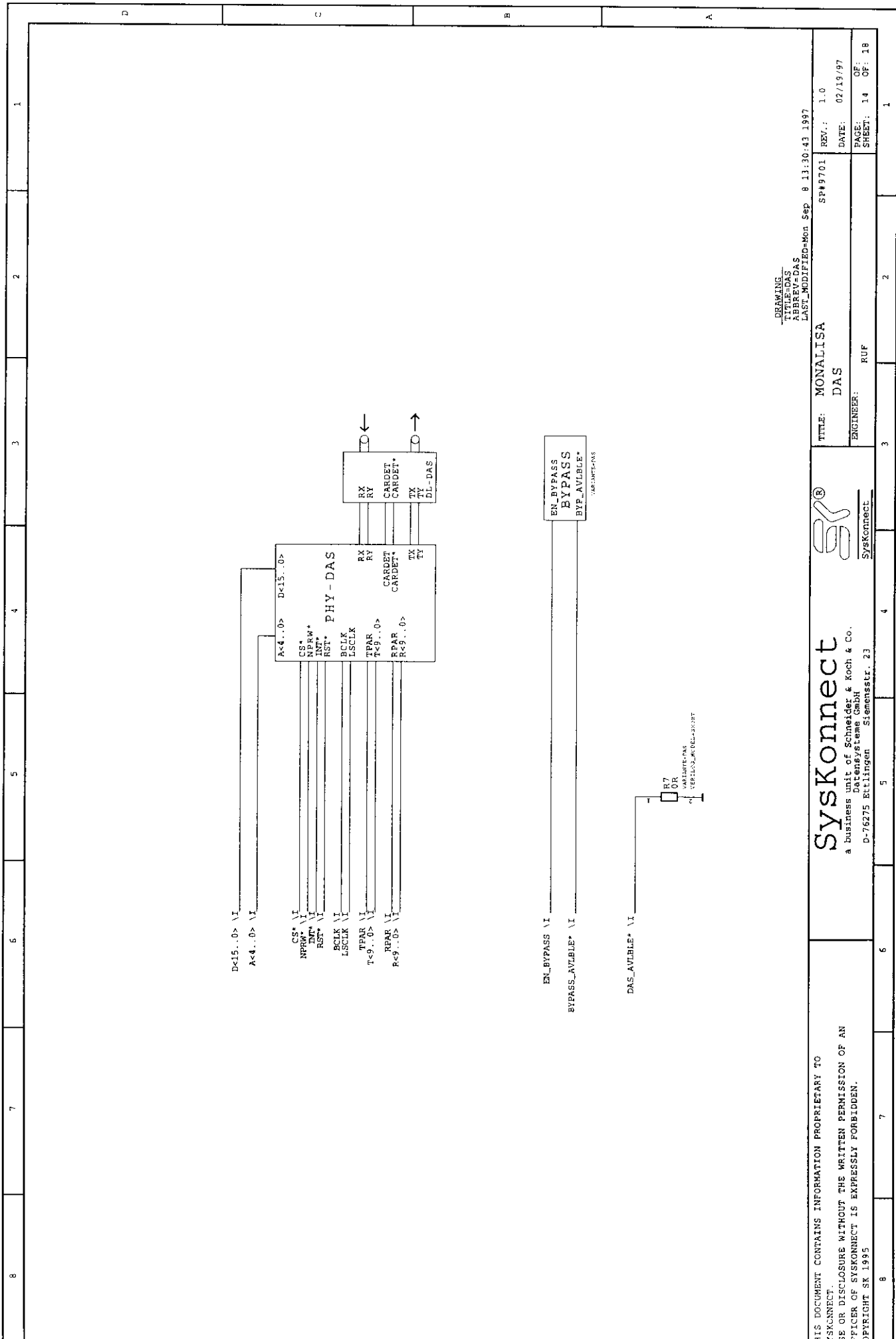
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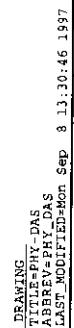
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TITLE: MONALISA
PHY-DAS

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TITLE: MONALISA
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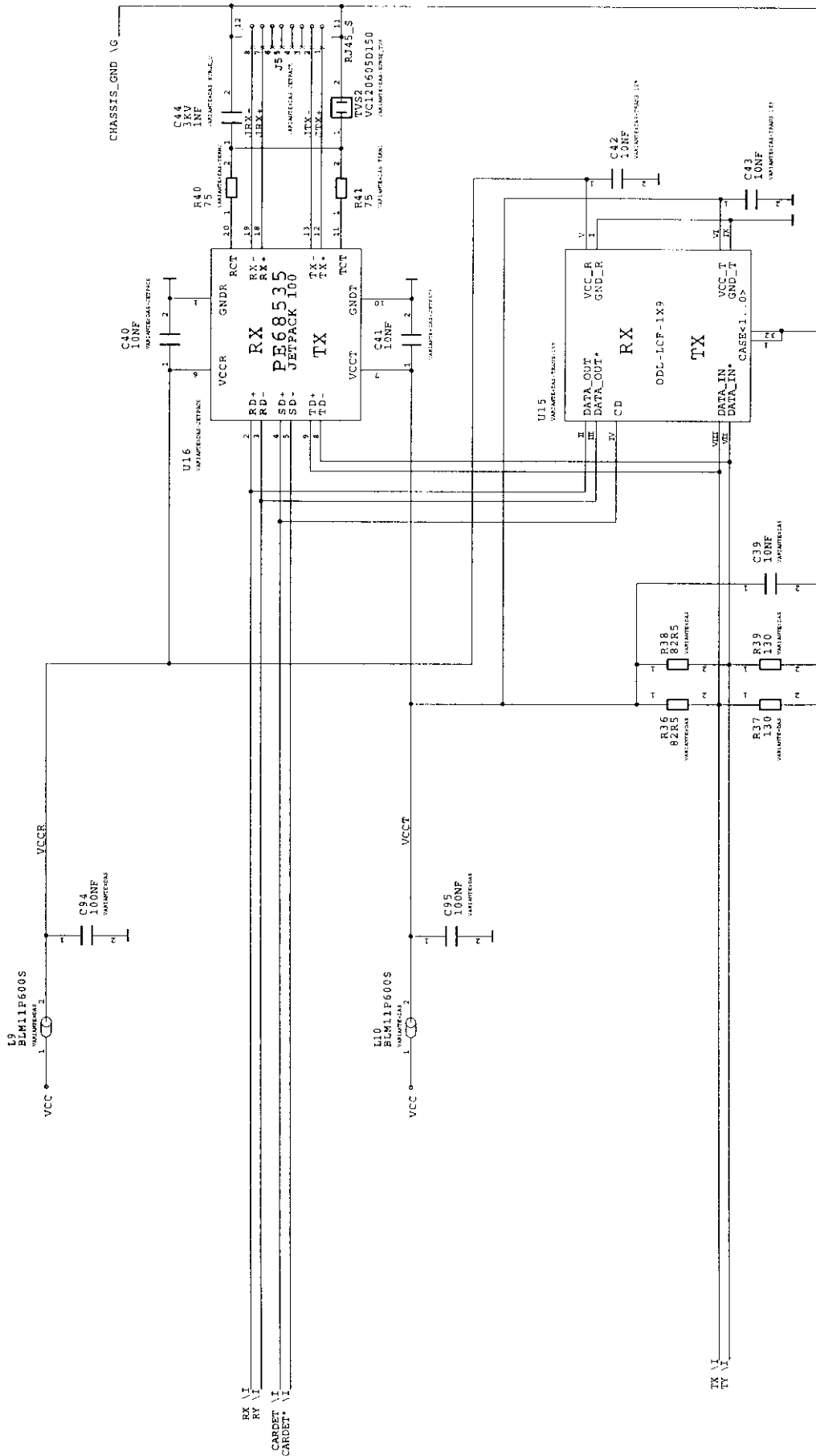
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TITLE: MONALISA

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