

BWS 5800
REV.0A
HARDWARE BLOCK DIAGRAMS

NOTICE OF COPYRIGHT AND PROPRIETARY RIGHTS

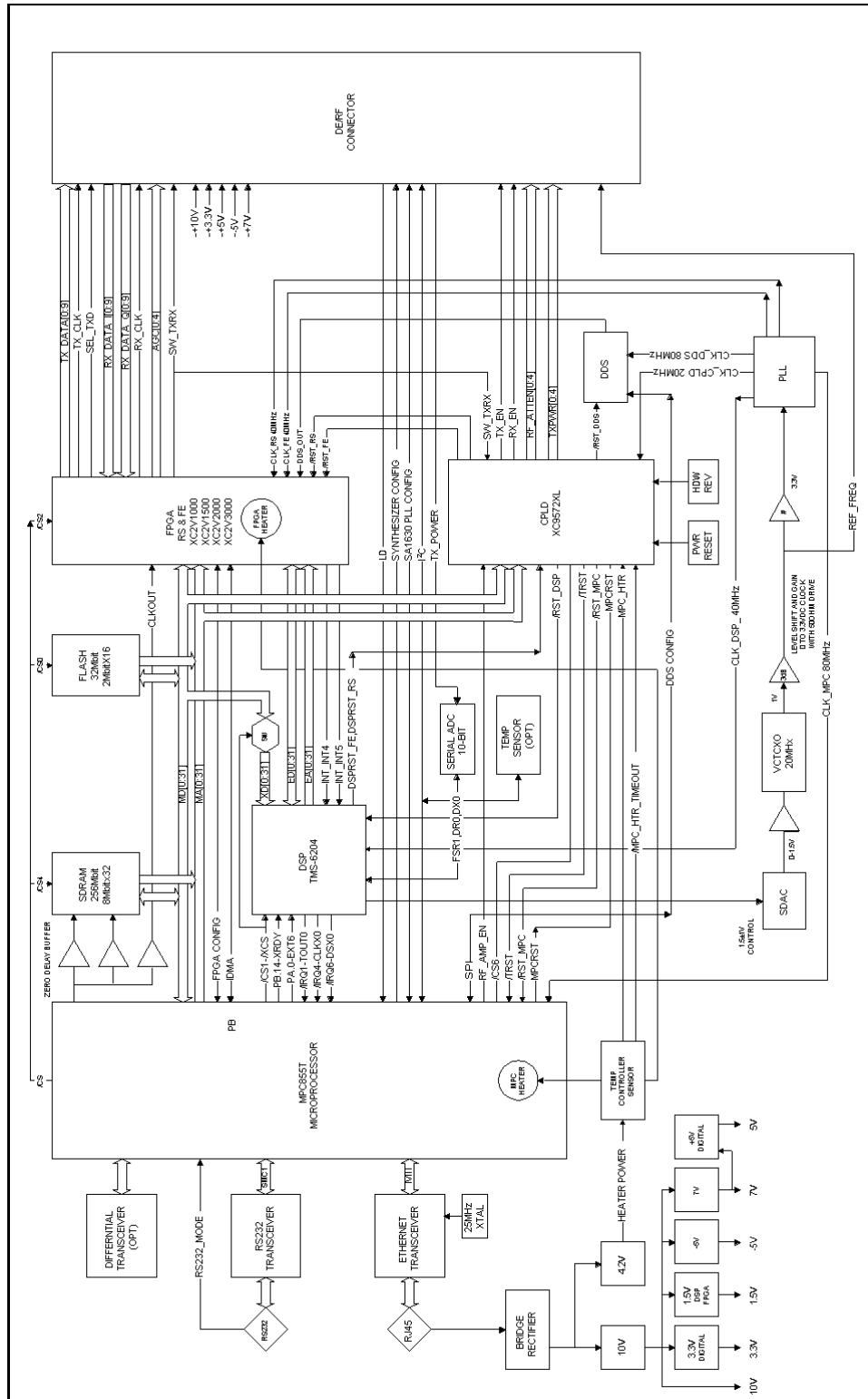
This document contains proprietary information, which is the exclusive property of **Wi-LAN, Inc.** No part of this work may be reproduced or transmitted in any form or by any means, except as permitted by written license agreement with **Wi-LAN, Inc.**

Information in this document is provided *as is* without warranty of any kind, either express or implied, including, but not limited to, the implied warranties of merchantability, fitness for a particular purpose, or non-infringement. In no event will **Wi-LAN, Inc.** be liable to any party for any direct, indirect, special or other consequential damages for any use of this document or any attachment to it, including, without limitation, any lost profits, business interruption, or otherwise, even if **Wi-LAN, Inc.** is expressly advised of the possibility of such damages.

The information contained in this document is subject to change without notice, and does not represent a commitment on the part of **Wi-LAN, Inc.** Although the information and material contained in this document are believed to be accurate and complete, this document and attached material may contain technical inaccuracies or typographical errors.

Any attached system diagrams, hardware schematics, technical descriptions, or software listings are for information purposes only. Reproduction in whole or in part to create working hardware or software is strictly prohibited, except as permitted by written license agreement with **Wi-LAN, Inc.**

Digital Block diagram



RF Block Diagram

