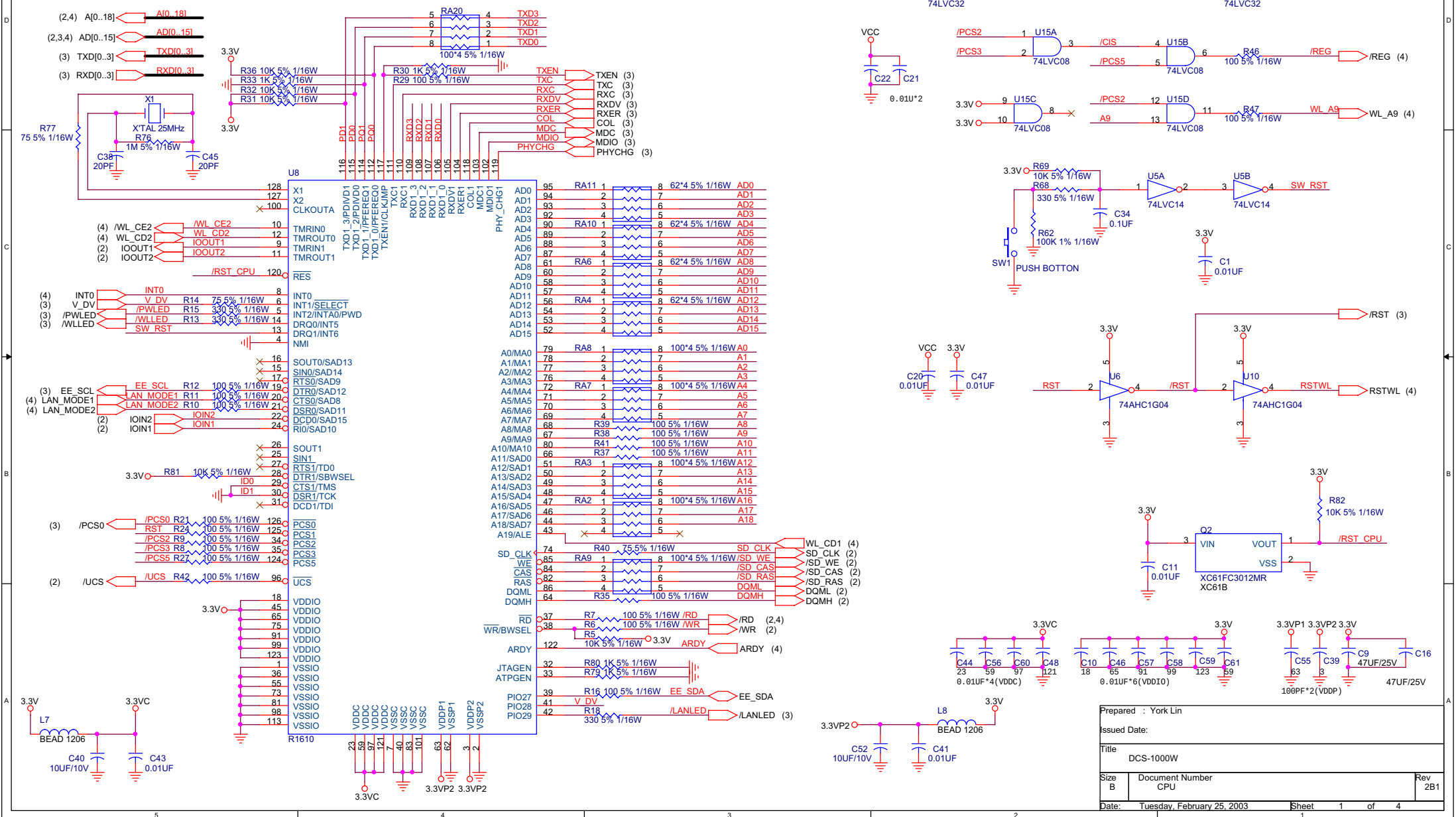
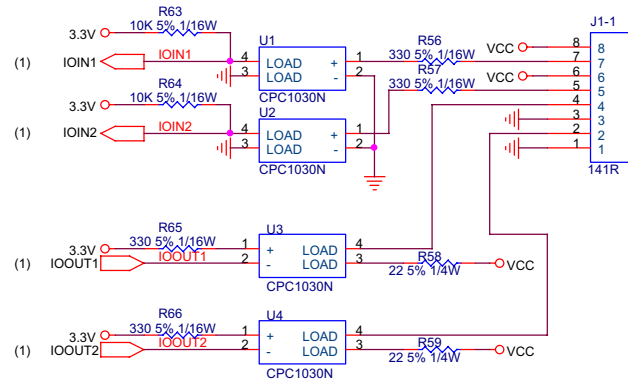
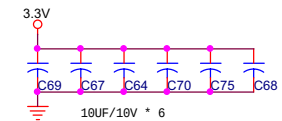
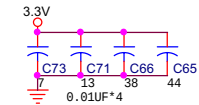
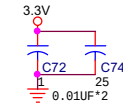
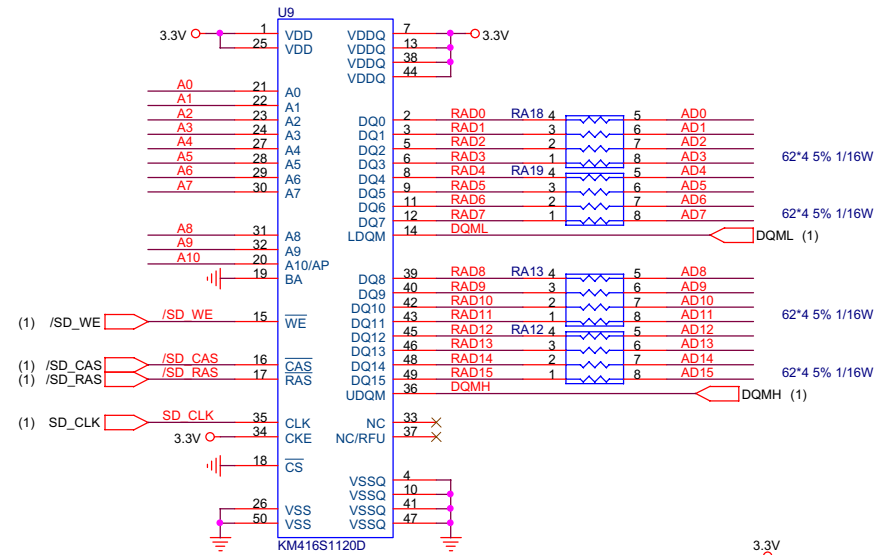
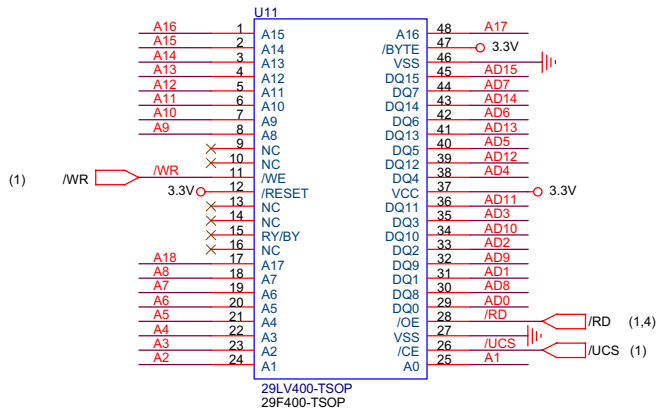


Software Version : Pin 29(U1_CTS) ID0= 0
Pin 30(U1_DSR) ID1= 0
Pin 38(/WR) = PU : ROM 16bit
Pin 32(JTAGEN)=PD : JTAGEN disable
Pin 33(ATPGEN)=PD : Sacn chain disable
Pin 117(TXEN1/CLKJMP)=PD : Clkout = X'tal
25MHZ*4=100MHZ
Pin 116(TXD1_3/PDIVD1) = PU
Pin 115(TXD1_2/PDIVD0) = PU
Pin 114(TXD1_1/PFEREQ1) = PD
Pin 112(TXD1_0/PFEREQ0) = PU
Pin 28(DTR1/SBWSSEL)=PU : UART1 disable

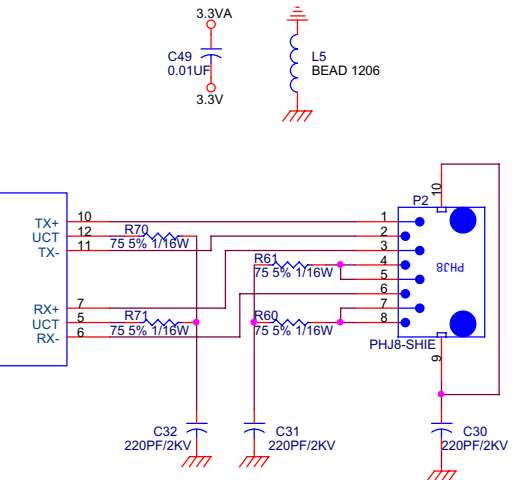


(1,3,4) AD[0..15]  AD[0..15]

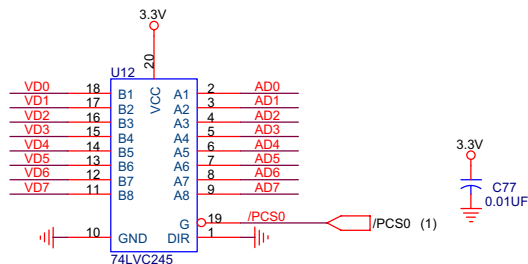
(1,4) A[0..18]  A[0..18]

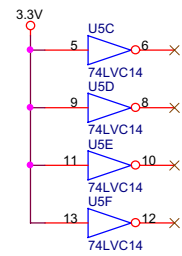
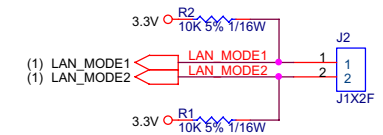
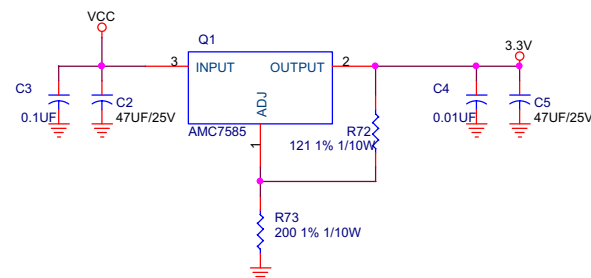
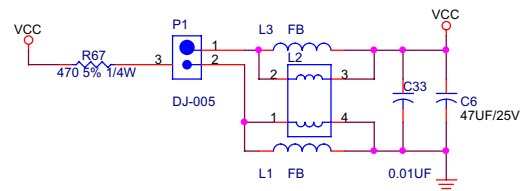
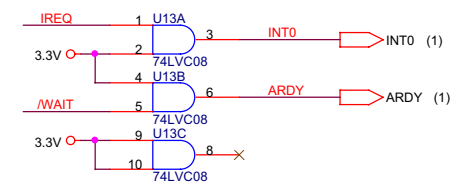
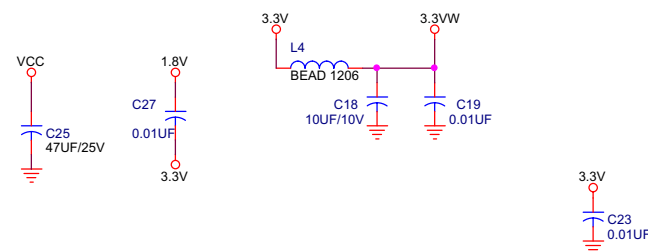
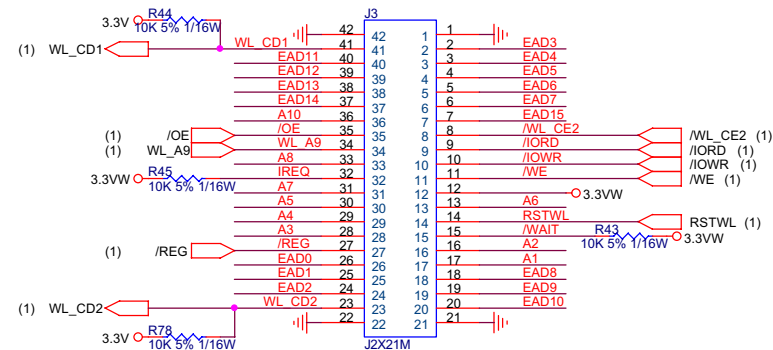
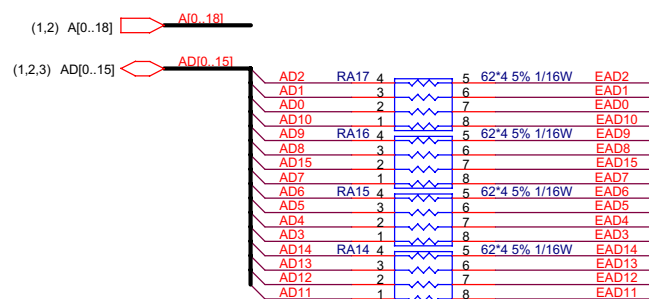


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```
modese[1:0]=10 | Slave I2C mode
SW1 : 1 | Remote wakeup disable
SW0 : 1 | Load TWAIN driver data upload disable
```





Prepared : York Lin			
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Title DCS-1000W			
Size B	Document Number Wireless & power		Rev 281
Date:	Tuesday, February 25, 2003	Sheet	4 of 4