

Appendix B. Test Results_Band n77 (3700-3980)

26dB and 99% Bandwidth

OB_n77_CP_10M_DL656000_Outer Full RB_16QAM



OB_n77_CP_10M_DL656000_Outer Full RB_64QAM



OB_n77_CP_10M_DL656000_Outer Full RB_256QAM



OB_n77_CP_10M_DL656000_Outer Full RB_QPSK



OB_n77_CP_15M_DL656000_Outer Full RB_16QAM



OB_n77_CP_15M_DL656000_Outer Full RB_64QAM



OB_n77_CP_15M_DL656000_Outer Full RB_256QAM



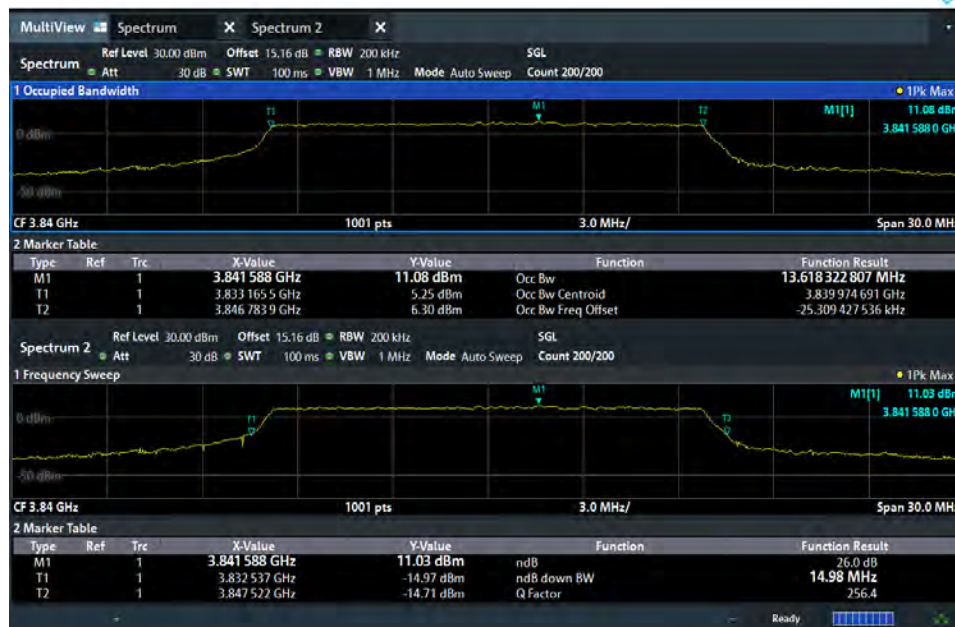
OB_n77_CP_15M_DL656000_Outer Full RB_QPSK



OB_n77_CP_20M_DL656000_Outer Full RB_16QAM



OB_n77_CP_20M_DL656000_Outer Full RB_64QAM



OB_n77_CP_20M_DL656000_Outer Full RB_256QAM



OB_n77_CP_20M_DL656000_Outer Full RB_QPSK



OB_n77_CP_40M_DL656000_Outer Full RB_16QAM



OB_n77_CP_40M_DL656000_Outer Full RB_64QAM



OB_n77_CP_40M_DL656000_Outer Full RB_256QAM



OB_n77_CP_40M_DL656000_Outer Full RB_QPSK



OB_n77_CP_50M_DL656000_Outer Full RB_16QAM



OB_n77_CP_50M_DL656000_Outer Full RB_64QAM



OB_n77_CP_50M_DL656000_Outer Full RB_256QAM



OB_n77_CP_50M_DL656000_Outer Full RB_QPSK



OB_n77_CP_60M_DL656000_Outer Full RB_16QAM



OB_n77_CP_60M_DL656000_Outer Full RB_64QAM



OB_n77_CP_60M_DL656000_Outer Full RB_256QAM



OB_n77_CP_60M_DL656000_Outer Full RB_QPSK



OB_n77_CP_80M_DL656000_Outer Full RB_16QAM



OB_n77_CP_80M_DL656000_Outer Full RB_64QAM



OB_n77_CP_80M_DL656000_Outer Full RB_256QAM



OB_n77_CP_80M_DL656000_Outer Full RB_QPSK



OB_n77_CP_90M_DL656000_Outer Full RB_16QAM



OB_n77_CP_90M_DL656000_Outer Full RB_64QAM



OB_n77_CP_90M_DL656000_Outer Full RB_256QAM



OB_n77_CP_90M_DL656000_Outer Full RB_QPSK



OB_n77_CP_100M_DL656000_Outer Full RB_16QAM



OB_n77_CP_100M_DL656000_Outer Full RB_64QAM



OB_n77_CP_100M_DL656000_Outer Full RB_256QAM



OB_n77_CP_100M_DL656000_Outer Full RB_QPSK



OB_n77_DFT_10M_DL656000_Outer Full RB_BPSK



OB_n77_DFT_15M_DL656000_Outer Full RB_BPSK



OB_n77_DFT_20M_DL656000_Outer Full RB_BPSK



OB_n77_DFT_40M_DL656000_Outer Full RB_BPSK



OB_n77_DFT_50M_DL656000_Outer Full RB_BPSK



OB_n77_DFT_60M_DL656000_Outer Full RB_BPSK



OB_n77_DFT_80M_DL656000_Outer Full RB_BPSK



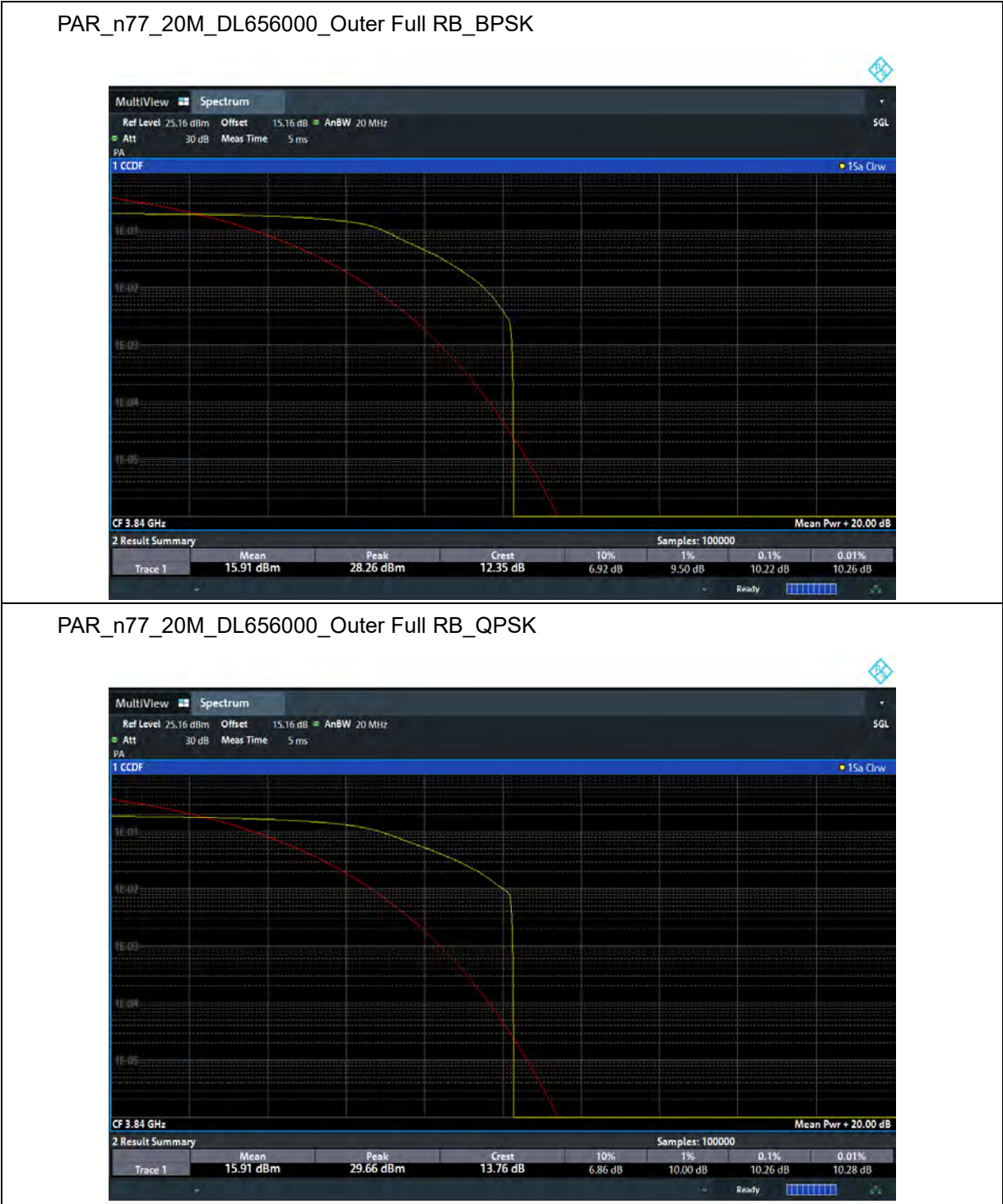
OB_n77_DFT_90M_DL656000_Outer Full RB_BPSK



OB_n77_DFT_100M_DL656000_Outer Full RB_BPSK

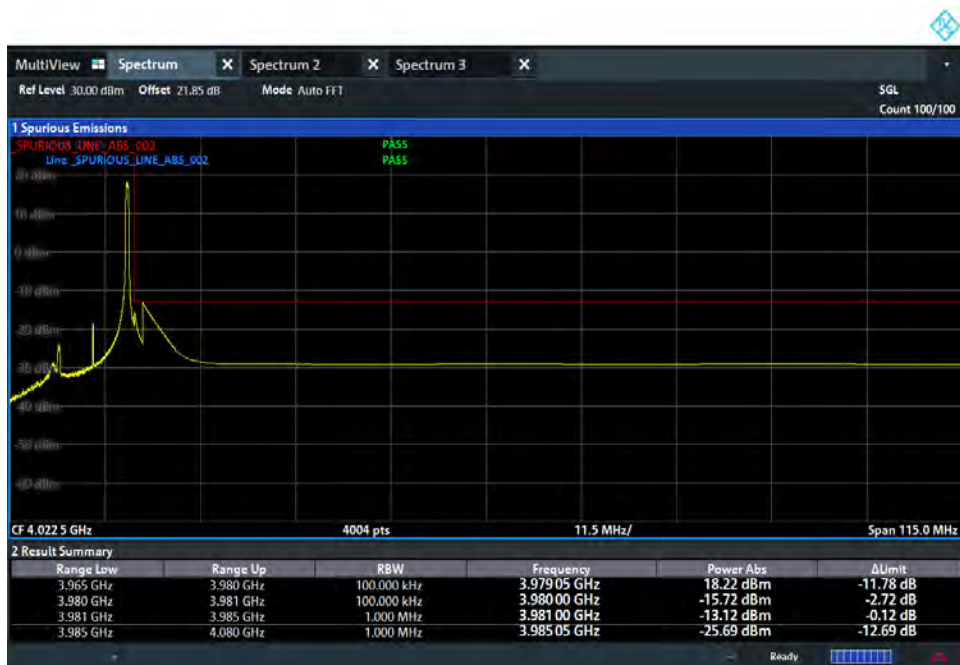


Peak to Average Power Ratio



Band Edge

BE_n77_10M_High CH_DL665000_3975MHz_CP_QPSK_1RB Right



BE_n77_10M_High CH_DL665000_3975MHz_CP_QPSK_Full RB



BE_n77_10M_High CH_DL665000_3975MHz_DFT_BPSK_1RB Right



BE_n77_10M_High CH_DL665000_3975MHz_DFT_BPSK_Full RB



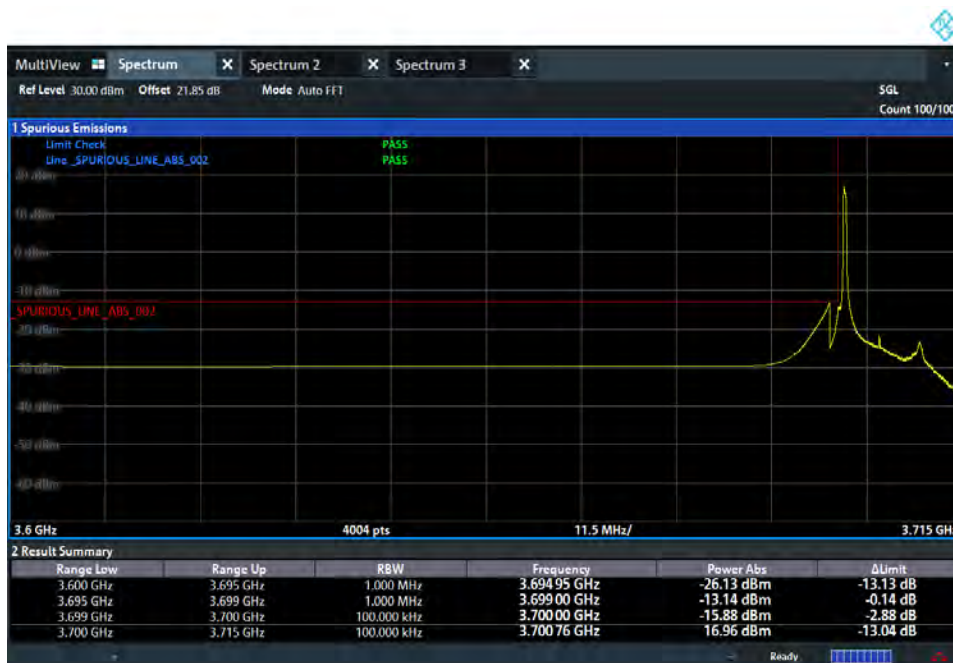
BE_n77_10M_High CH_DL665000_3975MHz_DFT_QPSK_1RB Right



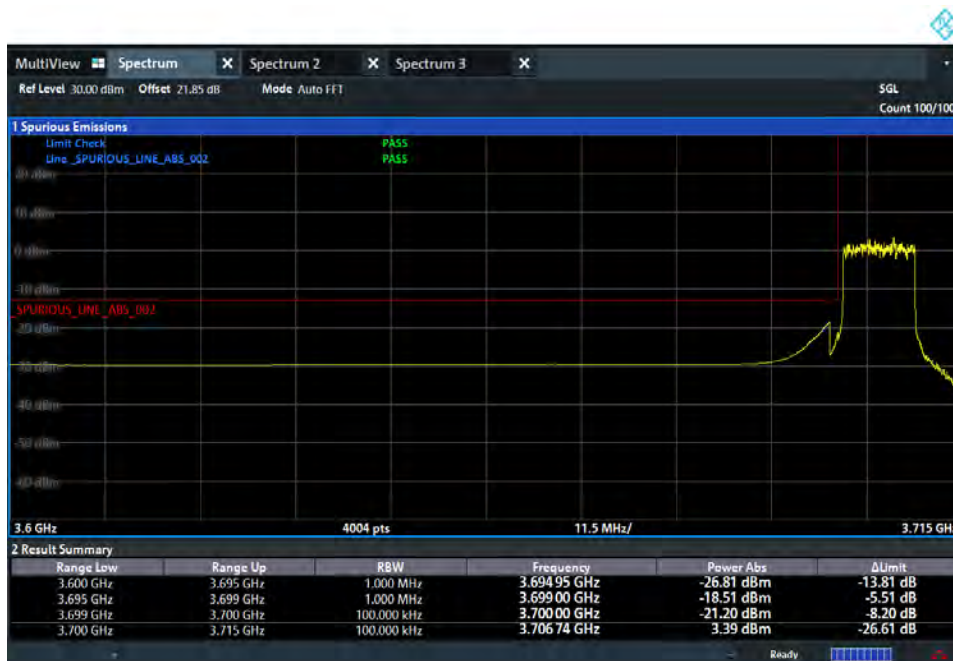
BE_n77_10M_High CH_DL665000_3975MHz_DFT_QPSK_Full RB



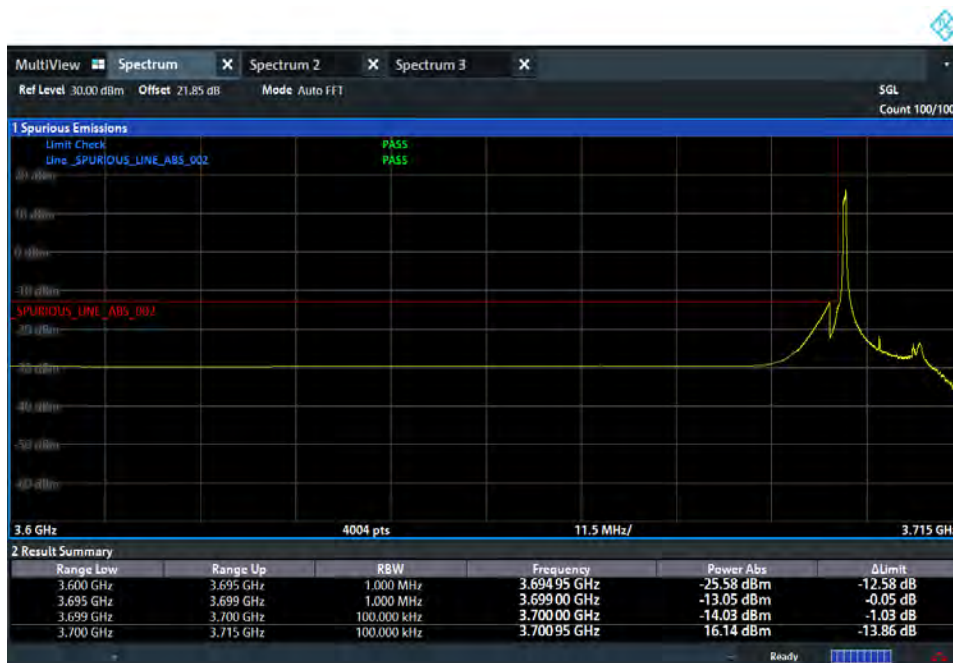
BE_n77_10M_Low CH_DL647000_3705MHz_CP_QPSK_1RB Left



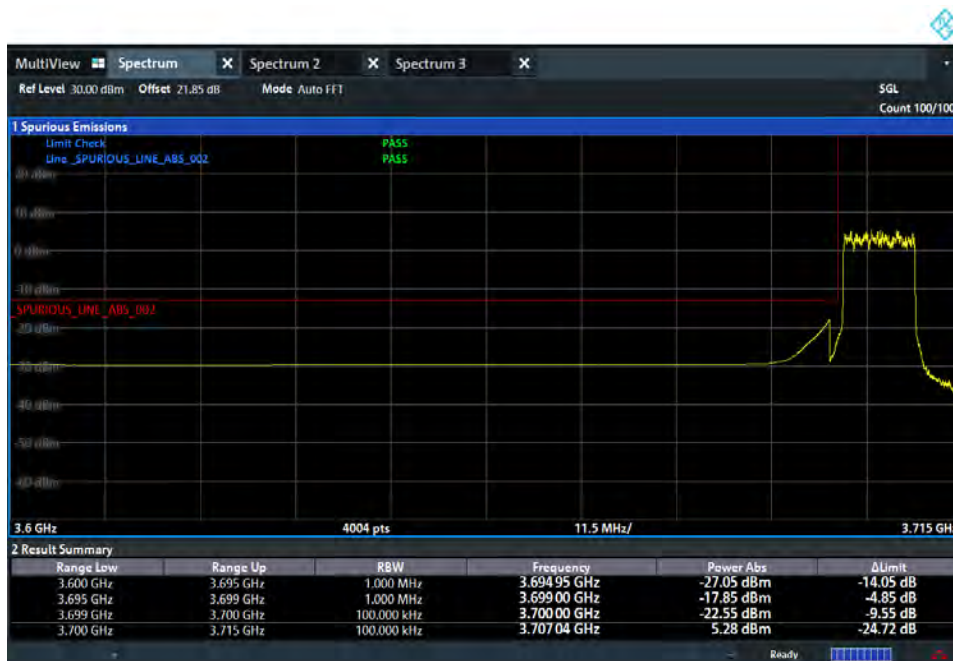
BE_n77_10M_Low CH_DL647000_3705MHz_CP_QPSK_Full RB



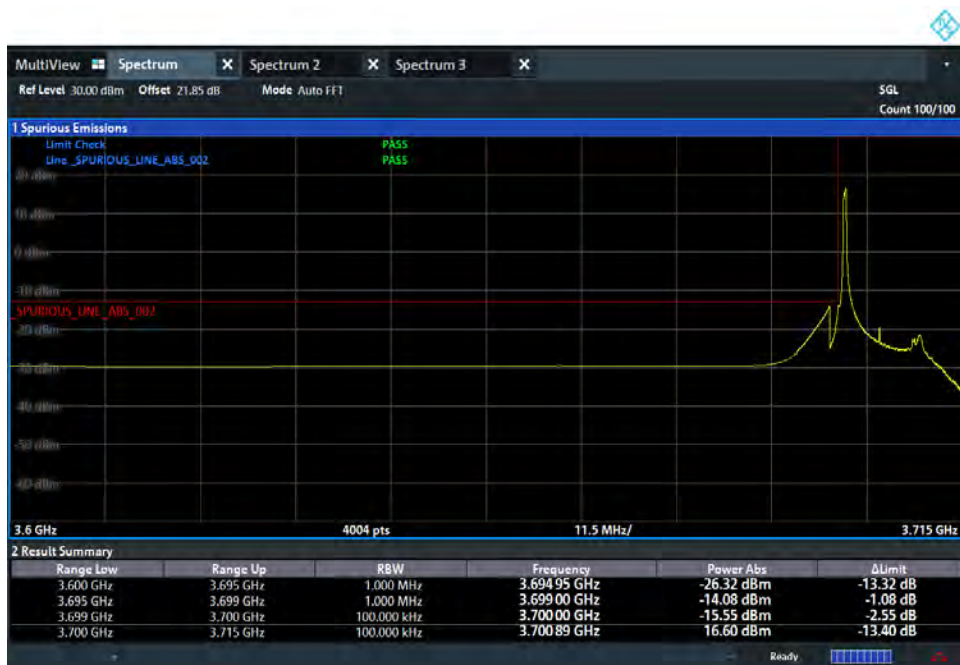
BE_n77_10M_Low CH_DL647000_3705MHz_DFT_BPSK_1RB Left



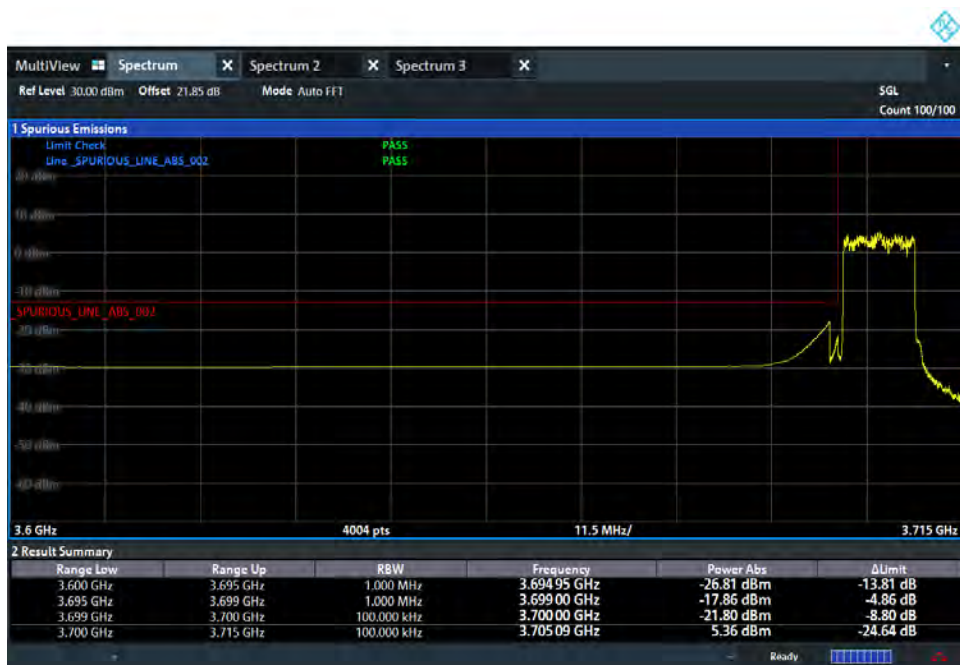
BE_n77_10M_Low CH_DL647000_3705MHz_DFT_BPSK_Full RB



BE_n77_10M_Low CH_DL647000_3705MHz_DFT_QPSK_1RB Left



BE_n77_10M_Low CH_DL647000_3705MHz_DFT_QPSK_Full RB



BE_n77_10M_Low CH_DL647000_3705MHz_DFT_QPSK_Full RB_Duty cycle



BE_n77_60M_High CH_DL663332_3949.98MHz_CP_QPSK_1RB Right



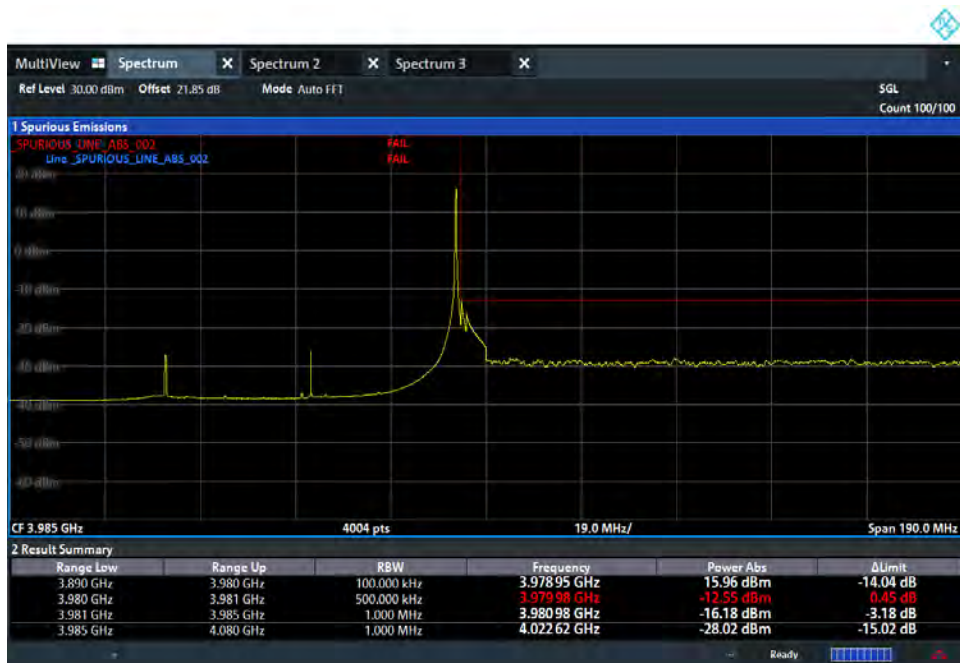
BE_n77_60M_High CH_DL663332_3949.98MHz_CP_QPSK_1RB Right_verify



BE_n77_60M_High CH_DL663332_3949.98MHz_CP_QPSK_Full RB



BE_n77_60M_High CH_DL663332_3949.98MHz_DFT_BPSK_1RB Right



BE_n77_60M_High CH_DL663332_3949.98MHz_DFT_BPSK_1RB Right_verify



BE_n77_60M_High CH_DL663332_3949.98MHz_DFT_BPSK_Full RB



BE_n77_60M_High CH_DL663332_3949.98MHz_DFT_QPSK_1RB Right



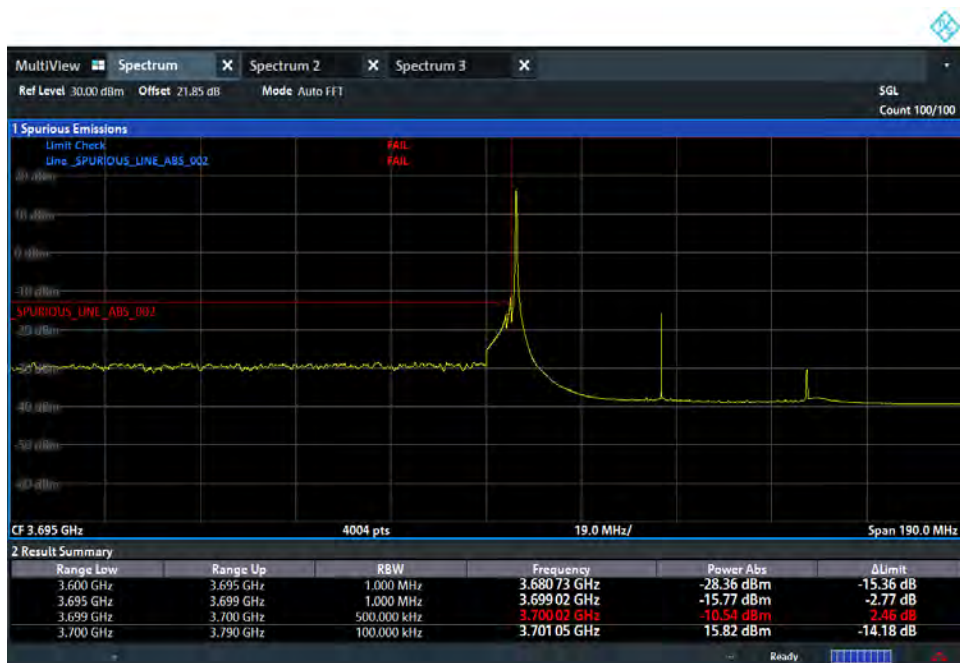
BE_n77_60M_High CH_DL663332_3949.98MHz_DFT_QPSK_1RB Right_verify



BE_n77_60M_High CH_DL663332_3949.98MHz_DFT_QPSK_Full RB



BE_n77_60M_Low CH_DL648668_3730.02MHz_CP_QPSK_1RB Left



BE_n77_60M_Low CH_DL648668_3730.02MHz_CP_QPSK_1RB Left_verify



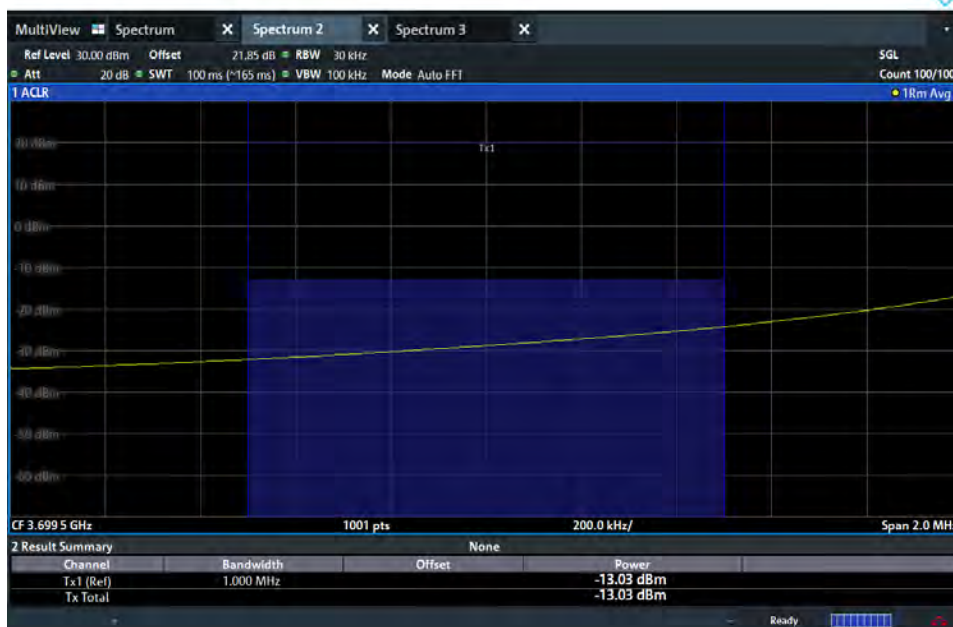
BE_n77_60M_Low CH_DL648668_3730.02MHz_CP_QPSK_Full RB



BE_n77_60M_Low CH_DL648668_3730.02MHz_DFT_BPSK_1RB Left



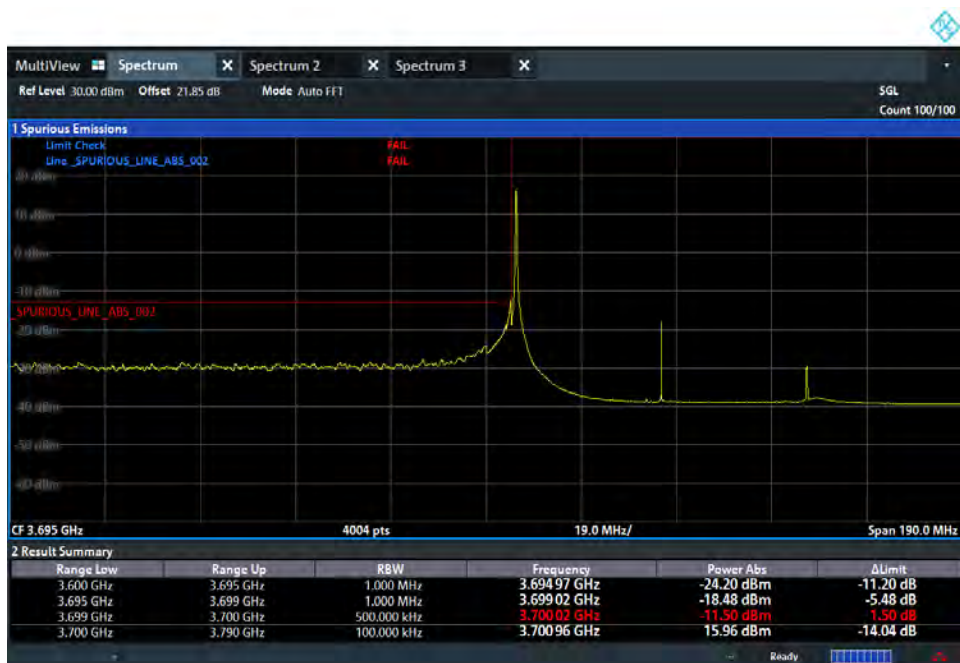
BE_n77_60M_Low CH_DL648668_3730.02MHz_DFT_BPSK_1RB Left_verify



BE_n77_60M_Low CH_DL648668_3730.02MHz_DFT_BPSK_Full RB



BE_n77_60M_Low CH_DL648668_3730.02MHz_DFT_QPSK_1RB Left



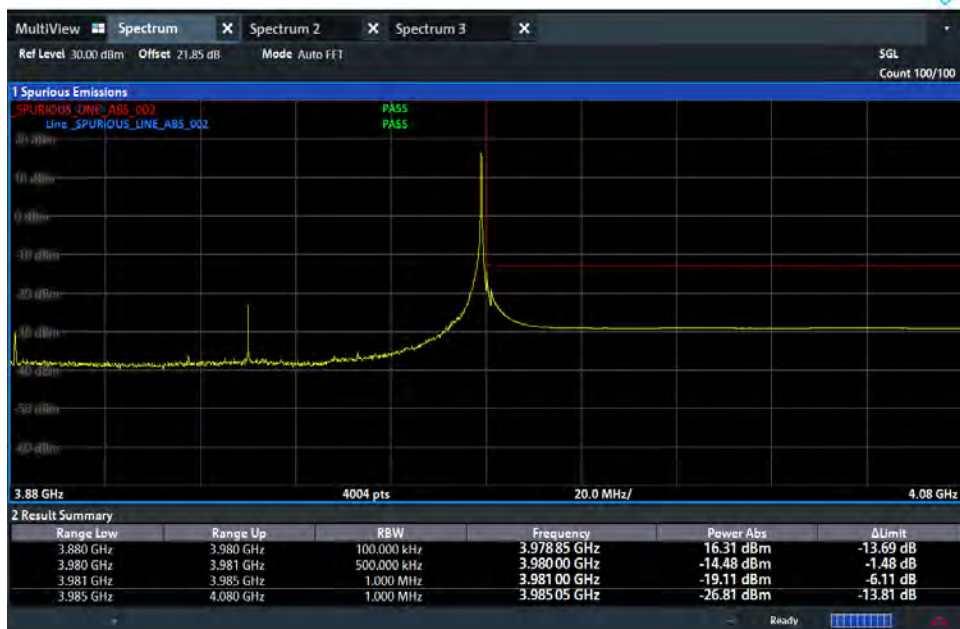
BE_n77_60M_Low CH_DL648668_3730.02MHz_DFT_QPSK_1RB Left_verify



BE_n77_60M_Low CH_DL648668_3730.02MHz_DFT_QPSK_Full RB



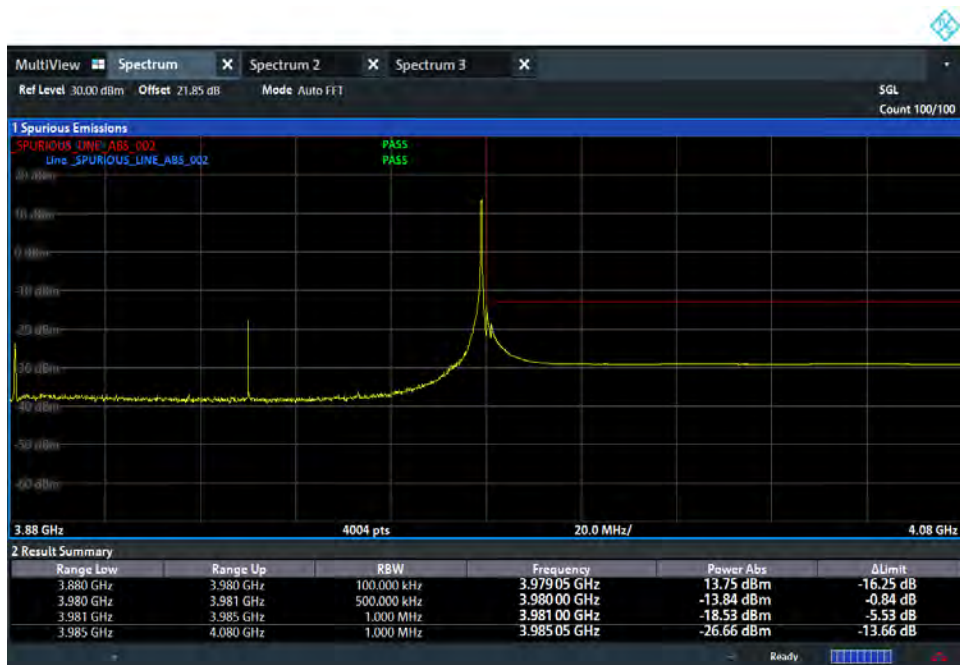
BE_n77_100M_High CH_DL662000_3930MHz_CP_QPSK_1RB Right



BE_n77_100M_High CH_DL662000_3930MHz_CP_QPSK_Full RB



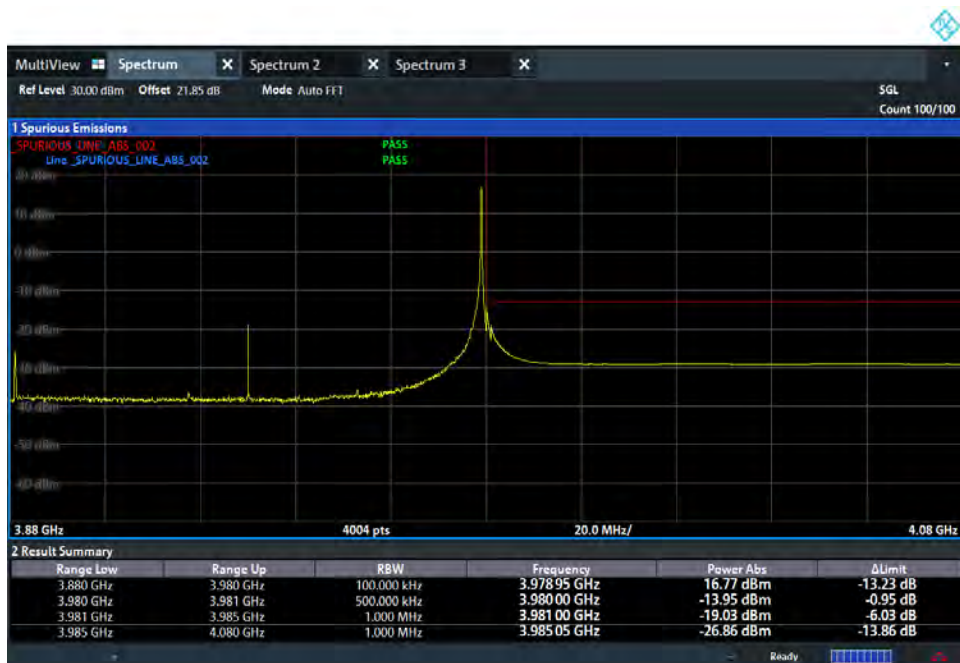
BE_n77_100M_High CH_DL662000_3930MHz_DFT_BPSK_1RB Right



BE_n77_100M_High CH_DL662000_3930MHz_DFT_BPSK_Full RB



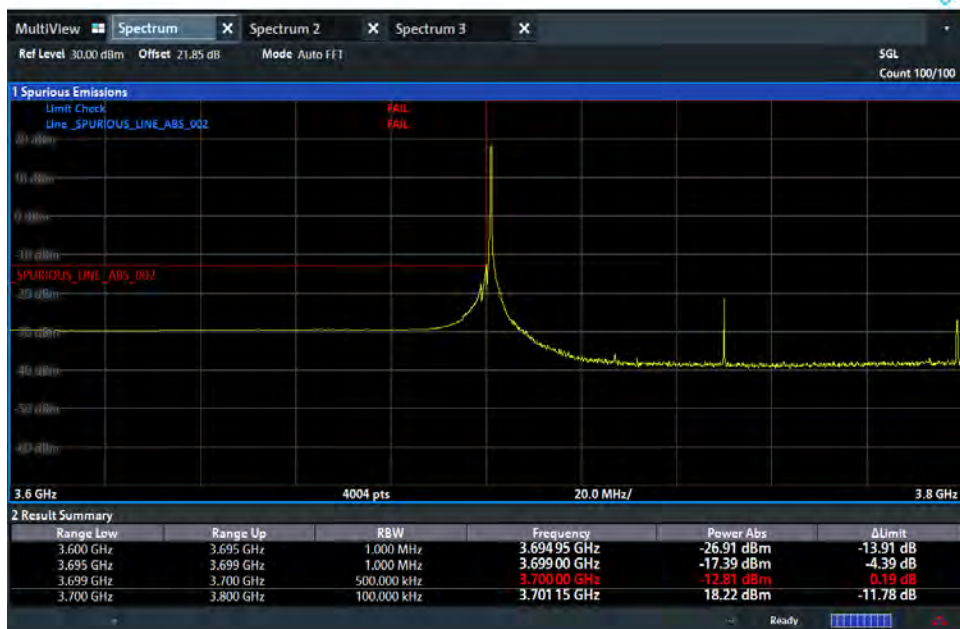
BE_n77_100M_High CH_DL662000_3930MHz_DFT_QPSK_1RB Right



BE_n77_100M_High CH_DL662000_3930MHz_DFT_QPSK_Full RB



BE_n77_100M_Low CH_DL650000_3750MHz_CP_QPSK_1RB Left



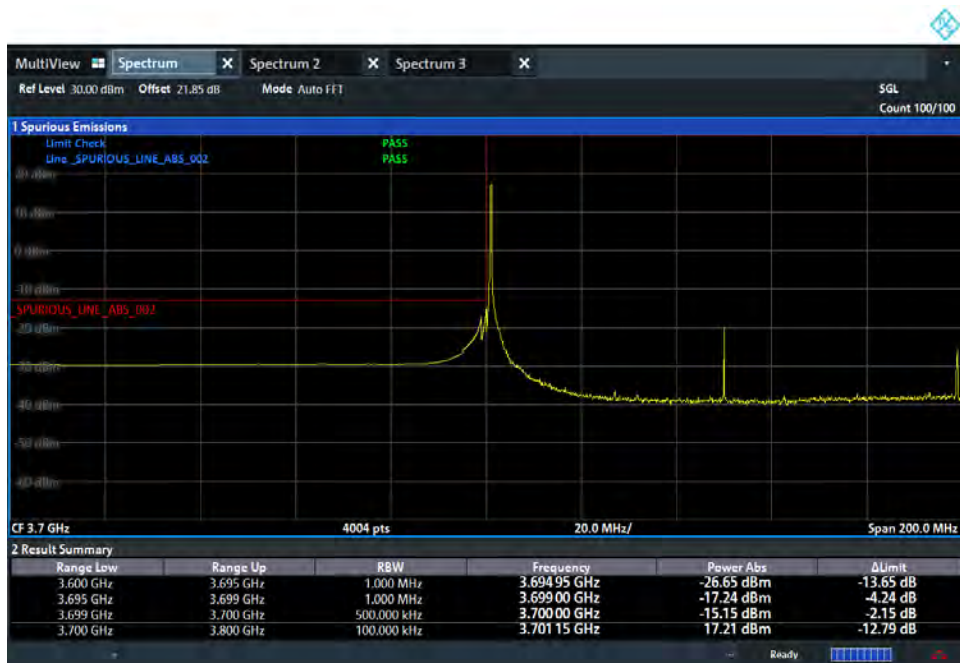
BE_n77_100M_Low CH_DL650000_3750MHz_CP_QPSK_1RB Left_verify



BE_n77_100M_Low CH_DL650000_3750MHz_CP_QPSK_Full RB



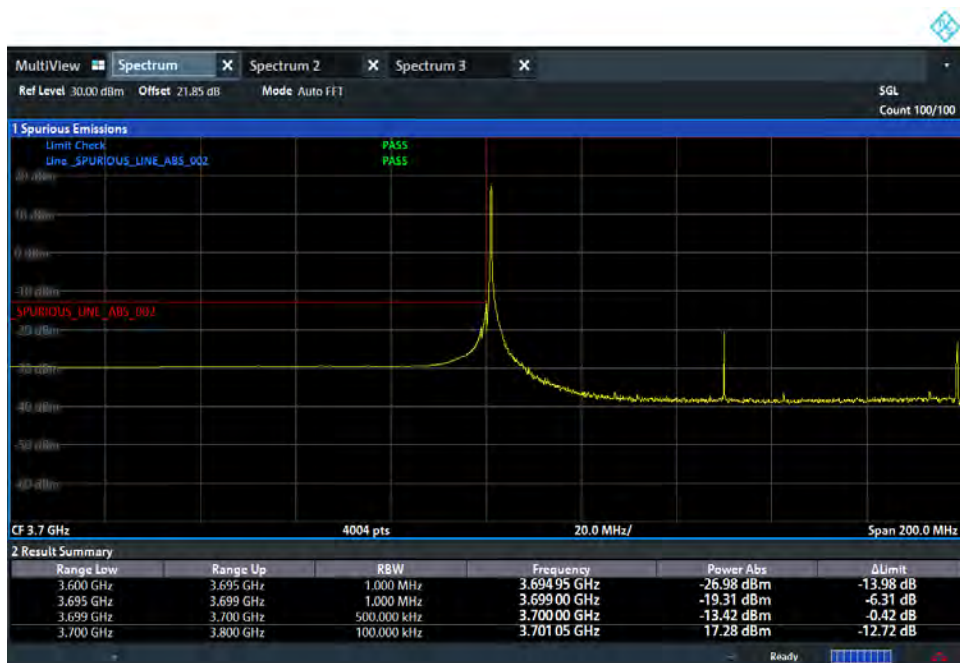
BE_n77_100M_Low CH_DL650000_3750MHz_DFT_BPSK_1RB Left



BE_n77_100M_Low CH_DL650000_3750MHz_DFT_BPSK_Full RB



BE_n77_100M_Low CH_DL650000_3750MHz_DFT_QPSK_1RB Left



BE_n77_100M_Low CH_DL650000_3750MHz_DFT_QPSK_Full RB



Conducted Spurious Emissions

CSE_n77_10M_High CH_DL665000_3975MHz_DFT_BPSK_1@1



CSE_n77_10M_High CH_DL665000_3975MHz_DFT_BPSK_1@22



CSE_n77_10M_High CH_DL665000_3975MHz_DFT_QPSK_1@1



CSE_n77_10M_High CH_DL665000_3975MHz_DFT_QPSK_1@22



CSE_n77_10M_Low CH_DL647000_3705MHz_DFT_BPSK_1@1



CSE_n77_10M_Low CH_DL647000_3705MHz_DFT_BPSK_1@22



CSE_n77_10M_Low CH_DL647000_3705MHz_DFT_QPSK_1@1



CSE_n77_10M_Low CH_DL647000_3705MHz_DFT_QPSK_1@22



CSE_n77_10M_Mid CH_DL656000_3840MHz_DFT_BPSK_1@1



CSE_n77_10M_Mid CH_DL656000_3840MHz_DFT_BPSK_1@22



CSE_n77_10M_Mid CH_DL656000_3840MHz_DFT_QPSK_1@1



CSE_n77_10M_Mid CH_DL656000_3840MHz_DFT_QPSK_1@22



CSE_n77_60M_High CH_DL663332_3949.98MHz_DFT_BPSK_1@1



CSE_n77_60M_High CH_DL663332_3949.98MHz_DFT_BPSK_1@160



CSE_n77_60M_High CH_DL663332_3949.98MHz_DFT_QPSK_1@1



CSE_n77_60M_High CH_DL663332_3949.98MHz_DFT_QPSK_1@160



CSE_n77_60M_Low CH_DL648668_3730.02MHz_DFT_BPSK_1@1



CSE_n77_60M_Low CH_DL648668_3730.02MHz_DFT_BPSK_1@160



CSE_n77_60M_Low CH_DL648668_3730.02MHz_DFT_QPSK_1@1



CSE_n77_60M_Low CH_DL648668_3730.02MHz_DFT_QPSK_1@160



CSE_n77_60M_Mid CH_DL656000_3840MHz_DFT_BPSK_1@1



CSE_n77_60M_Mid CH_DL656000_3840MHz_DFT_BPSK_1@160



CSE_n77_60M_Mid CH_DL656000_3840MHz_DFT_QPSK_1@1



CSE_n77_60M_Mid CH_DL656000_3840MHz_DFT_QPSK_1@160



CSE_n77_100M_High CH_DL662000_3930MHz_DFT_BPSK_1@1



CSE_n77_100M_High CH_DL662000_3930MHz_DFT_BPSK_1@271



CSE_n77_100M_High CH_DL662000_3930MHz_DFT_QPSK_1@1



CSE_n77_100M_High CH_DL662000_3930MHz_DFT_QPSK_1@271



CSE_n77_100M_Low CH_DL650000_3750MHz_DFT_BPSK_1@1



CSE_n77_100M_Low CH_DL650000_3750MHz_DFT_BPSK_1@271



CSE_n77_100M_Low CH_DL650000_3750MHz_DFT_QPSK_1@1



CSE_n77_100M_Low CH_DL650000_3750MHz_DFT_QPSK_1@271



CSE_n77_100M_Mid CH_DL656000_3840MHz_DFT_BPSK_1@1



CSE_n77_100M_Mid CH_DL656000_3840MHz_DFT_BPSK_1@271



CSE_n77_100M_Mid CH_DL656000_3840MHz_DFT_QPSK_1@1



CSE_n77_100M_Mid CH_DL656000_3840MHz_DFT_QPSK_1@271

