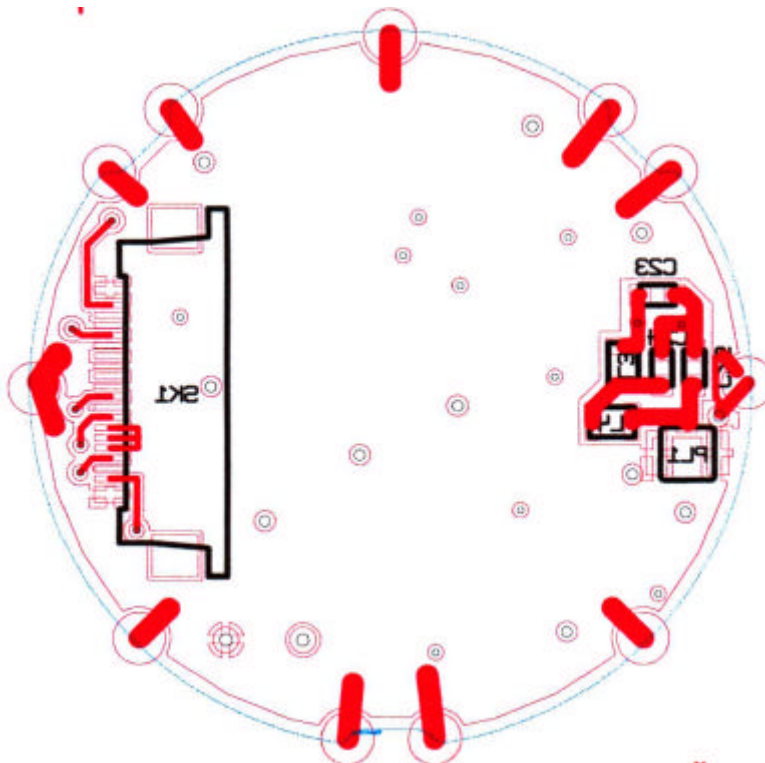
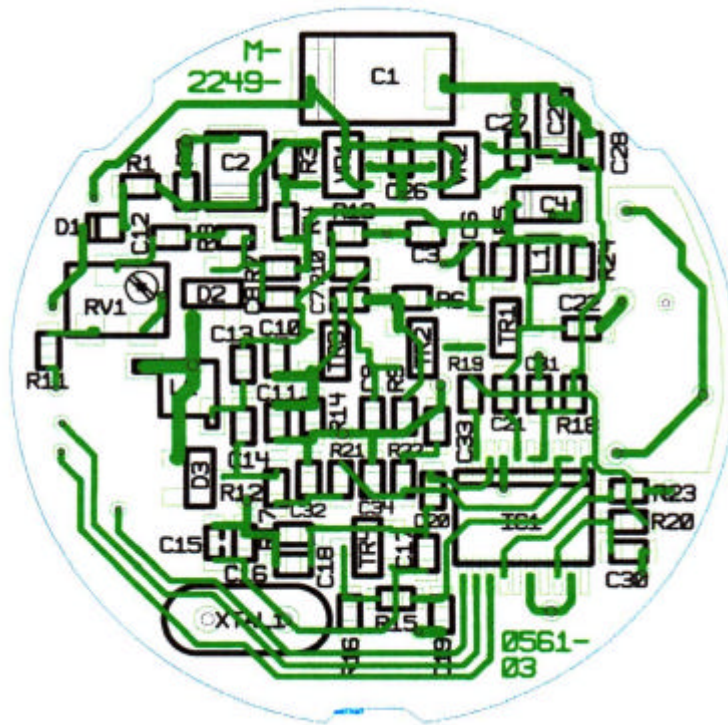
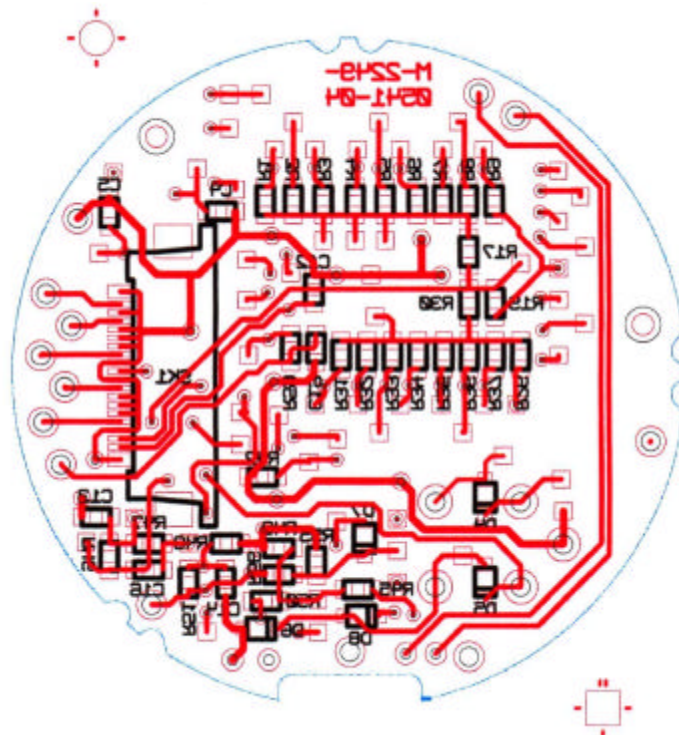
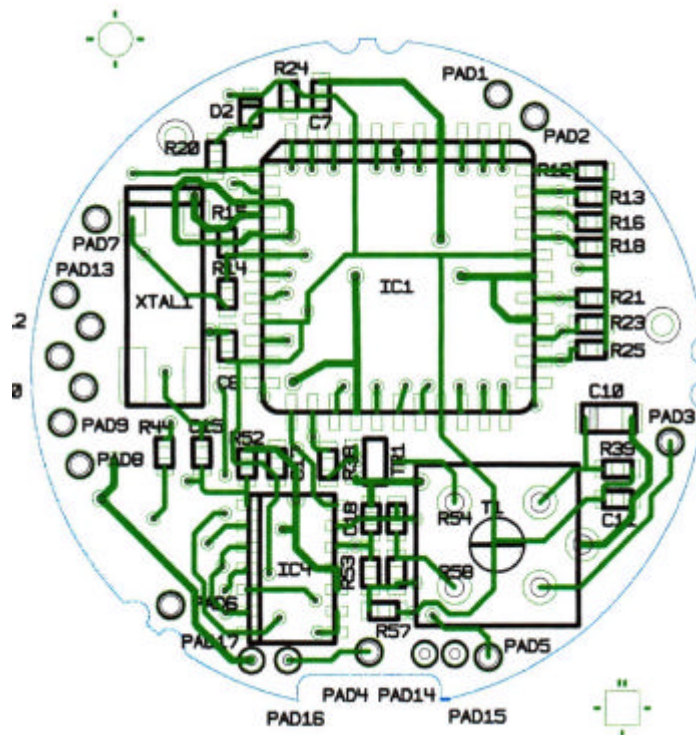


RMP3 LAYOUT



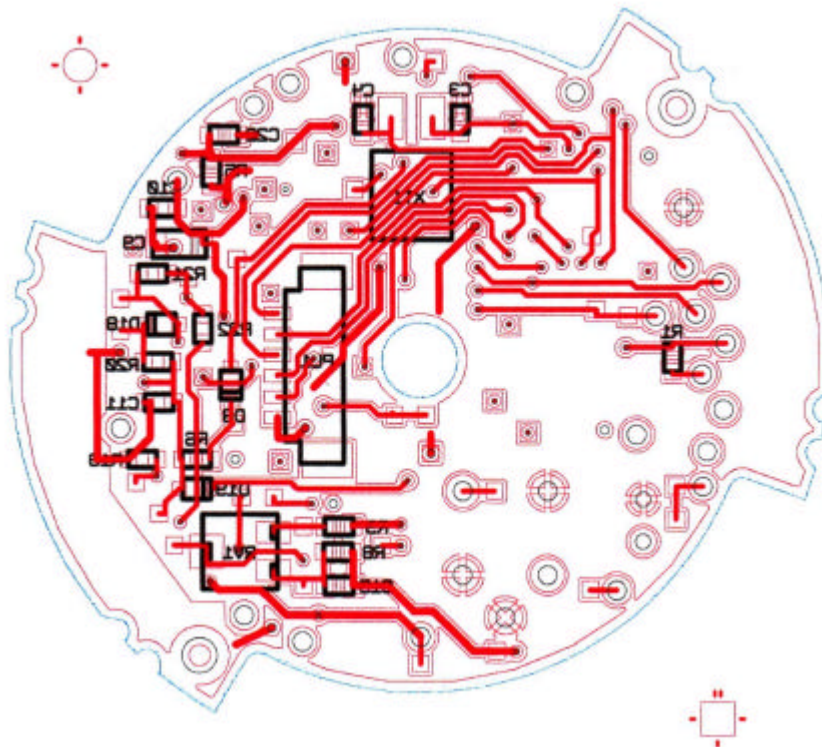
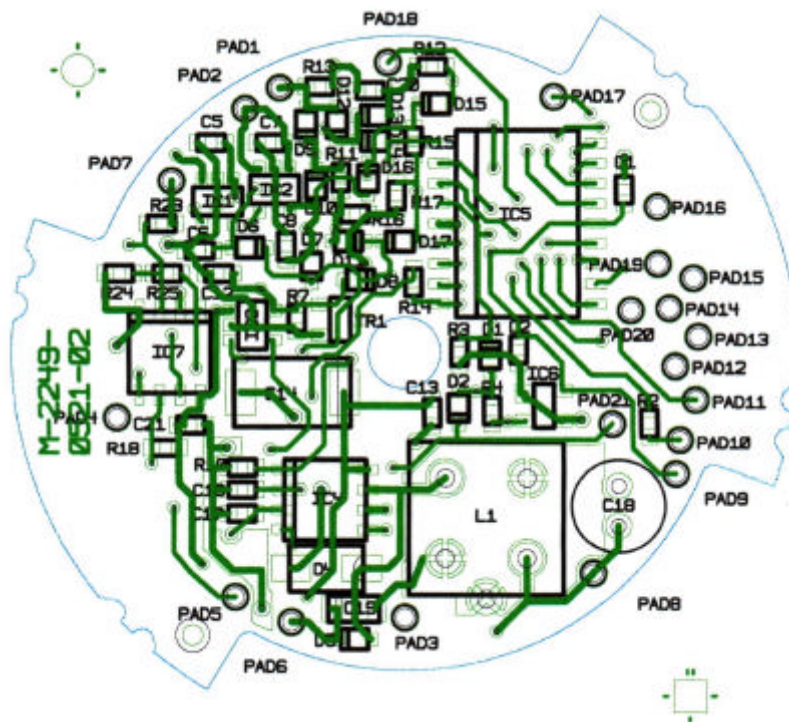
Title: Tx 224 MHz PWB top and bottom

RMP3 LAYOUT

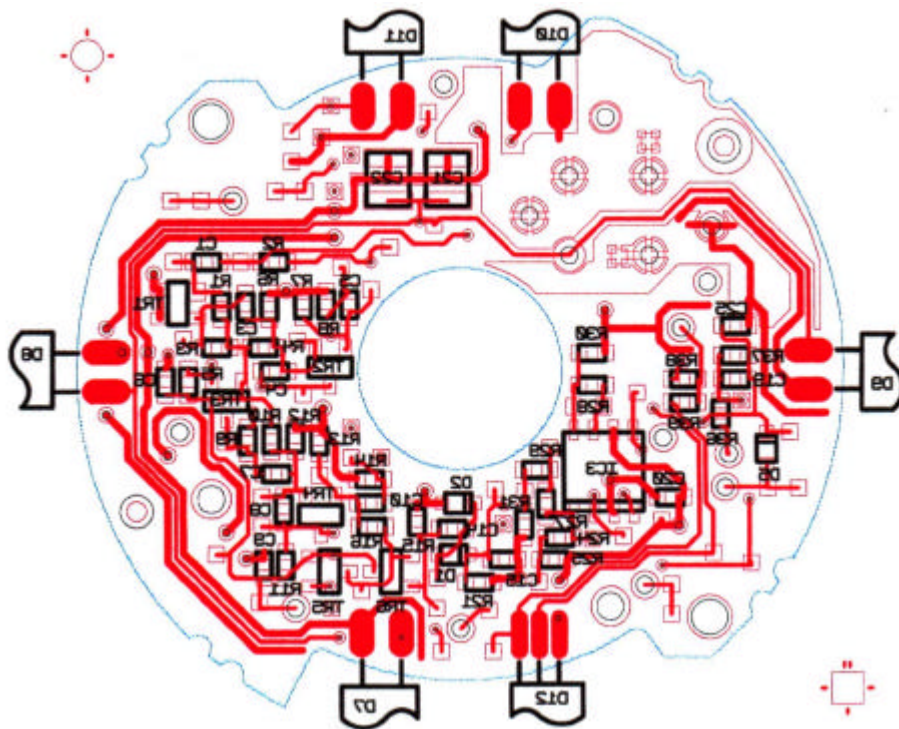


Title: FPGA PWB top and bottom

RMP3 LAYOUT



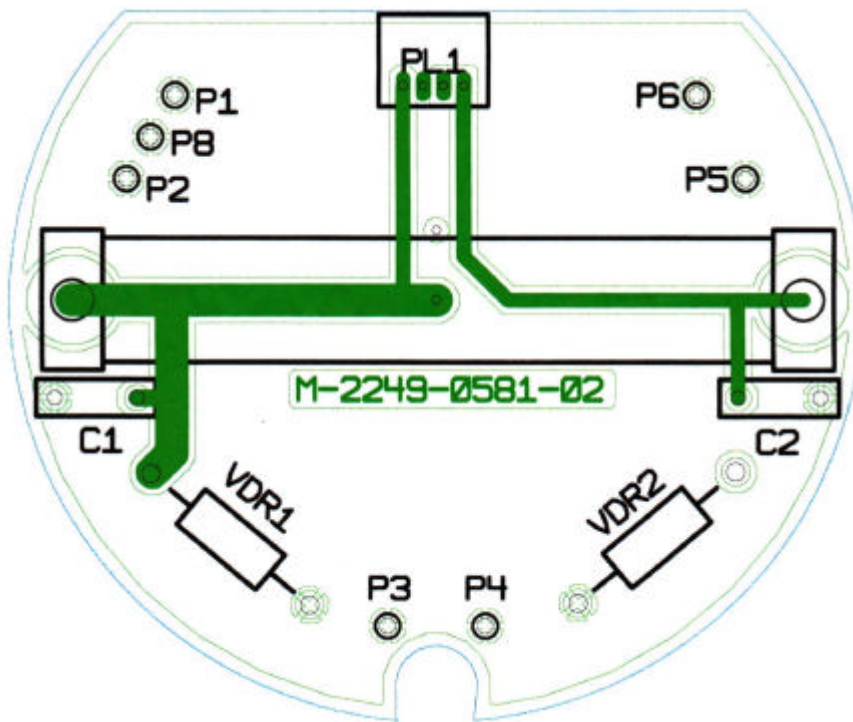
Title: Power/Pic PWB top and bottom



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RMP3 LAYOUT



Title: Centrifugal switch PWB top and bottom