

1. When the user take a snapshot by pressing the button, the 8051 programs the CMOS sensor to start writing pixel data to the SRAM.
2. The 8051 also enable the JPEG controller to take 8x8 blocks for encoding once the SRAM has 16 lines of raw data for processing.
3. The 8051 disables the CMOS sensor once a frame has been read from the CMOS sensor.
4. The 8051 polls to check whether JPEG encoding has completed.
5. Once completed, it programs the lynx2 chip so that 8051 can read the SRAM data and store them to external flash memory.

