
INTERTEK TESTING SERVICES

EXHIBIT 6

TECHNICAL SPECIFICATIONS

INTERTEK TESTING SERVICES

6.0 Technical Specifications

The block diagram and schematic of the Superregenerative Receiver are attached.

INTERTEK TESTING SERVICES

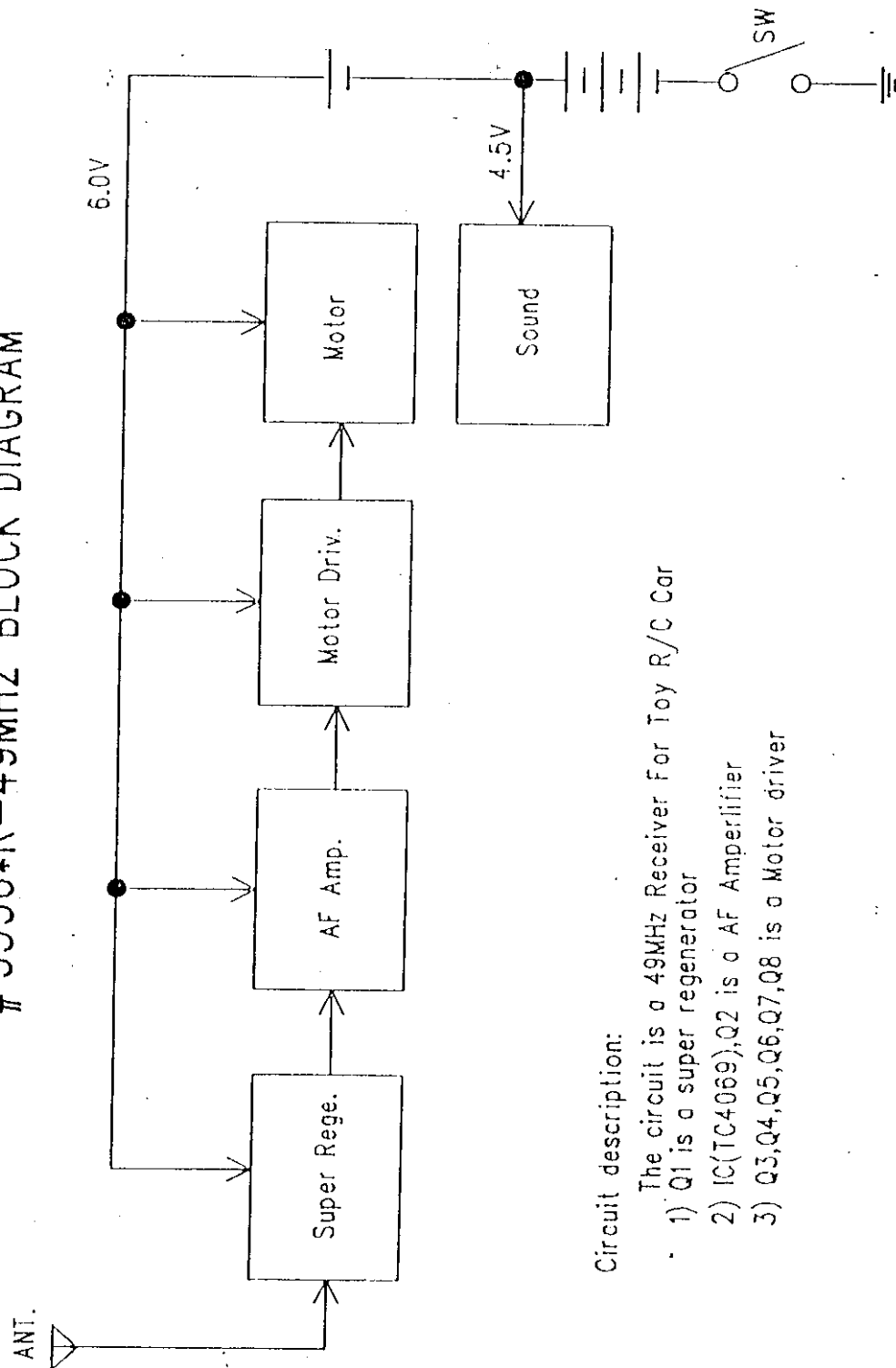
6.1 Receiver Block Diagram

Figure 6.1 Superregenerative Receiver Block Diagram

Y2

FAX OUT NO: 98021617

3536*R-49MHz BLOCK DIAGRAM



Circuit description:

The circuit is a 49MHz Receiver For Toy R/C Car

- 1) Q1 is a super regenerator
- 2) IC(TC4069), Q2 is a AF Amplifier
- 3) Q3, Q4, Q5, Q6, Q7, Q8 is a Motor driver