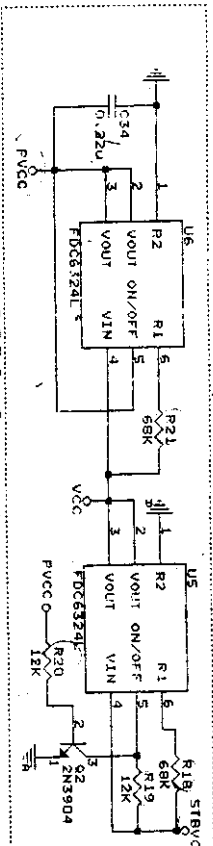


EXHIBIT D

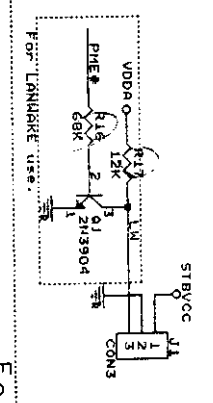
Circuit Diagram

[illegible]

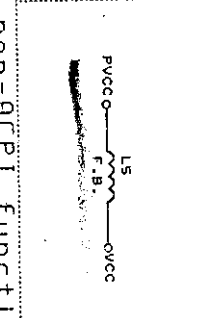
CIS TECHNOLOGY INC. FILE : MDL SCH ENGINEER : RAY CHEN P/N :		REV	
Title		ROOT	
STATE Document Number B : CIS-M400-P-CI-Car-d-VD Date : June 8, 1998 Sheet		U	



For ACPI function use.

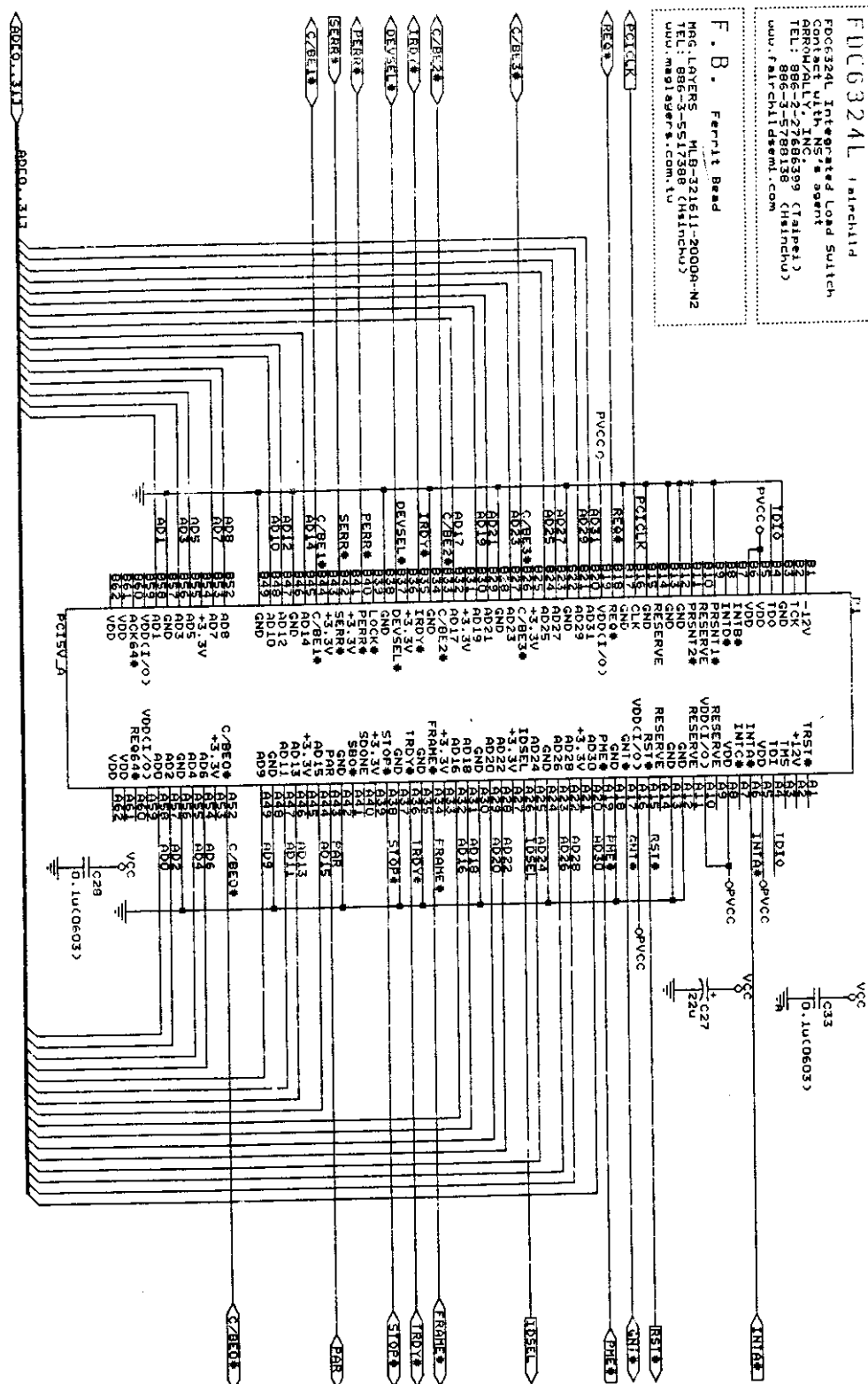


For LANMARK use.



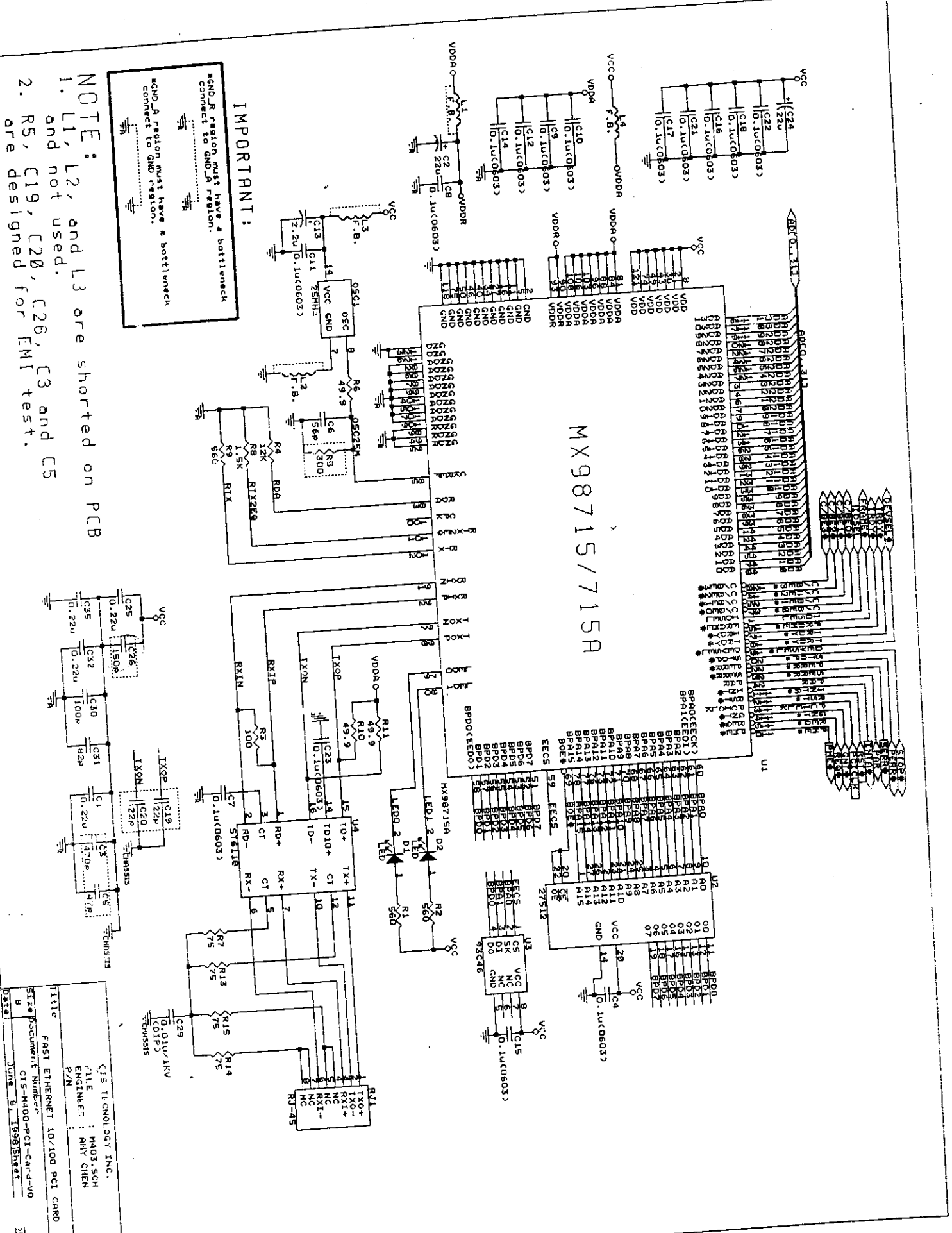
FDC6324L Integrated Load Switch
 FDC6324L Integrated Load Switch
 ORNALLY INC. a agent
 TEL: 886-3-2768339 (Taipei)
 886-3-5788138 (Hsinchu)
 www.fairchildsemi.com

F. B. Ferrit Bead
 HIG LAYERS M.B-321611-2000-N2
 TEL: 886-3-5517388 (Hsinchu)
 www.majisafe.com.tw



NOTE:
1. L1, L2, and L3 are shorted on PCB
and not used.
2. R5, C19, C20, C26, C3 and C5
are designed for EMI test.

IMPORTANT:
GND REGION MUST HAVE A BOTTLENECK
CONNECT TO GND REGION.
GND REGION MUST HAVE A BOTTLENECK
CONNECT TO GND REGION.



FILE	FAST ETHERNET 10/100 PCI CARD
ENGINEER	RAY CHEN
P/N	
DATE	June 8, 1998
REV	0