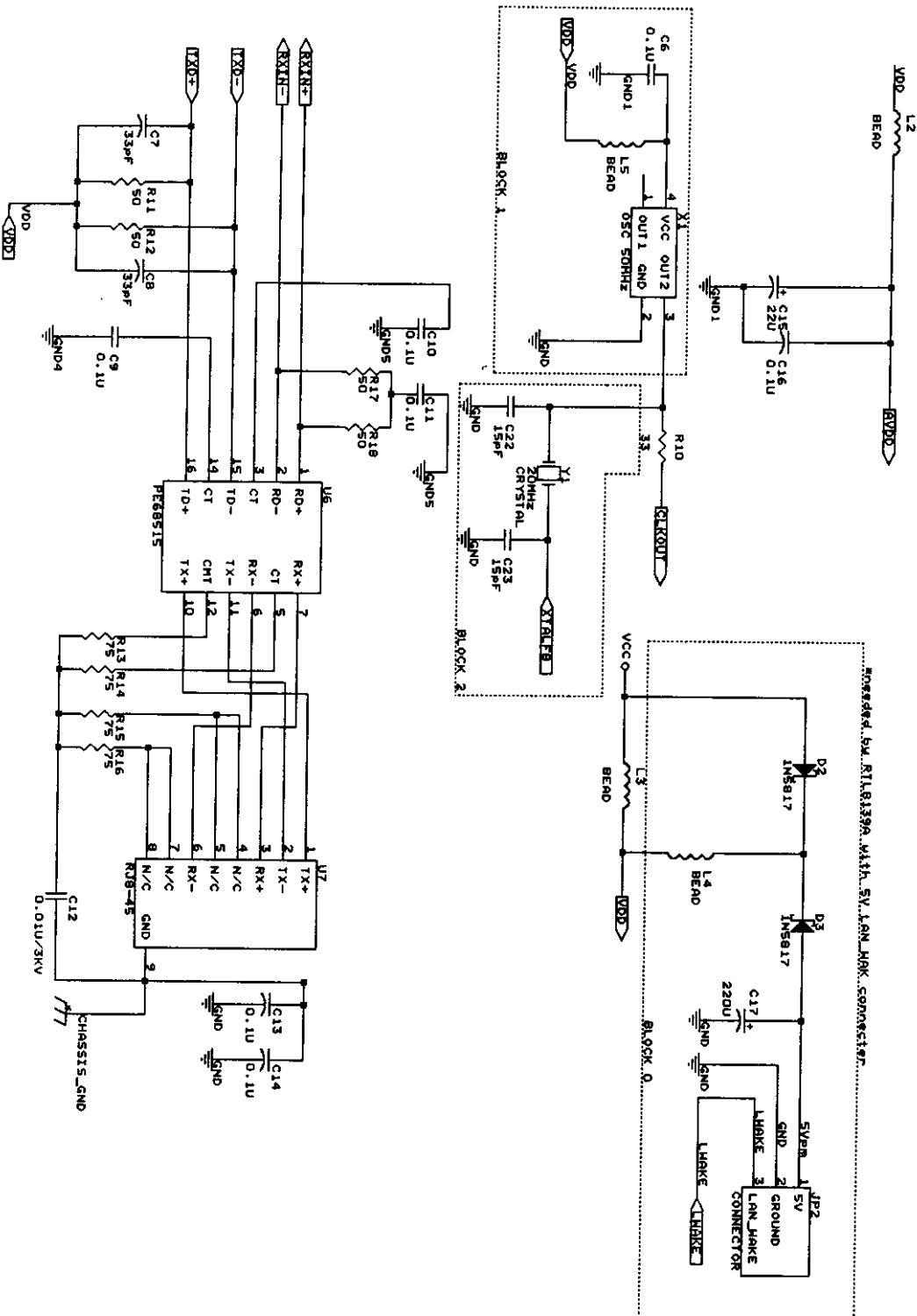


EXHIBIT D

Circuit Diagram

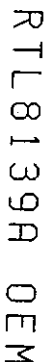
IF THE 20MHZ CRYSTAL IS USED AS THE CLOCK SOURCE, DISCARD BLOCK 1 AND APPLY R8 IN SHEET 1
 IF THE 50MHZ OSC IS USED AS THE CLOCK SOURCE, DISCARD BLOCK 2 AND R8 IN SHEET 1
 FOR RTL8139A WITH POWER MANAGEMENT APPLICATION, REMOVE L3
 FOR RTL8139A WITHOUT Vaux APPLICATION, REMOVE BLOCK 0
 FOR LUMP Vaux, C17 MUST BE LARGE ENOUGH TO AVOID POWER SUPPLY'S COMPATIBILITY
 FOR RTL8139 APPLICATION, L3 MUST HAVE RATED 300MA CURRENT/1000MHZ@100MHZ



This circuit is not the final version, and is subject to change possibly in the future. It's for reference only.

Realtek Semiconductor Co., Ltd.	
Title	RTL8139A OEM DEMO BOARD
Size Document Number	RED139A_002_V033V_000
REV	0.0
Date	JULY 28, 1998 Sheet 2 of 3

otherwise, discard this resistor to use 50MHz OSC as the clock source.



DOES NOT IMPLEMENT THIS CIRCUIT CURRENTLY.

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CD..317
ADCD..317
LINK
PCI.SCH
QA139A2.SCH

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PCI.SCH



RD39A001.SCH



RD39A002.SCH

KINSTON TECHNOLOGY INC.	
FILE : BLOCK.SCH	
ENGINEER : Bob Lee	
P/N : Main PCB Dlm.	
Title	
10/100 ENABLE PHY	
Size Document Number	
CIS-KNE1001X/E	
REV	
VO	
Date: September 14, 1998	
Sheet	
of	