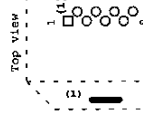
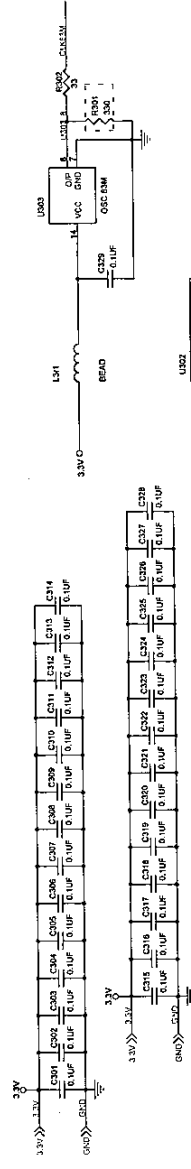
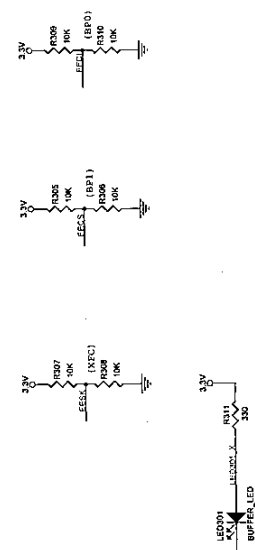
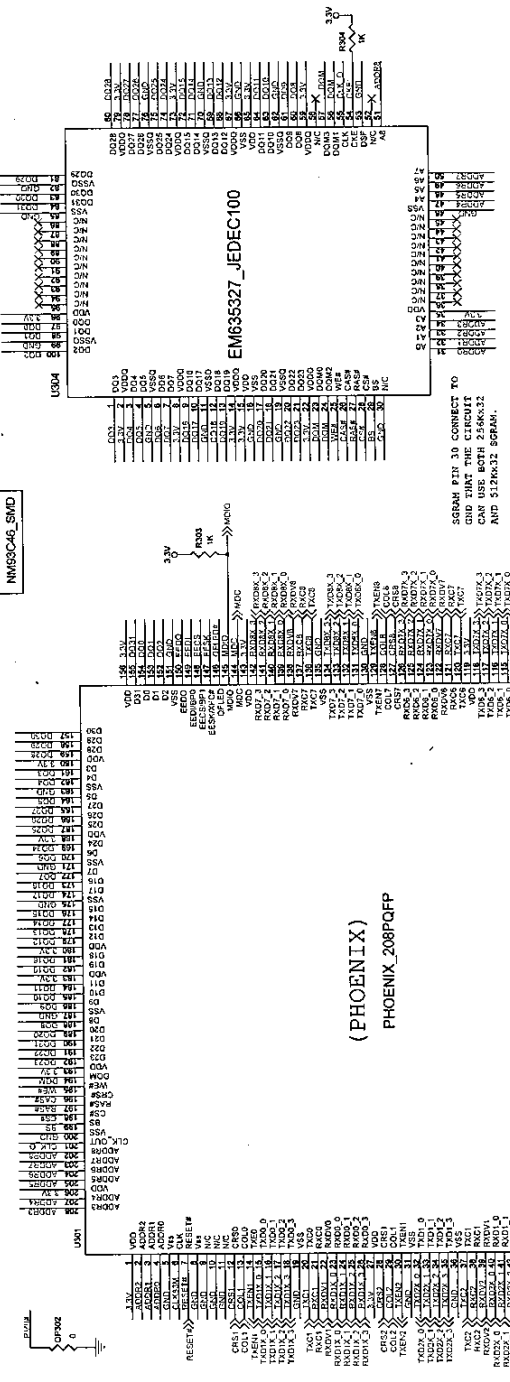


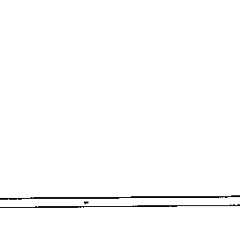
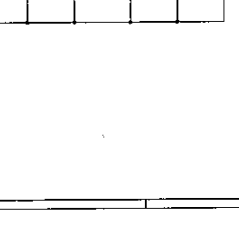
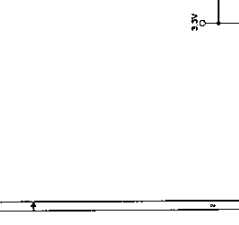
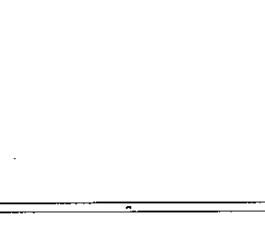
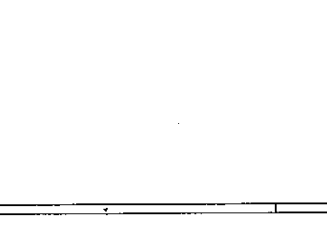
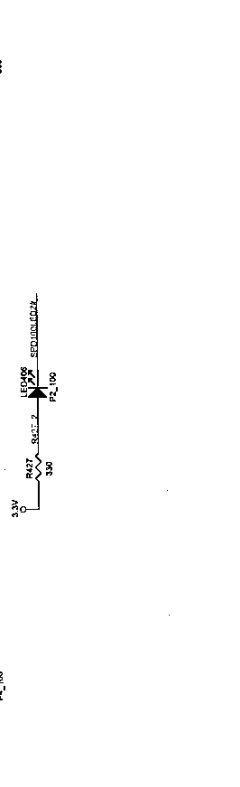
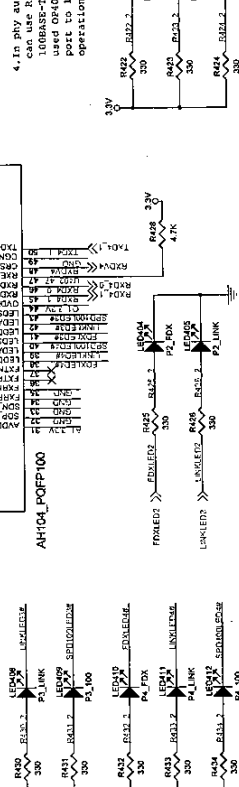
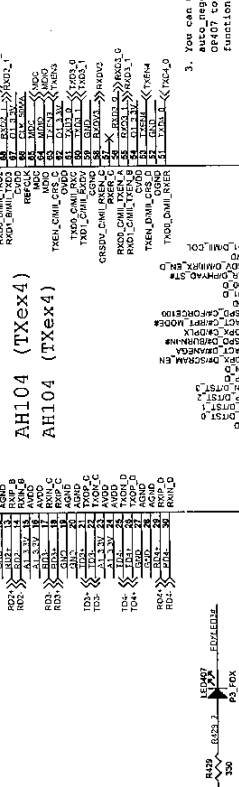
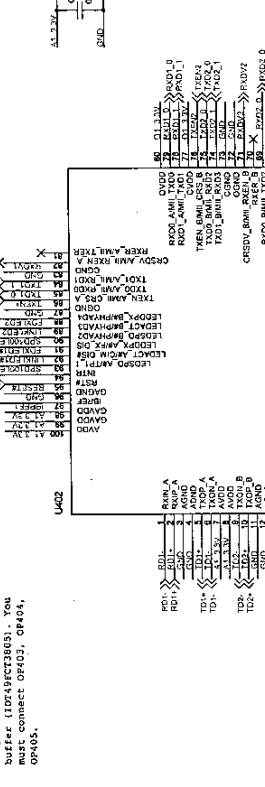
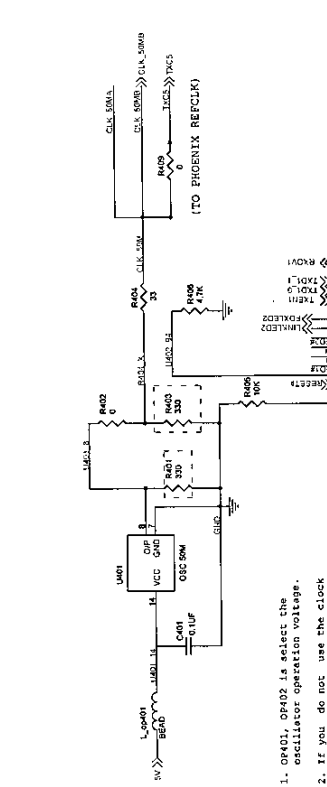
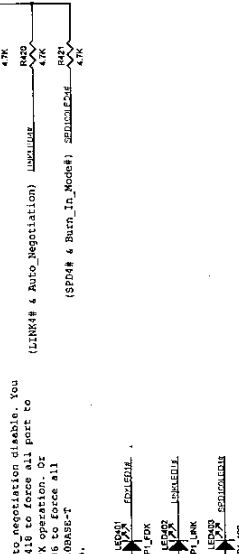
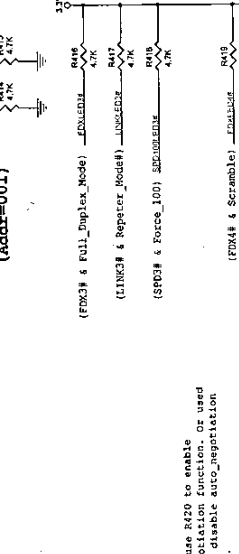
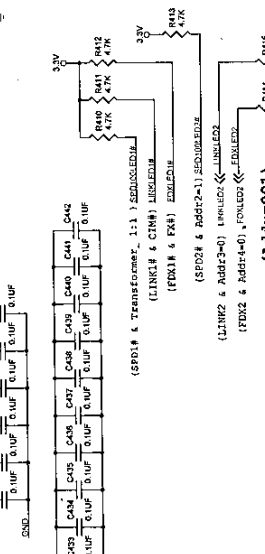
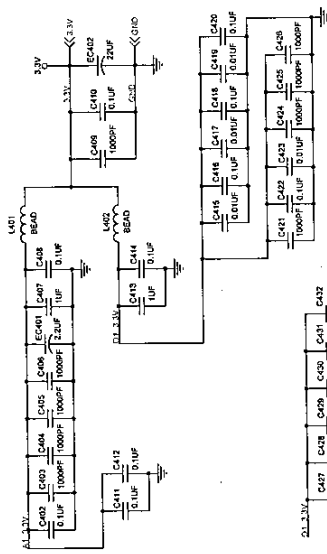
EXHIBIT D
Circuit Diagram



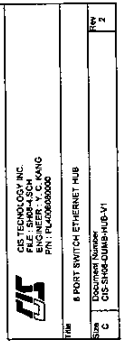


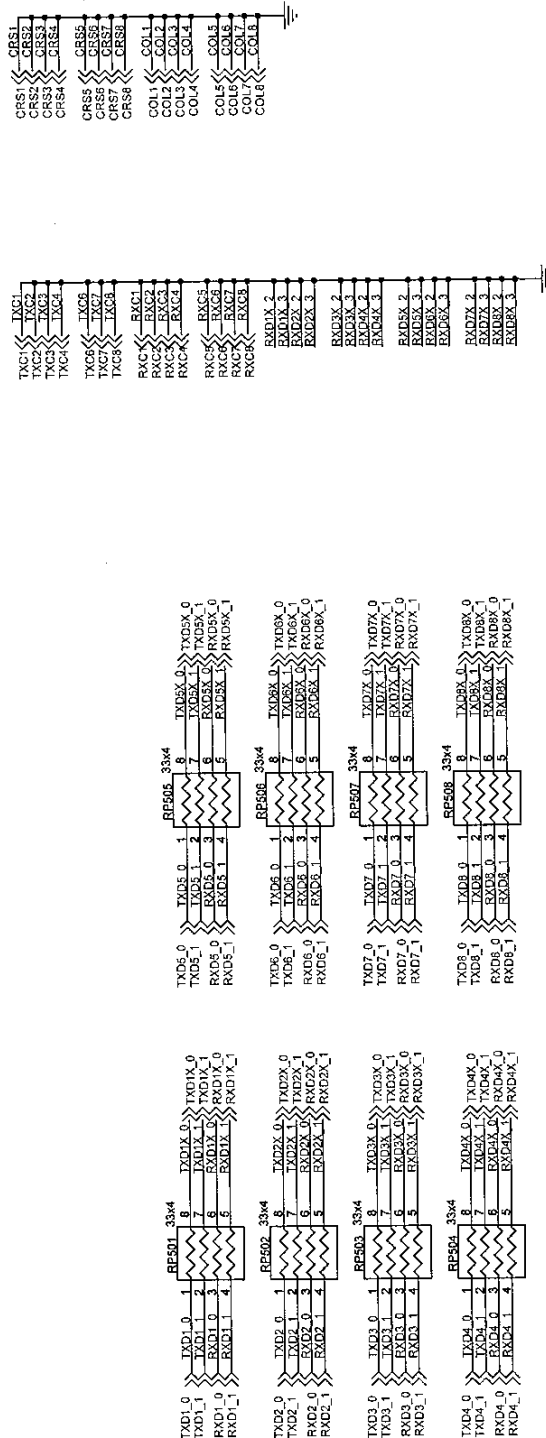
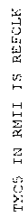
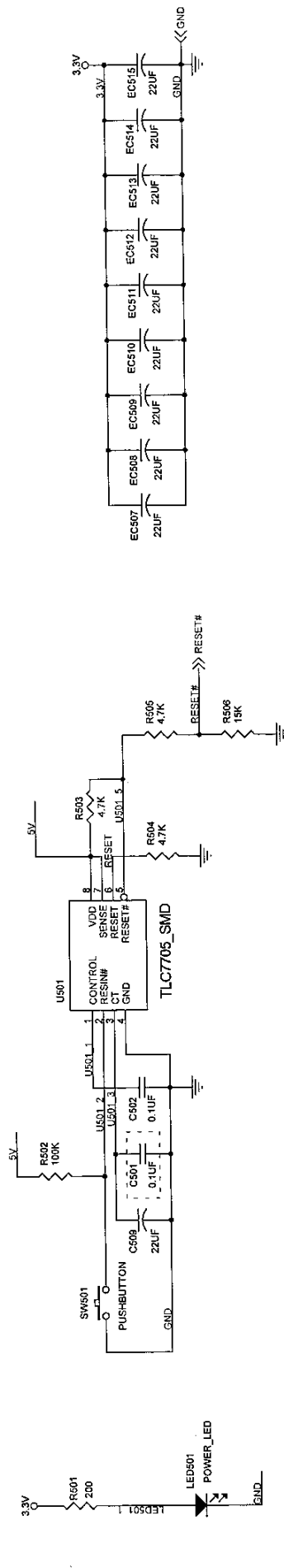
OP202 : Reduced M17.

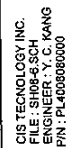




1. OP401, OP402 is select the oscillator operation voltage.
2. If you do not use the clock buffer (100PCT803), You must connect OP403, OP404, OP405.







CIS TECHNOLOGY INC.
FILE : SH08-6.SCH
ENGINEER : Y. C. KANG
P/N : PL4003080000

8 PORT SWITCH ETHERNET HUB

Size B	Document Number CIS-SH08-DUMB-HUB-V1
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Date: Tuesday, May 25, 1999

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