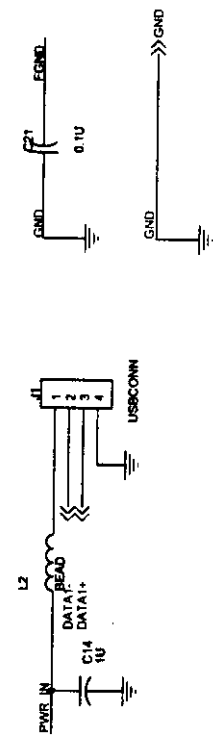
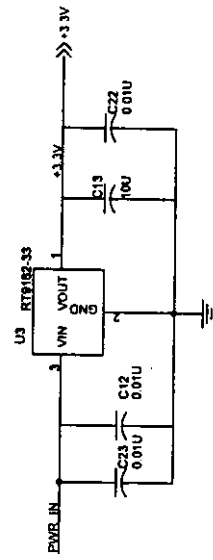
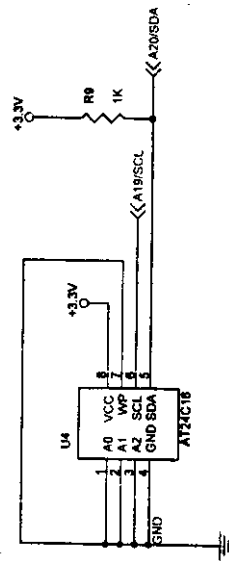
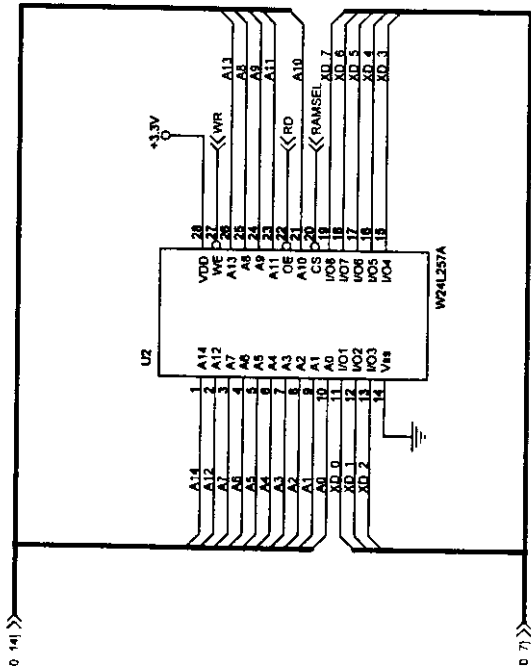


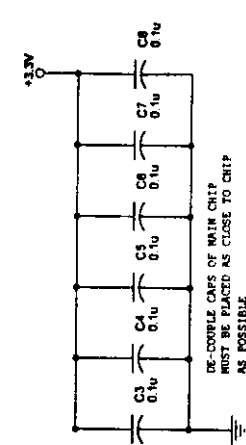
EXHIBIT D

Circuit Diagram




 CIS TECHNOLOGY INC.
 FILE : U001T -> RAM ROM Connect Power
 ENGINEER : Sky Wang
 PIN : PL4006

RAM ROM CONNECT POWER			
Doc	Document Number	Rev	1.1
Rev	CIS-T006CATV2-DUMB-HUB-V1	Rev	1.1
Date	Monday, June 28, 1999	Printed	3 of 4



-COUPLE CAPS OF MAIN CHIP
ST BE PLACED AS CLOSE TO CHIP
POSSIBLE

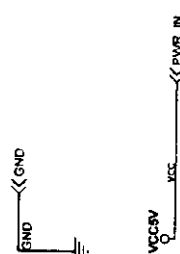


CIS TECHNOLOGY INC.
FILE : U001T ->Black Diagram
ENGINEER : Shy Wang
P/N : PL4008

1100

Document Number

Date: Monday, June 28, 1988



File	PHY TRANSFORMER PART		
Size	B	Document Number	CIS-T00GCA72-DUMB-HUB-V1
Date	Monday, June 28, 1999	Sheet	1