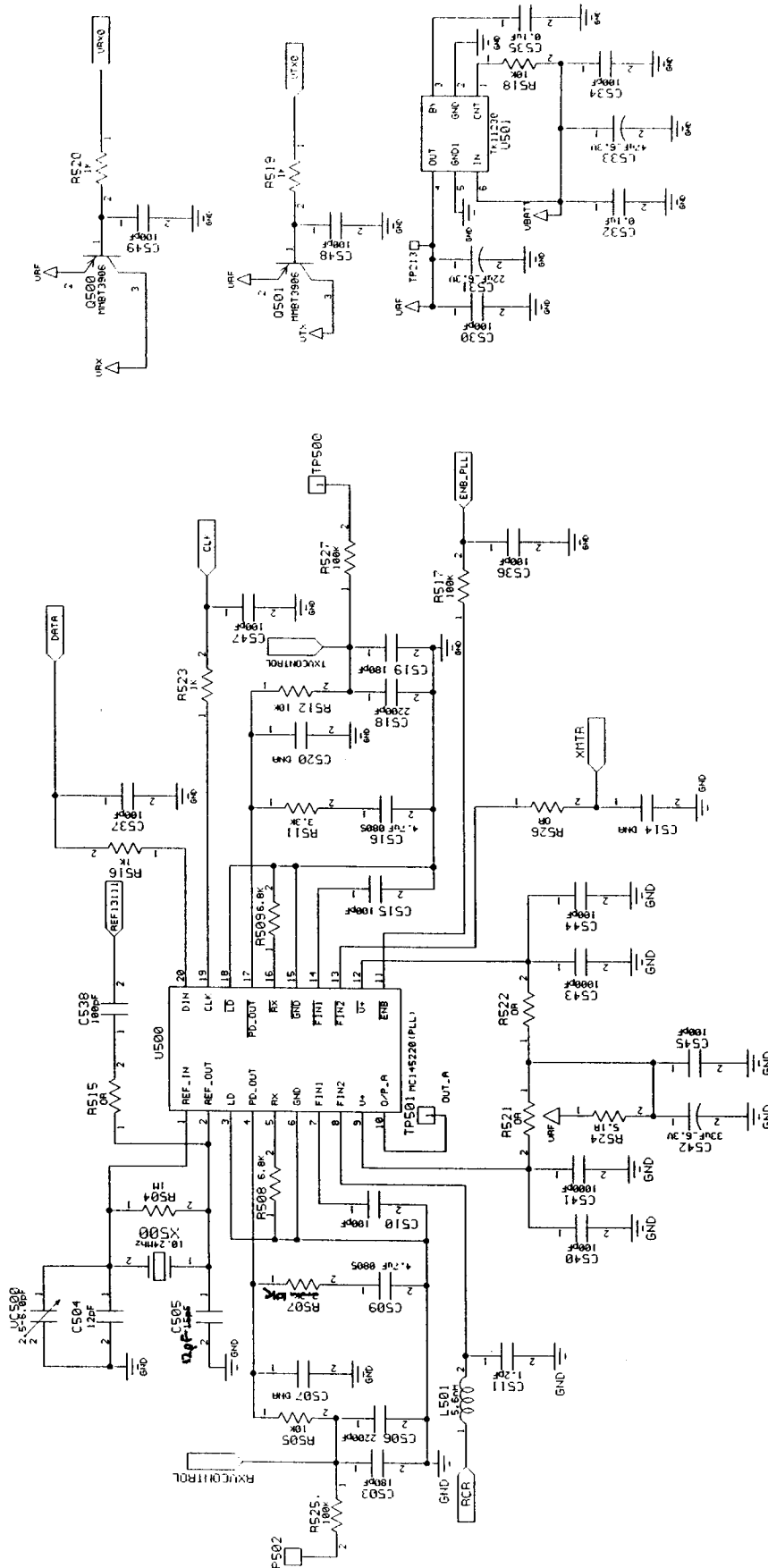
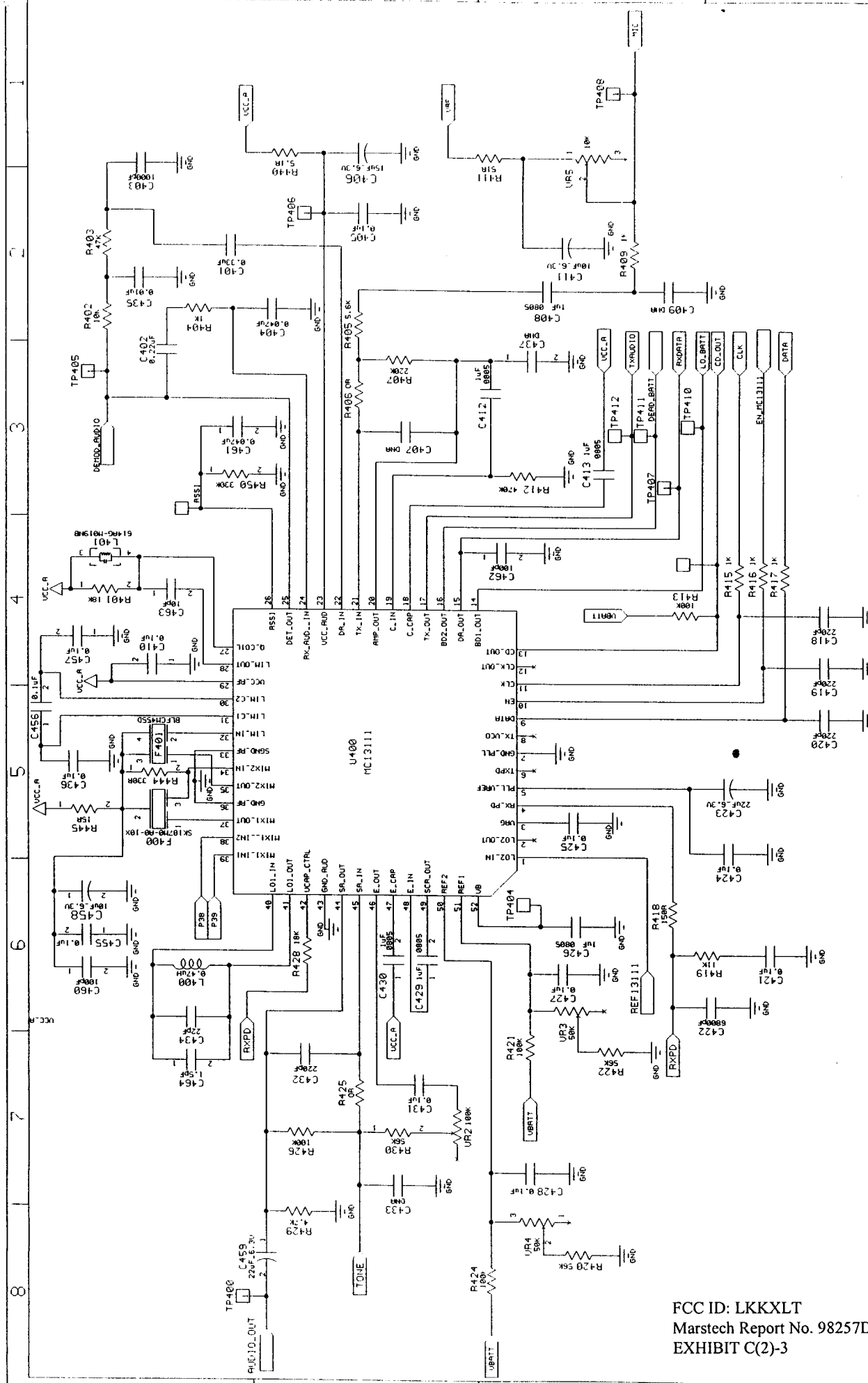






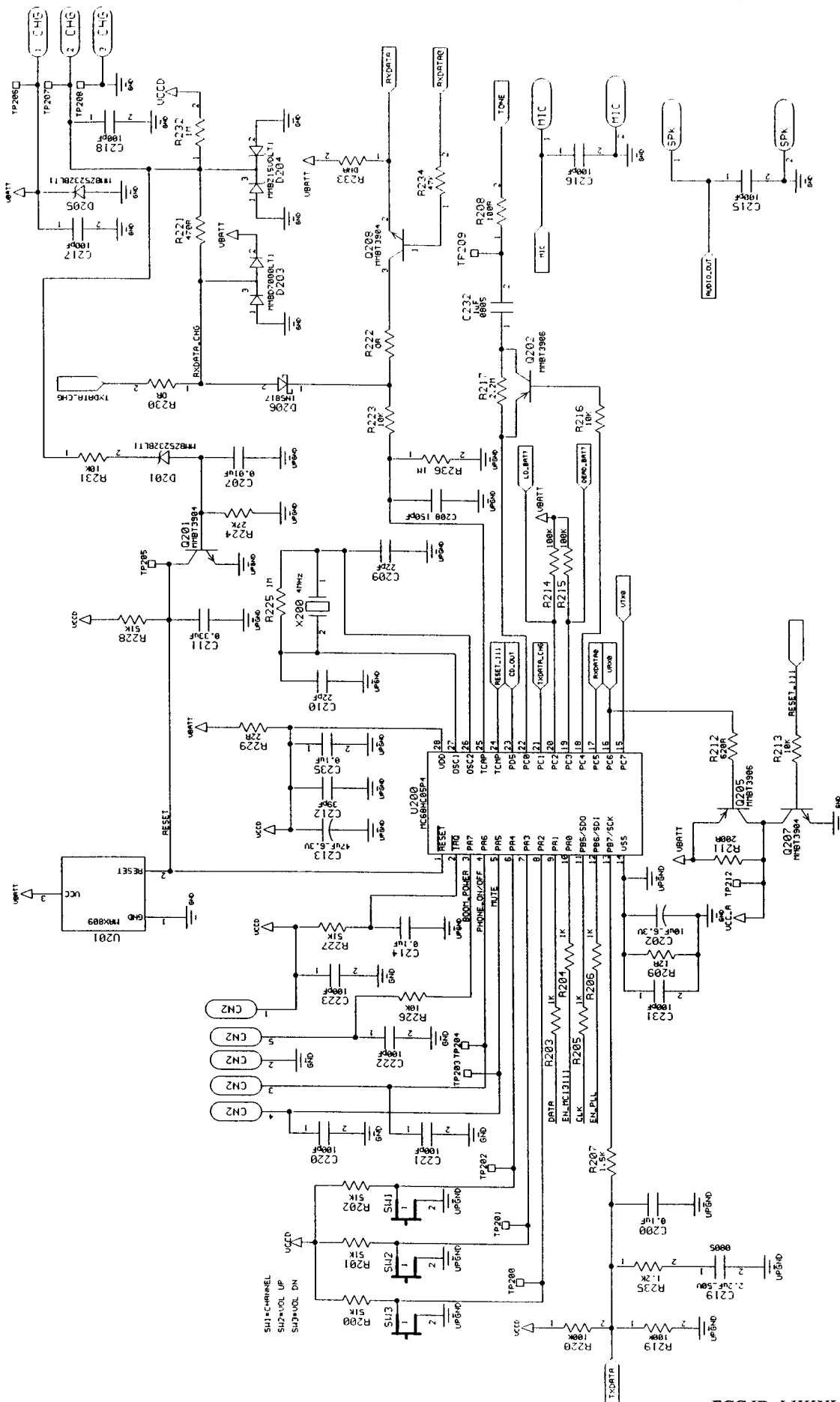
FCC ID: LKKXLT  
 Marstech Report No. 98257D  
 EXHIBIT C(2)-2

|                         |          |                                                        |         |             |              |          |              |              |        |
|-------------------------|----------|--------------------------------------------------------|---------|-------------|--------------|----------|--------------|--------------|--------|
| ASTRALINK<br>Technology | TITLE    | 900MHz Headset RF Board Schematic Diagram (HPI-Picard) |         |             | DESIGN BY    | DRAW BY  | APPROVED BY  | PRINTED DATE | SHEET  |
|                         | FUNCTION | PLL and Wideband IF                                    |         |             | CHARL THOMAS | S.C. GOM | CHARL THOMAS | 15 04 98     | 1 OF 1 |
|                         | VERSION  | 3.3 DATE 16/04/98                                      | PCB P/N | H-PCB000001 |              |          |              |              |        |



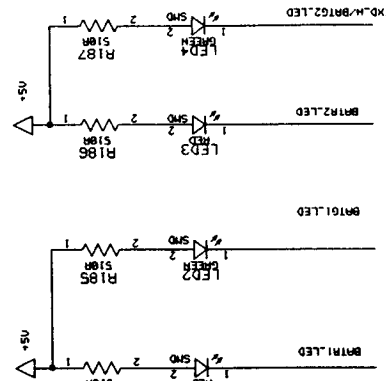
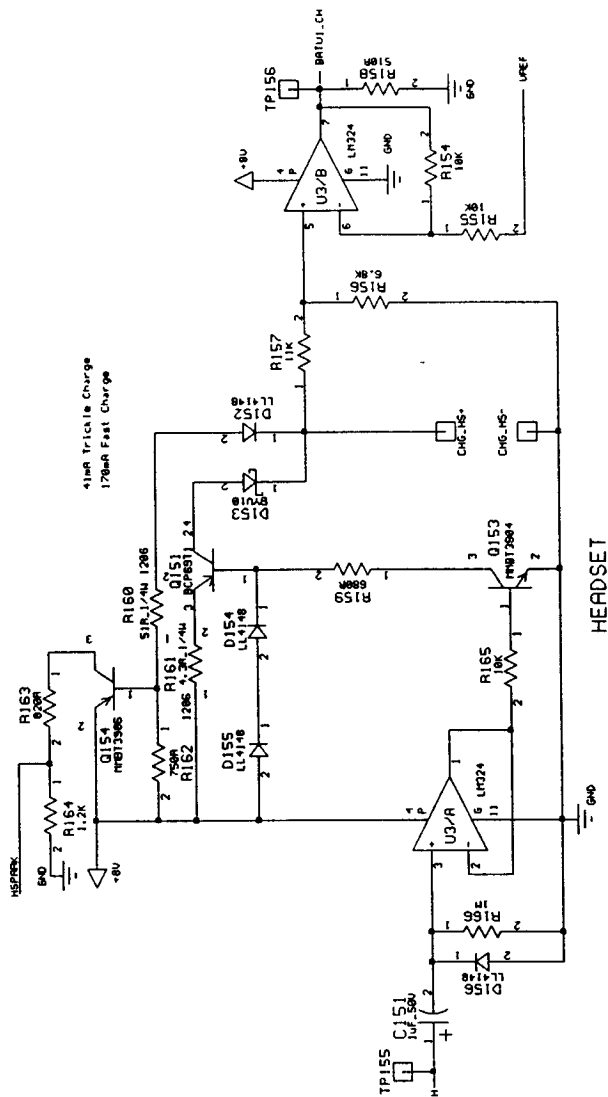
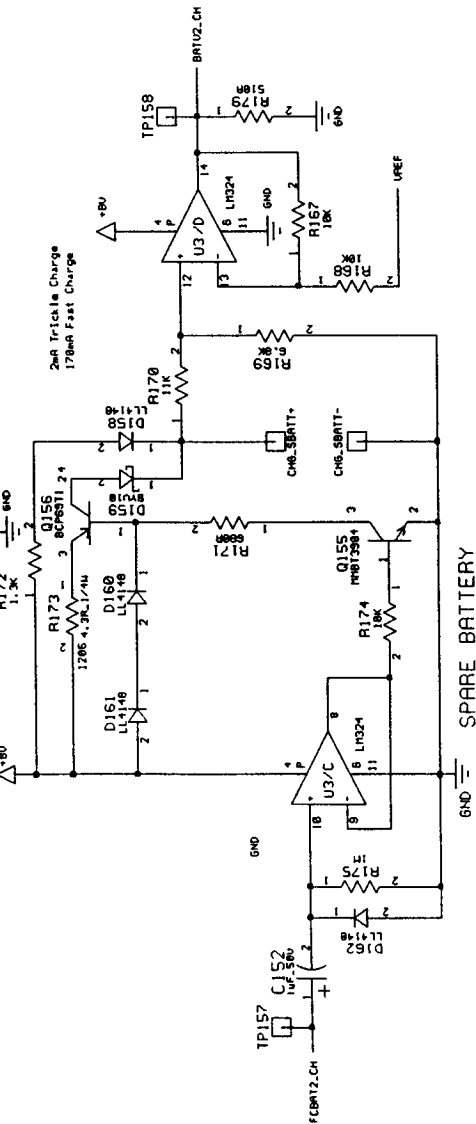
| TITLE    | 900MHz Headset RF Board Schematic Diagram (H01 Picard) | DESIGN BY | CHAR/THOMAS | S.C. 60H | APPROVED BY | CHAR/THOMAS | PRINTED DATE | SHEET  |
|----------|--------------------------------------------------------|-----------|-------------|----------|-------------|-------------|--------------|--------|
| FUNCTION | Baseband MC1311                                        |           |             |          |             |             |              |        |
| VERSION  | 3.3 DATE 16/04/98 PCB P/N H-PCB00001                   |           |             |          |             |             |              |        |
|          |                                                        |           |             |          |             |             | 15.04.98     | 3 OF 4 |

FCC ID: LKKXLT  
Marstech Report No. 98257D  
EXHIBIT C(2)-3



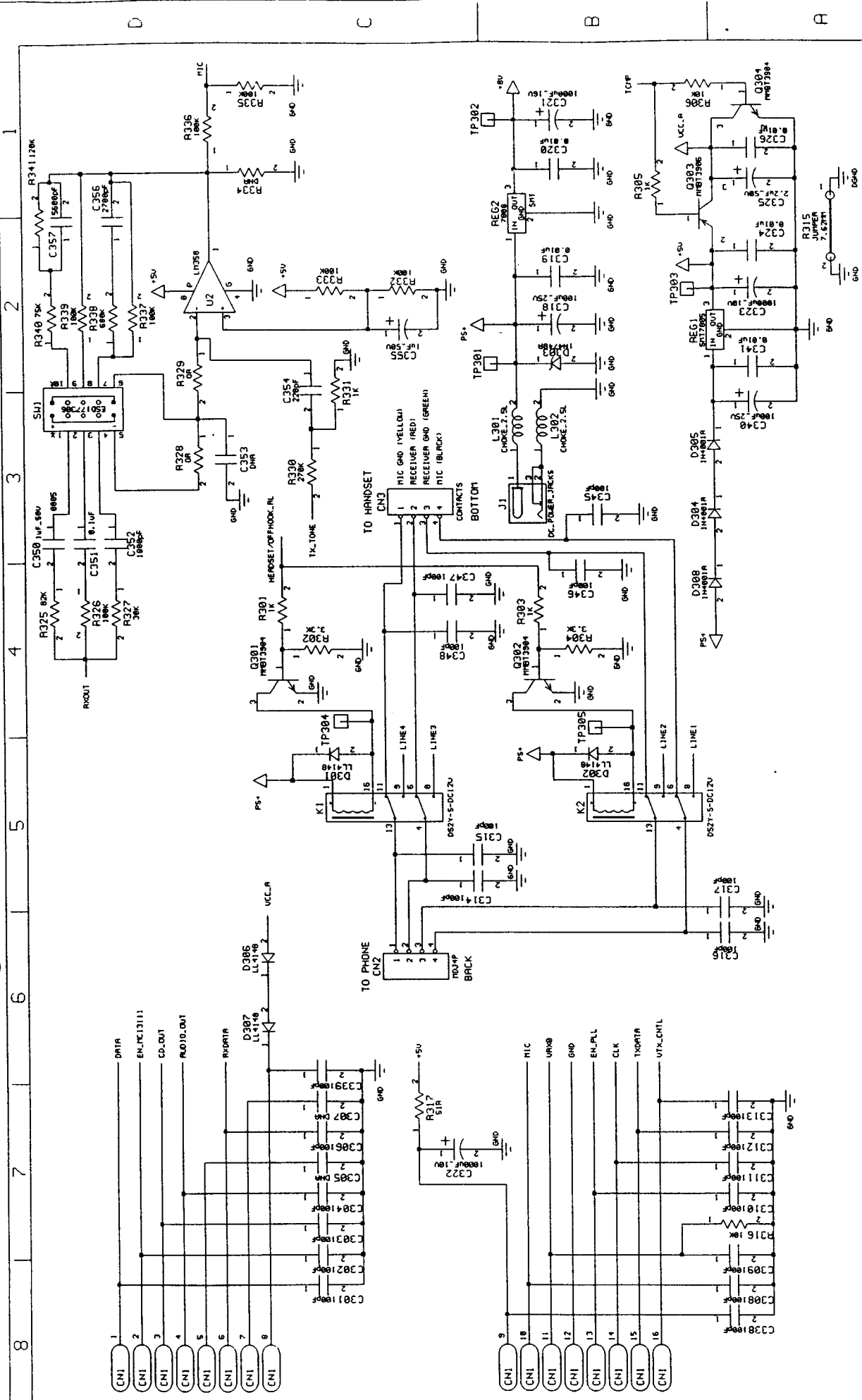
|                          |  |                                                              |  |                 |                     |                     |              |          |
|--------------------------|--|--------------------------------------------------------------|--|-----------------|---------------------|---------------------|--------------|----------|
| ASTRALINK<br>Technology  |  | TITLE 900MHz Headset RF Board Schematic Diagram (HDI Picard) |  | DESIGN BY       | DRAN BY             | APPROVED BY         | PRINTED DATE | SHEET    |
| FUNCTION MicroController |  | VERSION 3.3                                                  |  | DATE 16/04/2008 | SCH P/N W-SCH000001 | PCB P/N W-PCB000001 | CHAN/THOMAS  | S.C. GOH |
|                          |  |                                                              |  |                 |                     |                     |              | 15/04/98 |
|                          |  |                                                              |  |                 |                     |                     |              | 1 OF 4   |



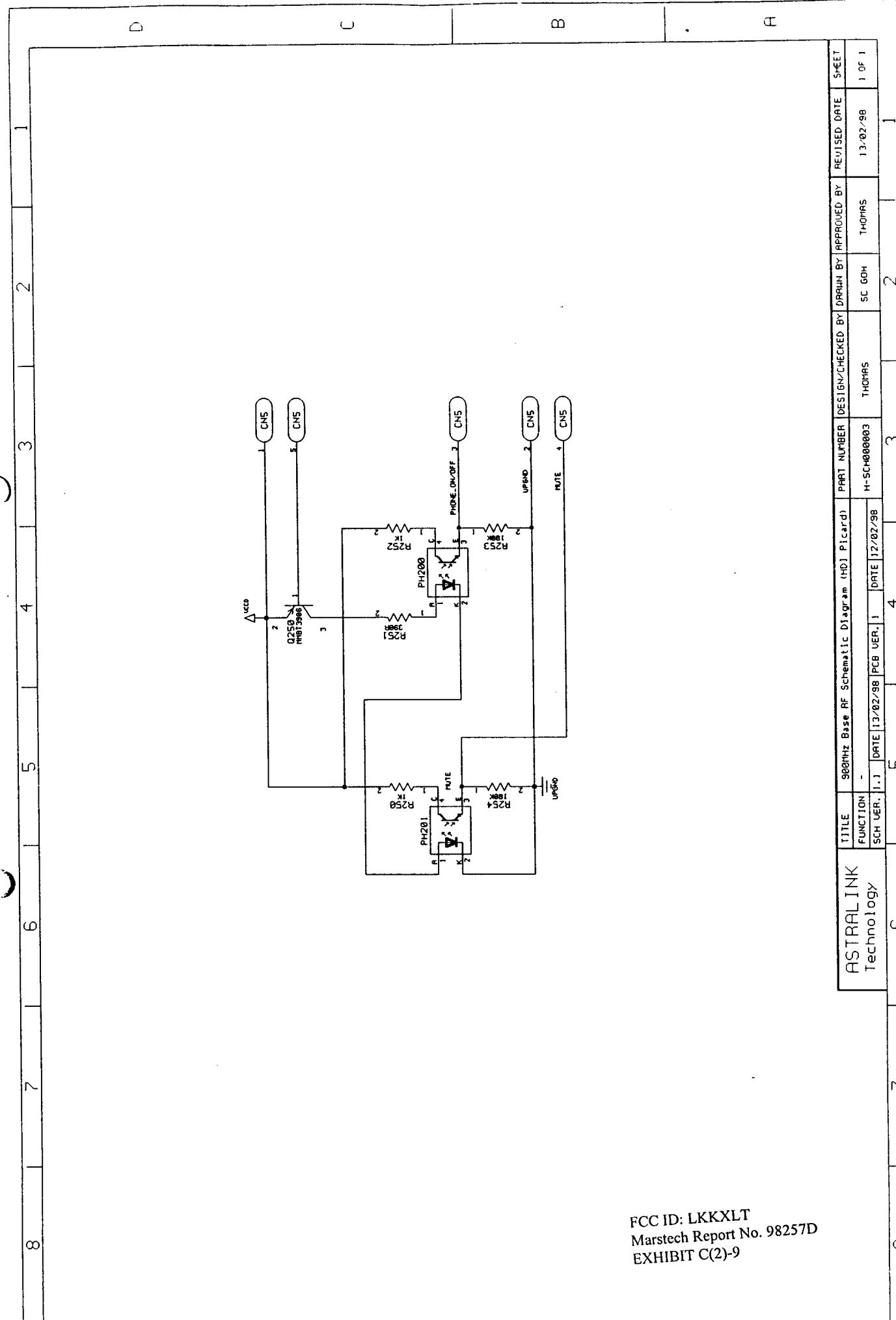


|                         |  |                                    |      |          |          |     |      |             |  |          |  |              |  |
|-------------------------|--|------------------------------------|------|----------|----------|-----|------|-------------|--|----------|--|--------------|--|
| ASTRALINK<br>Technology |  |                                    |      |          |          |     |      |             |  |          |  |              |  |
| TITLE                   |  | Hello Direct Picard Base Schematic |      |          |          |     |      |             |  |          |  |              |  |
| FUNCTION                |  | BATTERY CHARGER                    |      |          |          |     |      |             |  |          |  |              |  |
| SCH VER.                |  | 3.2                                | DATE | 17/04/98 | PCB VER. | 3.0 | DATE | 20/04/98    |  |          |  |              |  |
|                         |  | 6                                  |      | 5        |          | 4   |      | 3           |  | 2        |  | 1            |  |
|                         |  |                                    |      |          |          |     |      | THOMAS/SEET |  | SERENE   |  | THOMAS/SEET  |  |
|                         |  |                                    |      |          |          |     |      | SCH3000001  |  |          |  |              |  |
|                         |  |                                    |      |          |          |     |      |             |  | DRAIN BY |  | APPROVED BY  |  |
|                         |  |                                    |      |          |          |     |      |             |  |          |  | REVISED DATE |  |
|                         |  |                                    |      |          |          |     |      |             |  |          |  | 17/04/98     |  |
|                         |  |                                    |      |          |          |     |      |             |  |          |  | SHEET        |  |
|                         |  |                                    |      |          |          |     |      |             |  |          |  | 2 OF 4       |  |





| TITLE                |  | PART NUMBER                        |  | DESIGN/CHECKED BY |  | DRAWN BY    |  | APPROVED BY |  | REVISED DATE |  | SHEET  |  |
|----------------------|--|------------------------------------|--|-------------------|--|-------------|--|-------------|--|--------------|--|--------|--|
| ASTRALINK Technology |  | Hello Direct Picard Base Schematic |  | SCH 308001        |  | THOMAS/SEET |  | SERENE      |  | 17/04/98     |  | 4 OF 4 |  |
| FUNCTION             |  | Interface/ Power Supply            |  | DATE              |  | 17/04/98    |  | PCB VER.    |  | 3.0          |  |        |  |
| SCH VER.             |  | 3.2                                |  | DATE              |  | 17/04/98    |  | PCB VER.    |  | 3.0          |  |        |  |



8 7 6 5 4 3 2 1

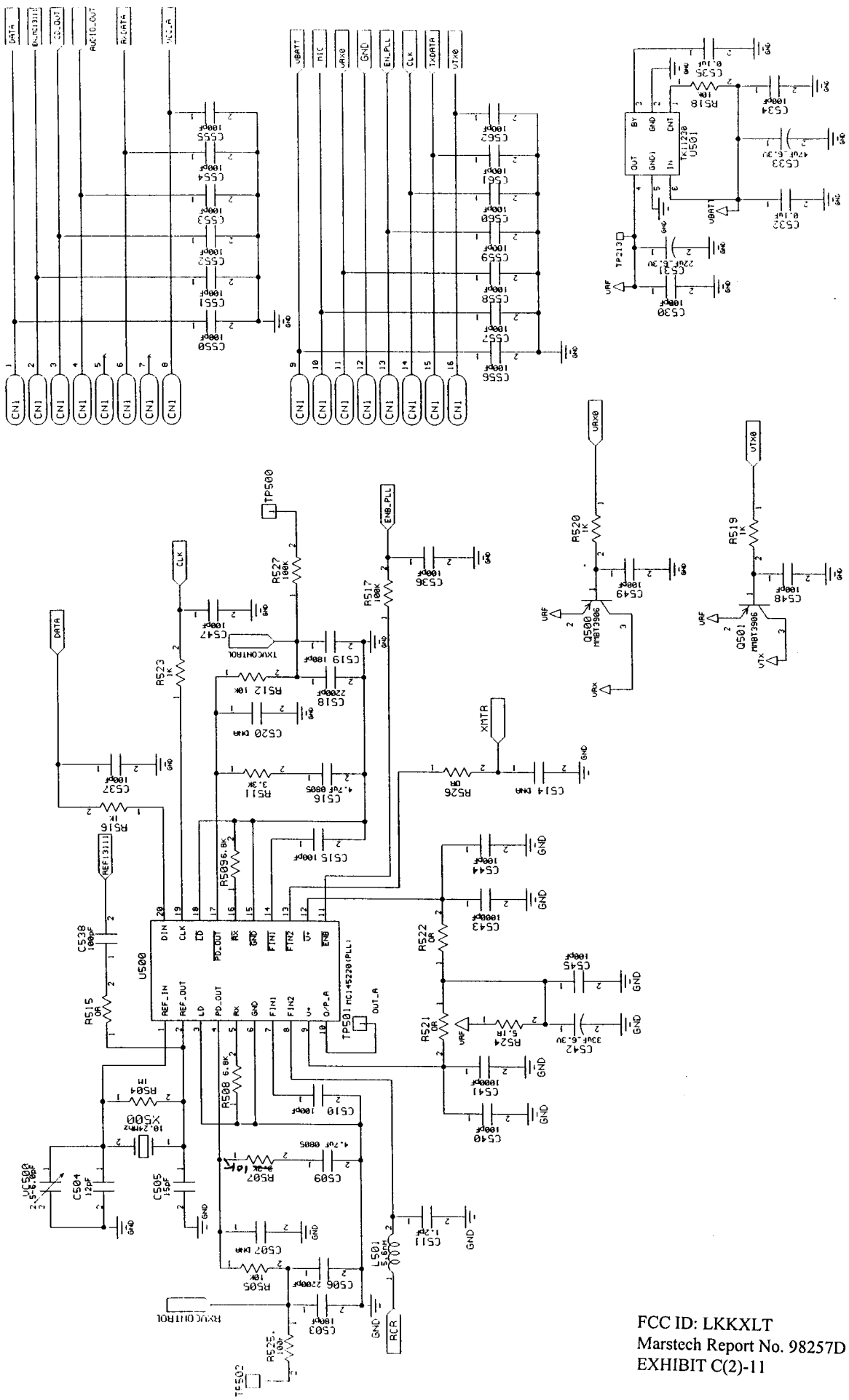
D C B A

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 Marstech Report No. 98257D  
 EXHIBIT C(2)-9

|                         |          |           |                                               |      |             |     |                   |   |          |          |             |    |              |        |          |        |
|-------------------------|----------|-----------|-----------------------------------------------|------|-------------|-----|-------------------|---|----------|----------|-------------|----|--------------|--------|----------|--------|
| ASTRALINK<br>Technology | TITLE    |           | 900MHz Base RF Schematic Diagram (HDI Picard) |      | PART NUMBER |     | DESIGN/CHECKED BY |   | DRAWN BY |          | APPROVED BY |    | REVISED DATE |        | SHEET    |        |
|                         | FUNCTION | SCH. VER. | 1.1                                           | DATE | 13/02/98    | PCB | VER.              | 1 | DATE     | 12/02/98 | THOMAS      | SC | G0H          | THOMAS | 13/02/98 | 1 OF 1 |

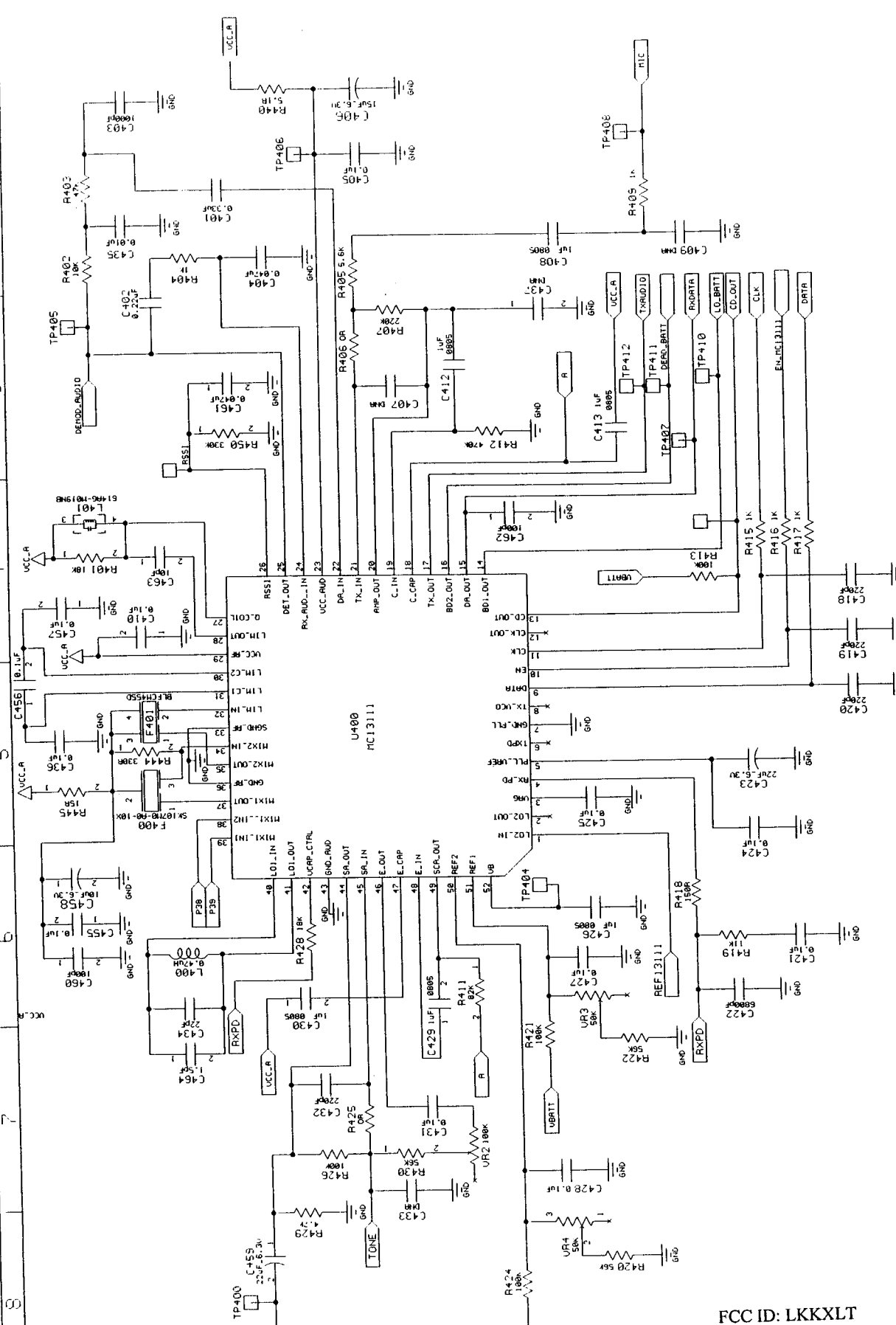
8 7 6 5 4 3 2 1





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 Marstech Report No. 98257D  
 EXHIBIT C(2)-11

|                              |  |                                                           |  |             |             |             |              |        |
|------------------------------|--|-----------------------------------------------------------|--|-------------|-------------|-------------|--------------|--------|
| ASTRALINK Technology         |  | TITLE 900MHz Base RF Board Schematic Diagram (HDI-Picard) |  | DESIGN BY   | DRAW BY     | APPROVED BY | PRINTED DATE | SHEET  |
| FUNCTION PLL and Hideband IF |  | VERSION 2.2 DATE 16/04/98/PCB DATE -                      |  | CHAN/THOMAS | S.C. GOH    | CHAN/THOMAS | 16/04/98     | 1 OF 3 |
|                              |  |                                                           |  | SCH P/N     | M-PCB000001 | PCB P/N     | M-PCB000001  |        |



|                           |  |                                                           |  |             |          |             |              |        |
|---------------------------|--|-----------------------------------------------------------|--|-------------|----------|-------------|--------------|--------|
| ASTRALINK Technology      |  | TITLE 500MHz Base RE Board Schematic Diagram (HDI Picard) |  | DESIGN BY   | DATE     | APPROVED BY | PRINTED DATE | SHEET  |
| FUNCTION Baseband MC13111 |  | VERSION 2.2 DATE 16/04/98 PCB P/N H-PCB0001               |  | CHAN/THOMAS | S.C. GOM | CHAN/THOMAS | 16/04/98     | 3 OF 3 |

FCC ID: LKKXLT  
Marstech Report No. 98257D  
EXHIBIT C(2)-12