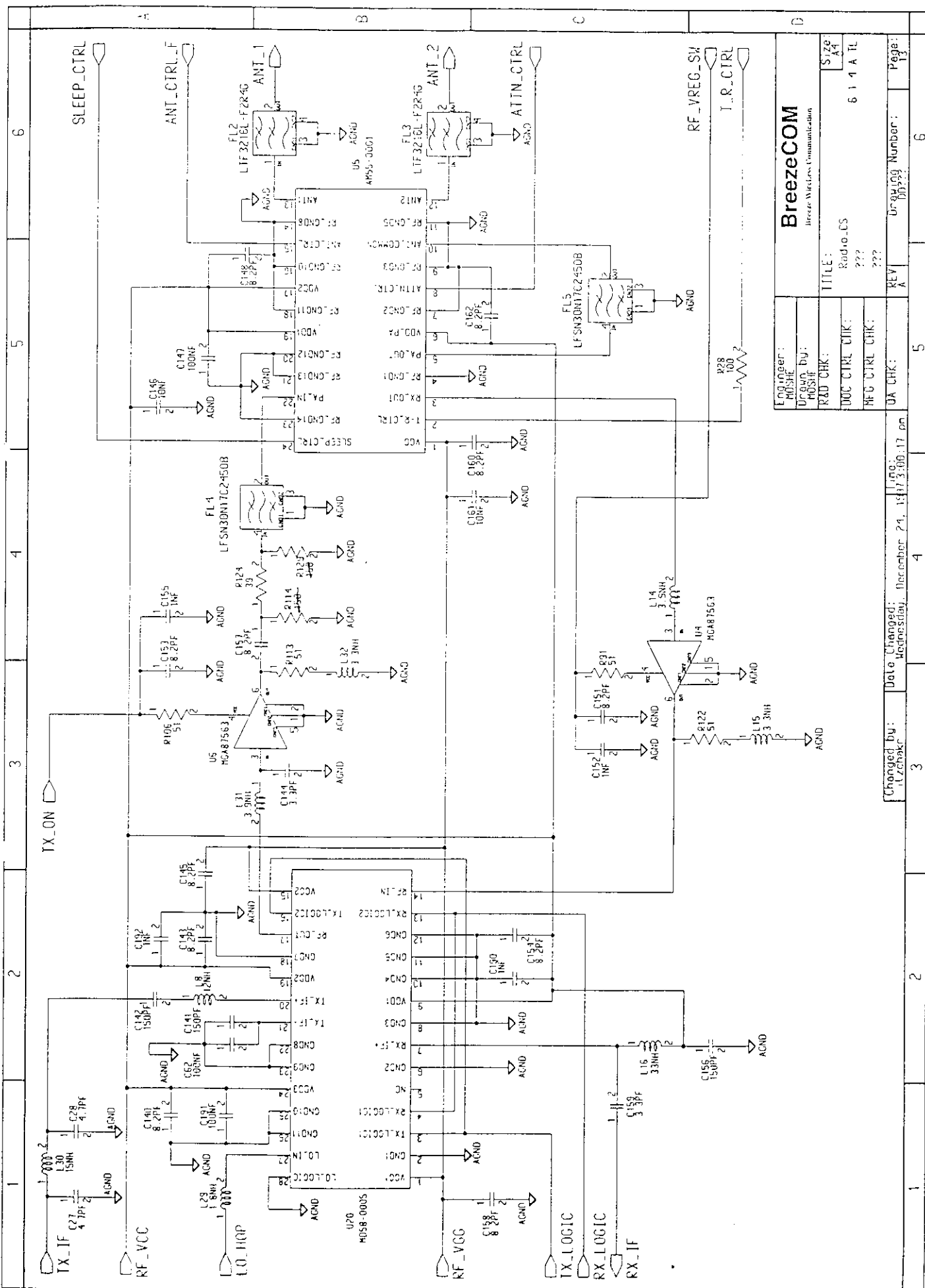


FCC requirements § 2.1033 (b)(4), (11)

**CIRCUIT DESCRIPTION.
SCHEMATIC DIAGRAM AND BLOCK DIAGRAM.**

This page is followed by the SA-PC transceiver description, antennas description, block diagram and the schematic diagram, hopping sequence on 20 pages.



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Engineer: MDSHE
Drawn by: MDSHE
R&D CHK: ???
DOC CTRL CHK: ???
REC CTRL CHK: ???

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Checked by: MDSHE
Date Changed: December 24, 15:17:30.17 pm
Revised by: MDSHE

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