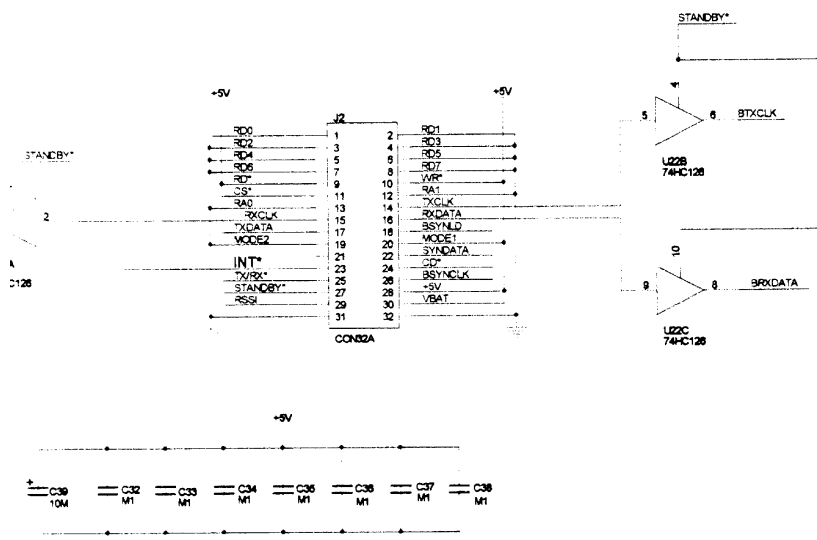


REV	ECN #	Description	Date	Drawn	CHK'D	APP'D
1	218:79	Changed R67 from 8R2 to 4R7	10DEC97	MAP		
	218:80	Added buffering to radio output signals				

10 V18
 11 SER
 12 A
 13 0
 14 0
 15 0
 16 0
 17 0
 18 0
 19 0
 20 0
 21 0
 22 0
 23 0
 24 0
 25 0
 26 0
 27 0
 28 0
 29 0
 30 0
 31 0
 32 0
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 35 0
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 37 0
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 97 0
 98 0
 99 0
 100 0

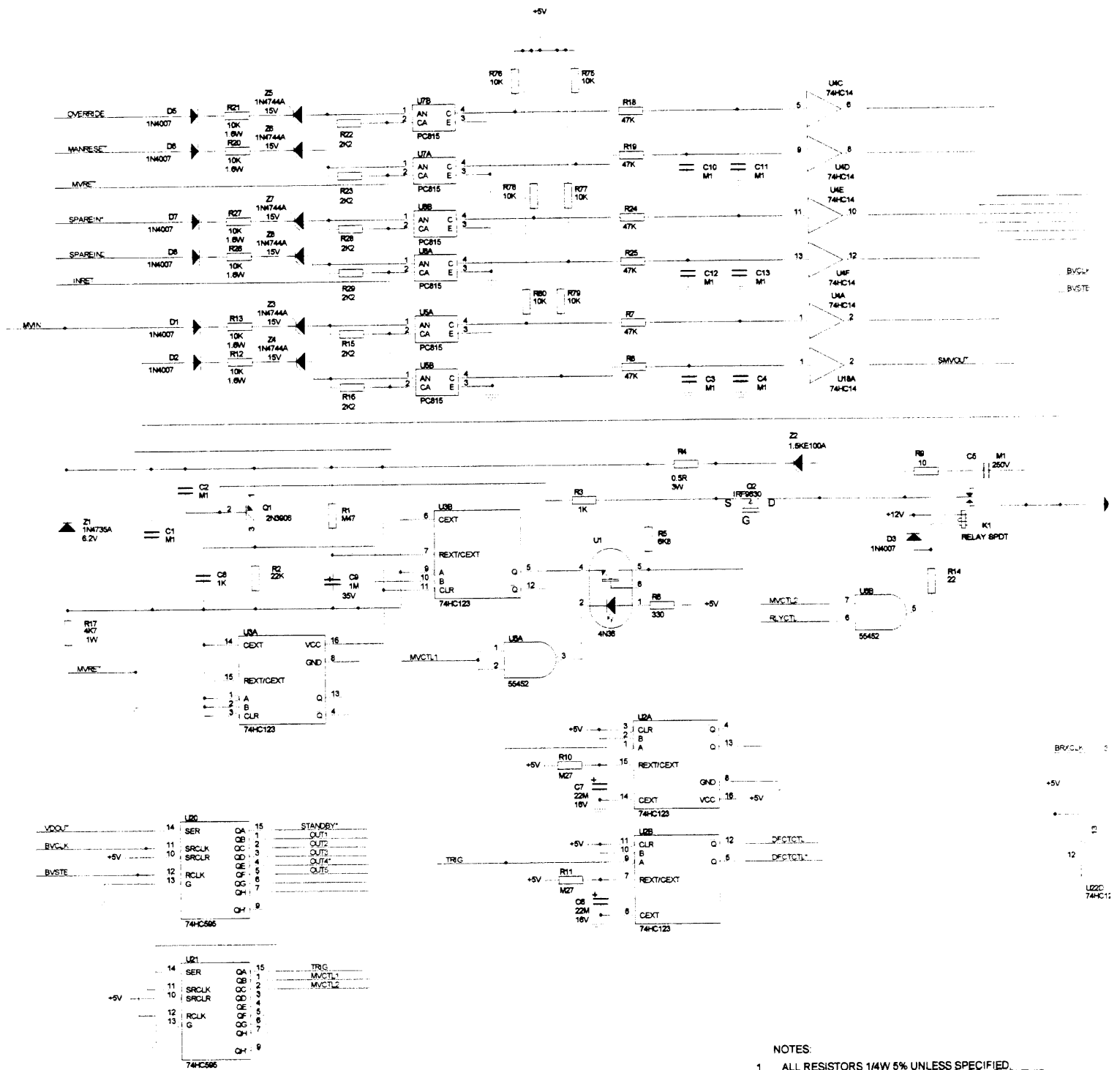
COPY
 CERTIFIED
 DATE 22 Jun 98

6E
 6F



Q-TRON LTD.
 CALGARY, ALBERTA

TITLE	ER CPU TRANSIENT SUPPRESSOR				
Drawn	EIH	27FEB95	P/N:	5821617.5821617:01	
Checked	WMJ	27FEB95	SIZE:	C	REV: 1
Approved	DRB	02MAR95	DWG#:	Q-89216-17-A01	
DATE:	Wednesday, December 10, 1997		SHEET	1	OF 2



NOTES:

1. ALL RESISTORS 1/4W 5% UNLESS SPECIFIED.
2. ALL NON-POLARIZED CAPACITORS ARE MONOLITHIC.
50V MINIMUM UNLESS SPECIFIED.
3. ALL POLARIZED CAPACITORS ARE TANTALUM UNLESS SPECIFIED.