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2.4.1. Block/Schematic Circuit Diagram

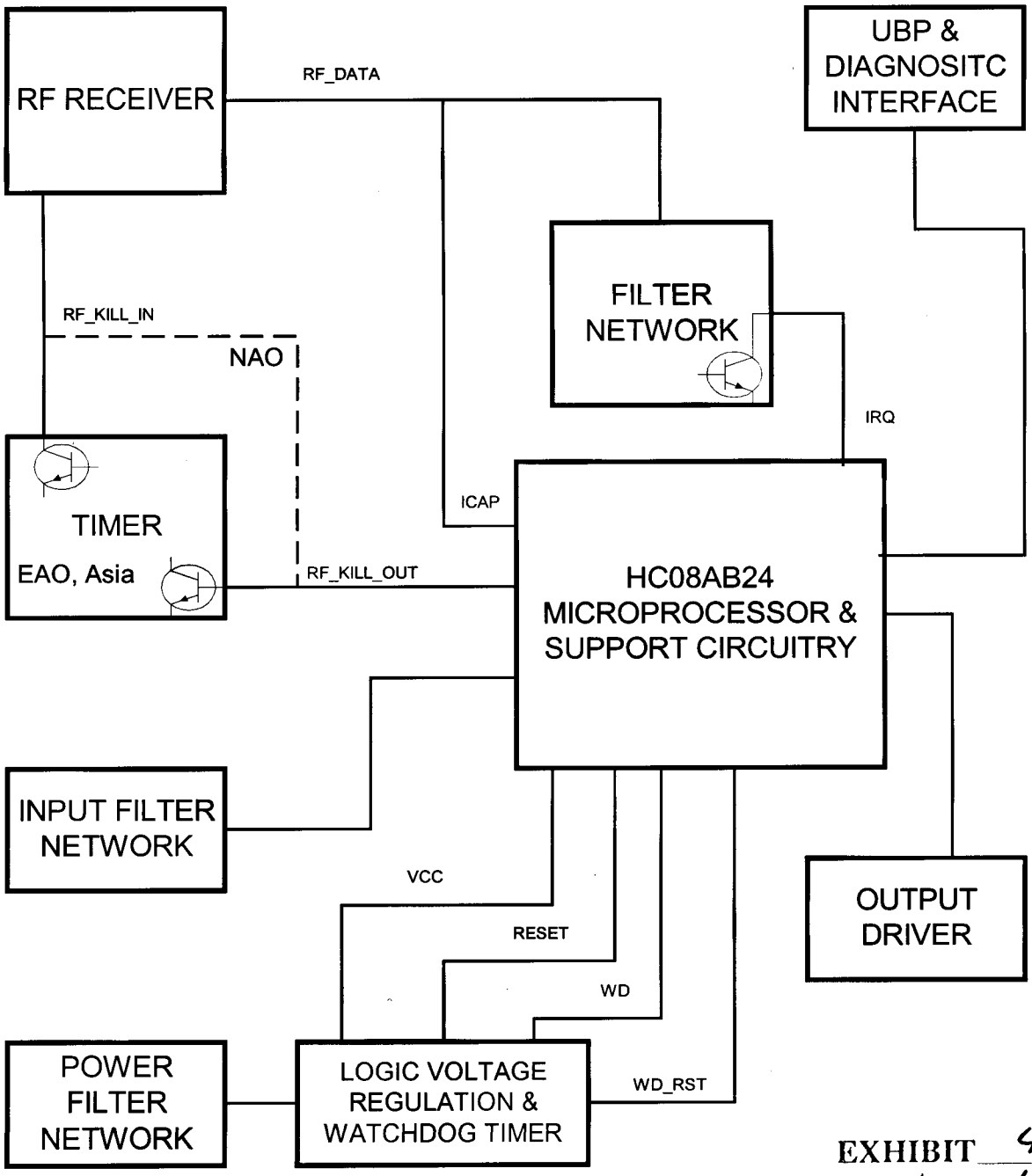


EXHIBIT 4
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