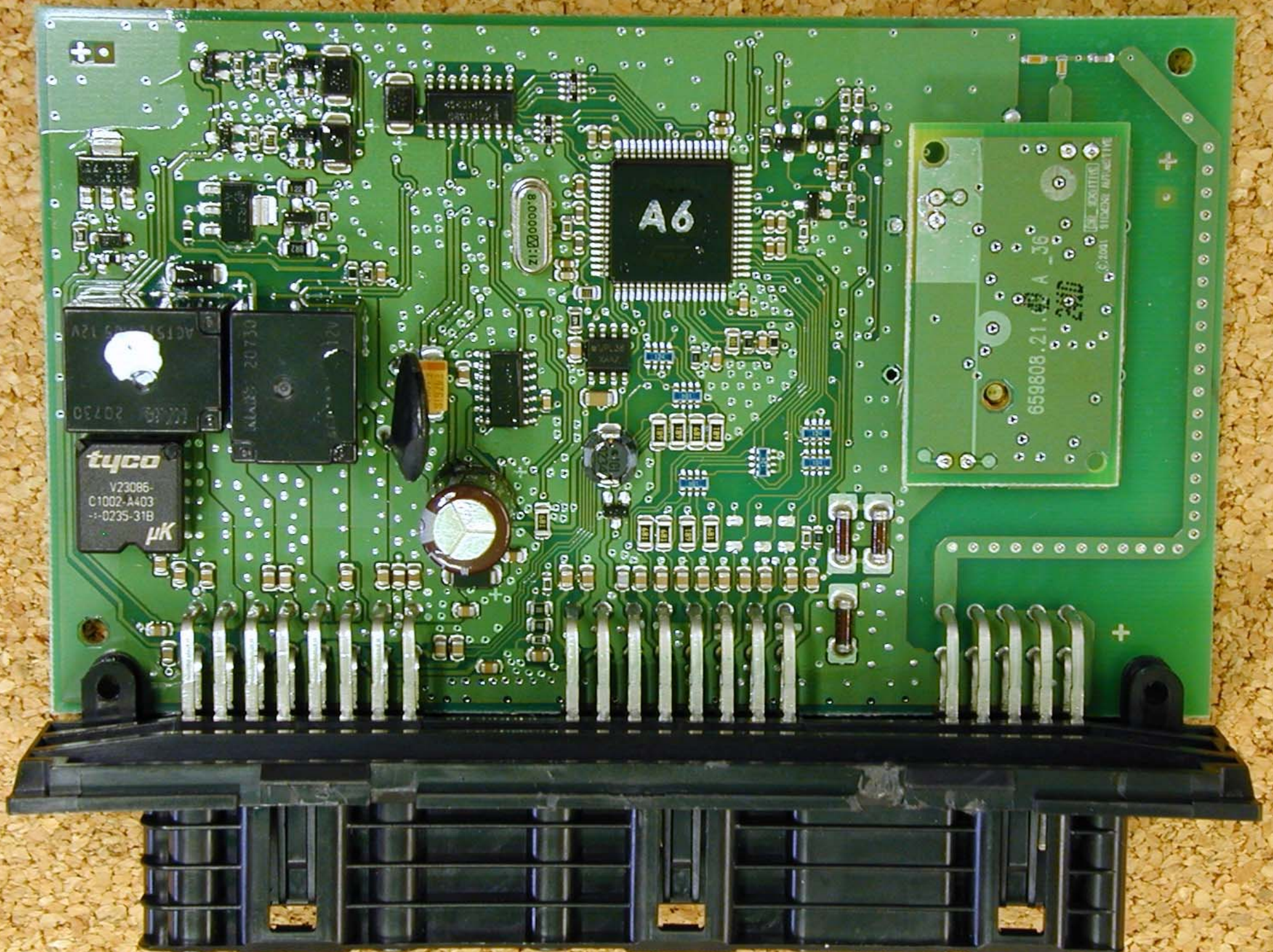
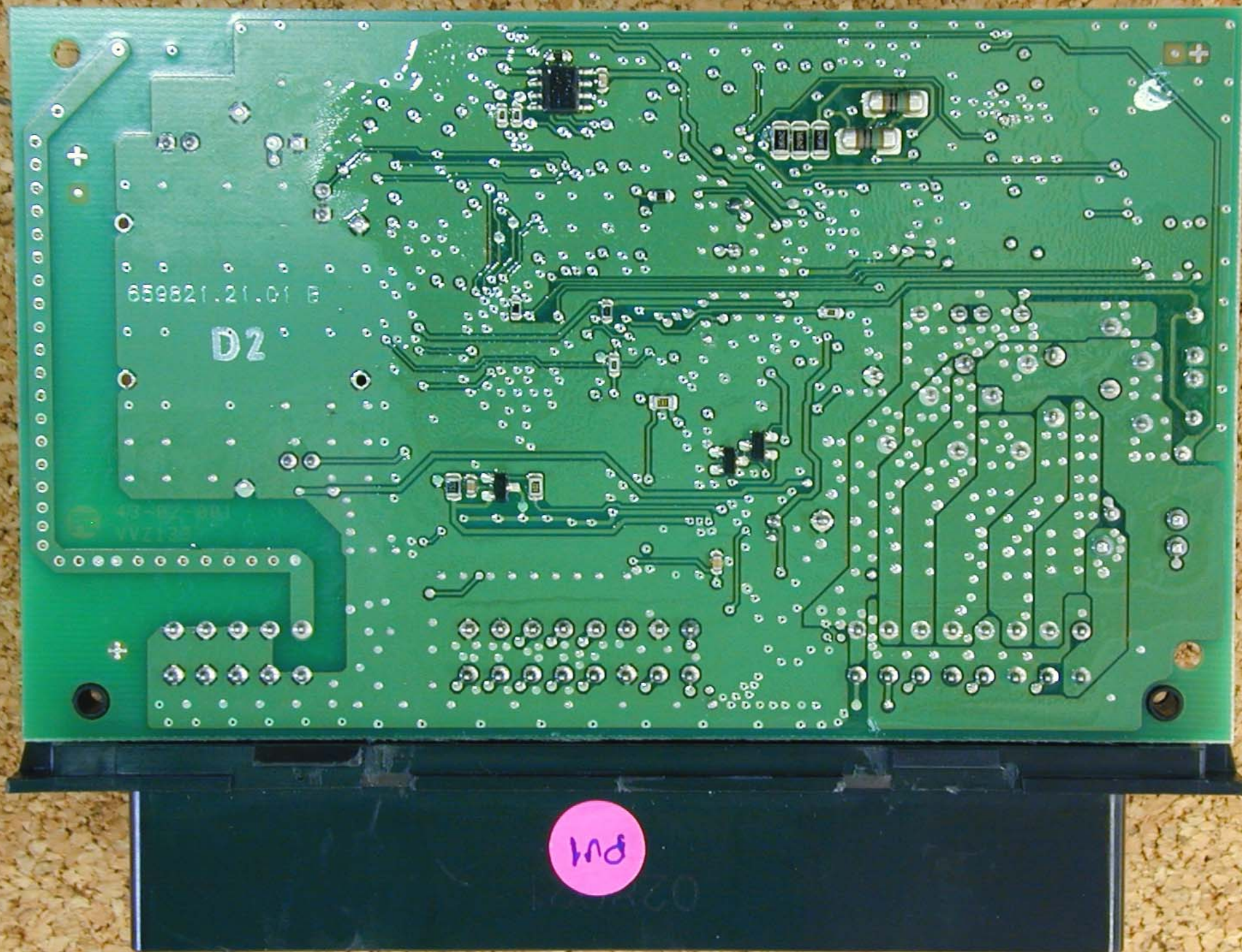
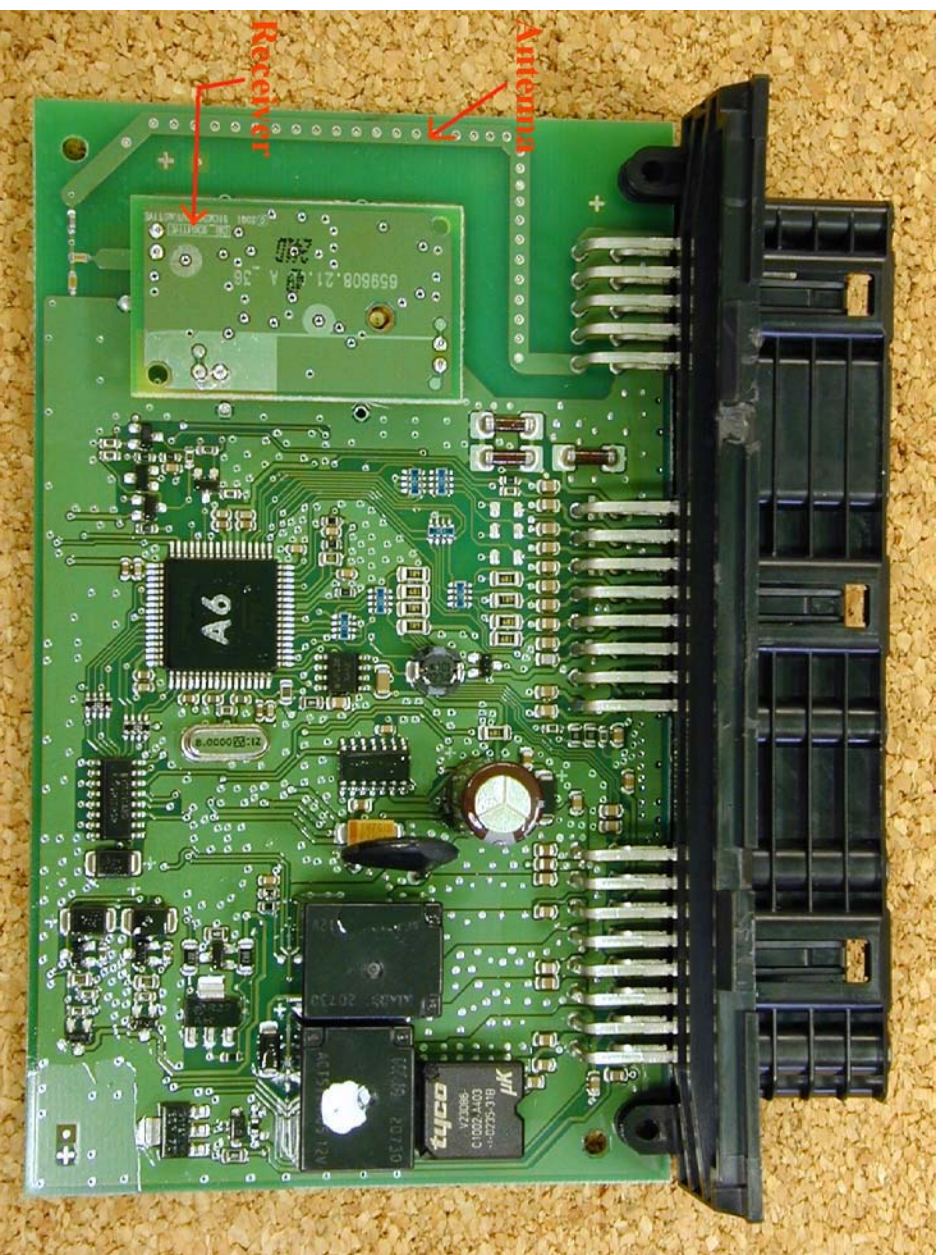




Circuit Board Top

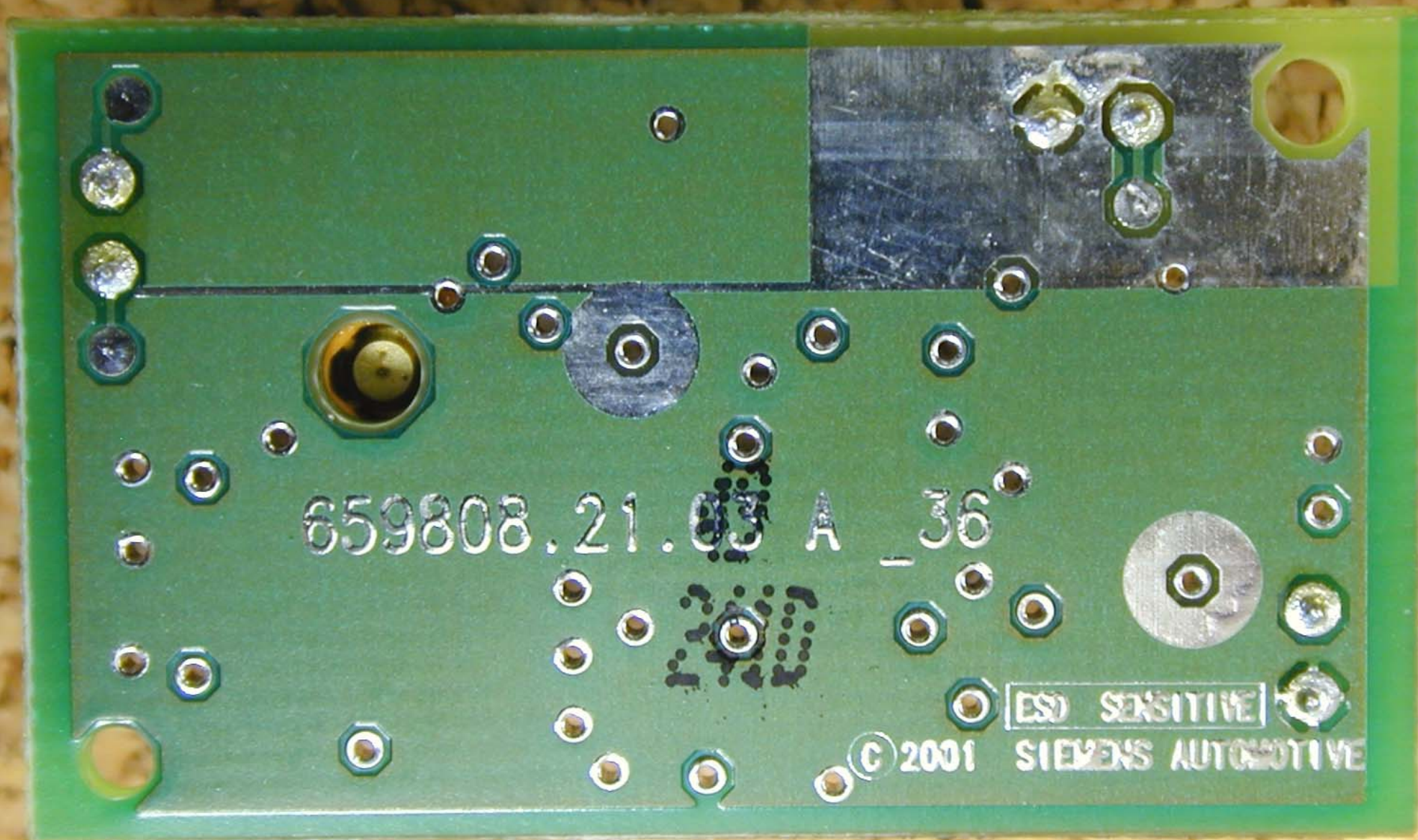


Circuit Board Bottom









Daughterboard Bottom