

Conducted Output Power Plots

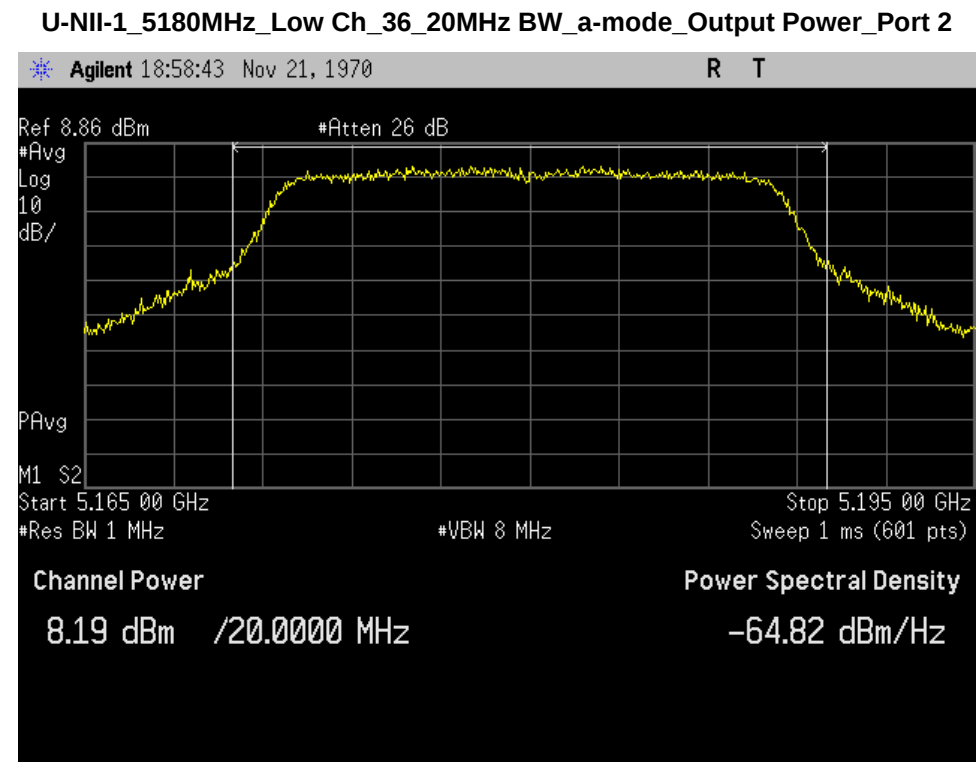
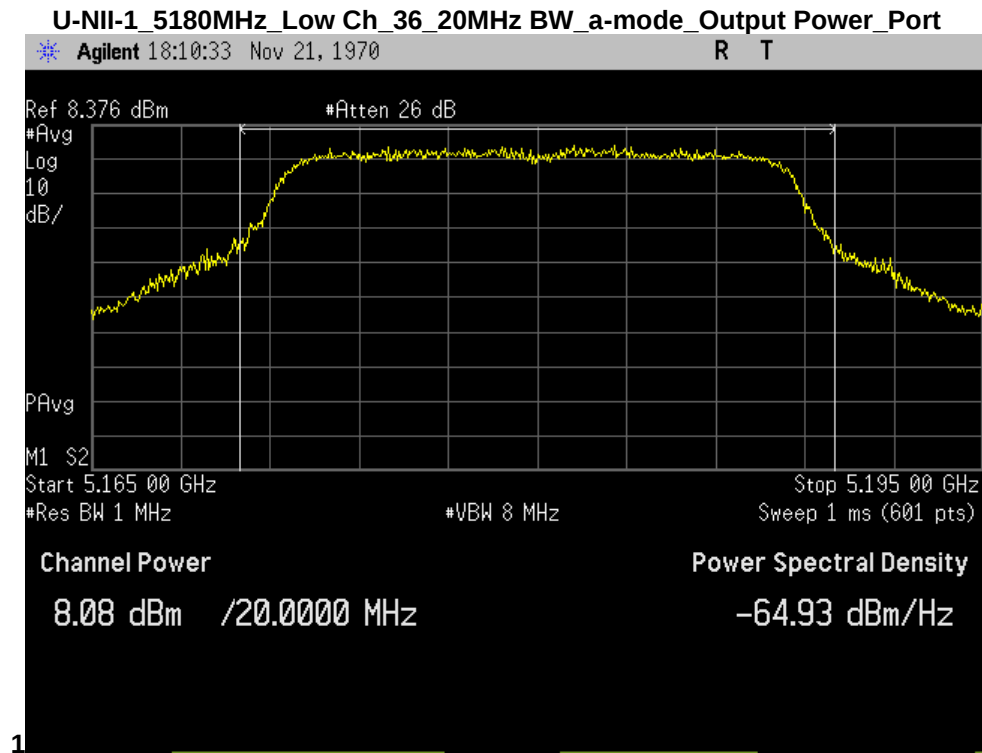
The following are conducted output power plots (port 1 and port 2) for report number WIR118717-FCC407 RSS247.

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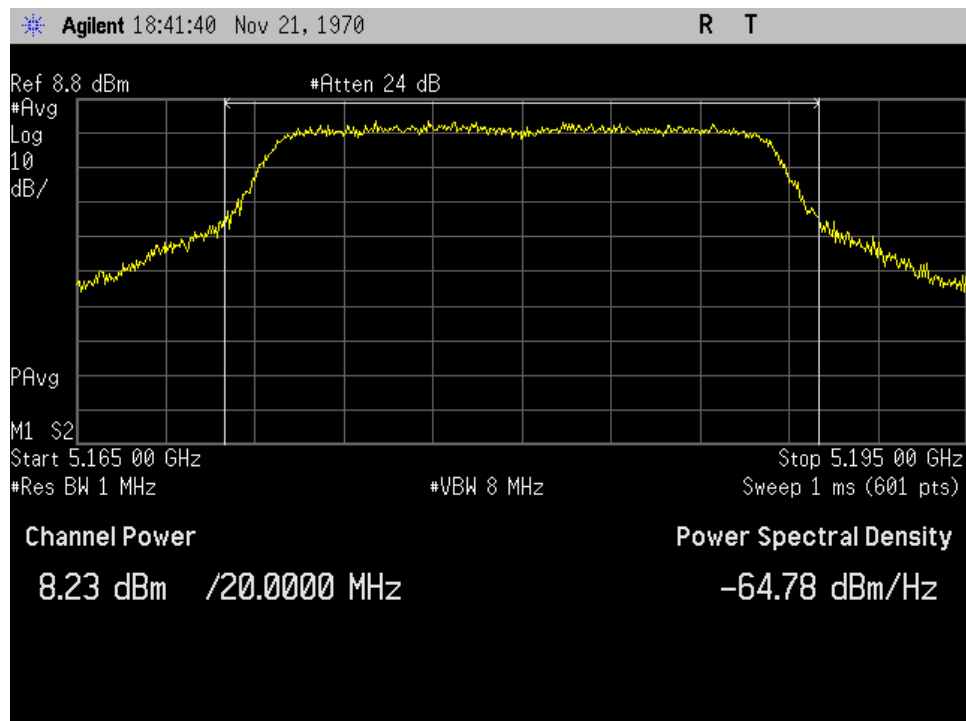
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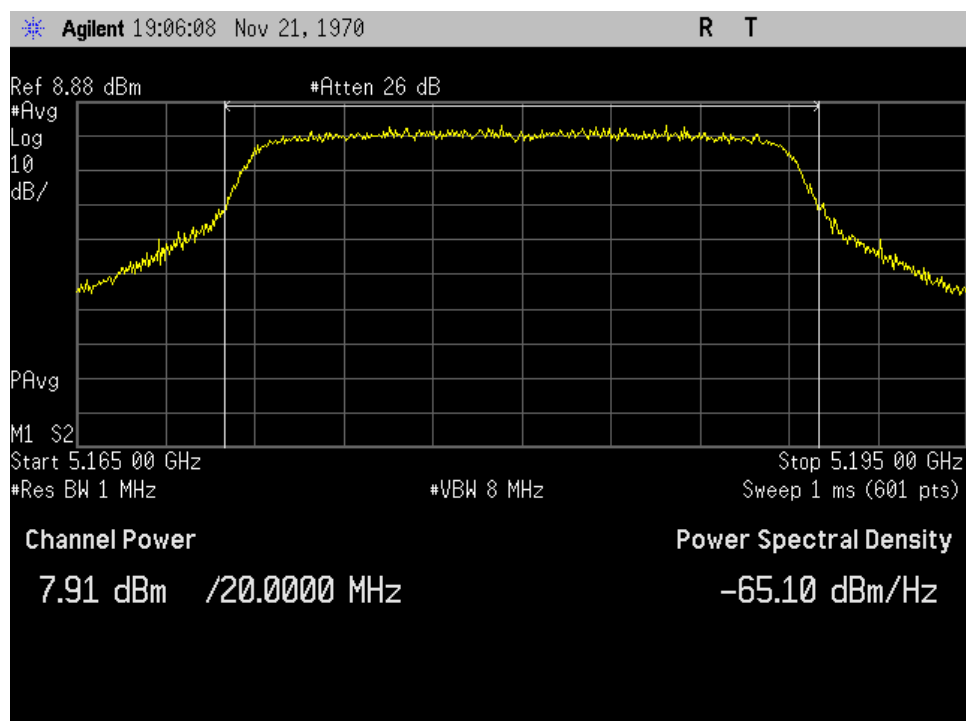
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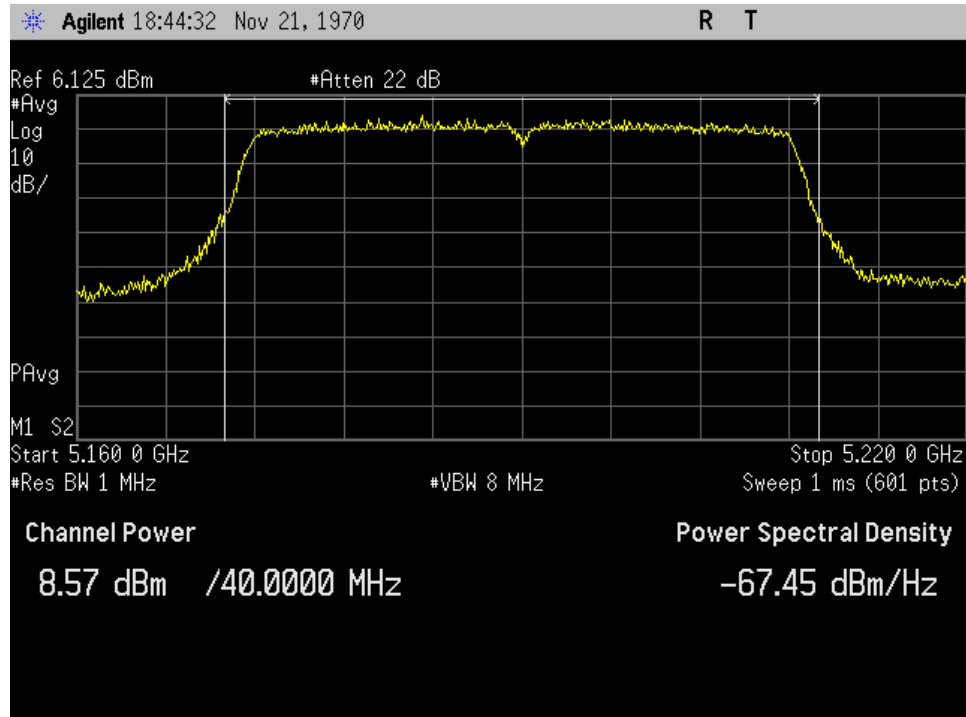
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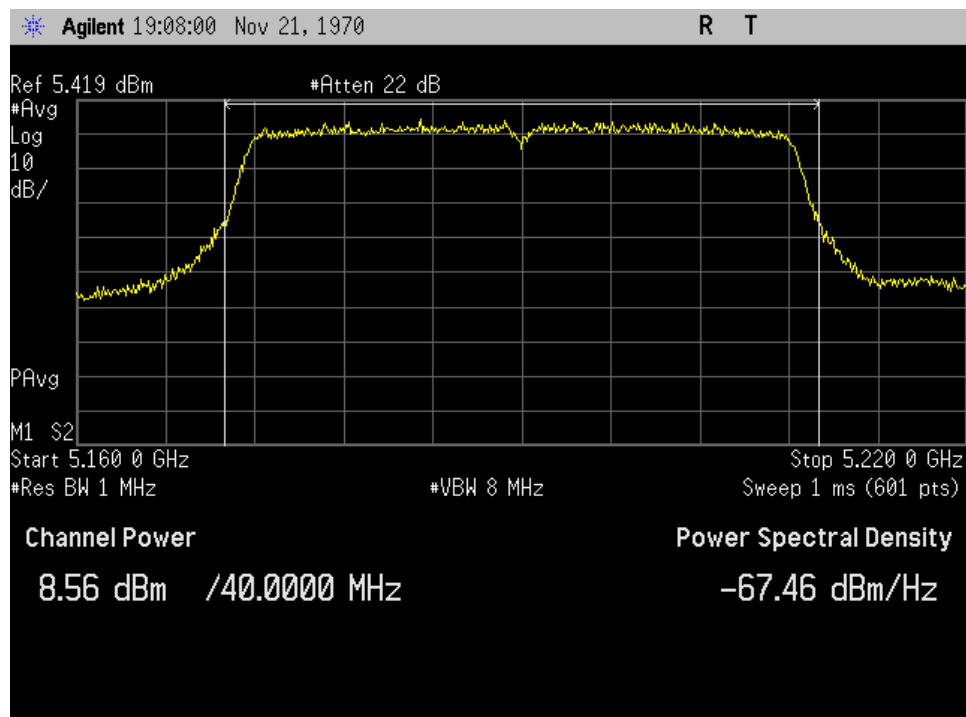
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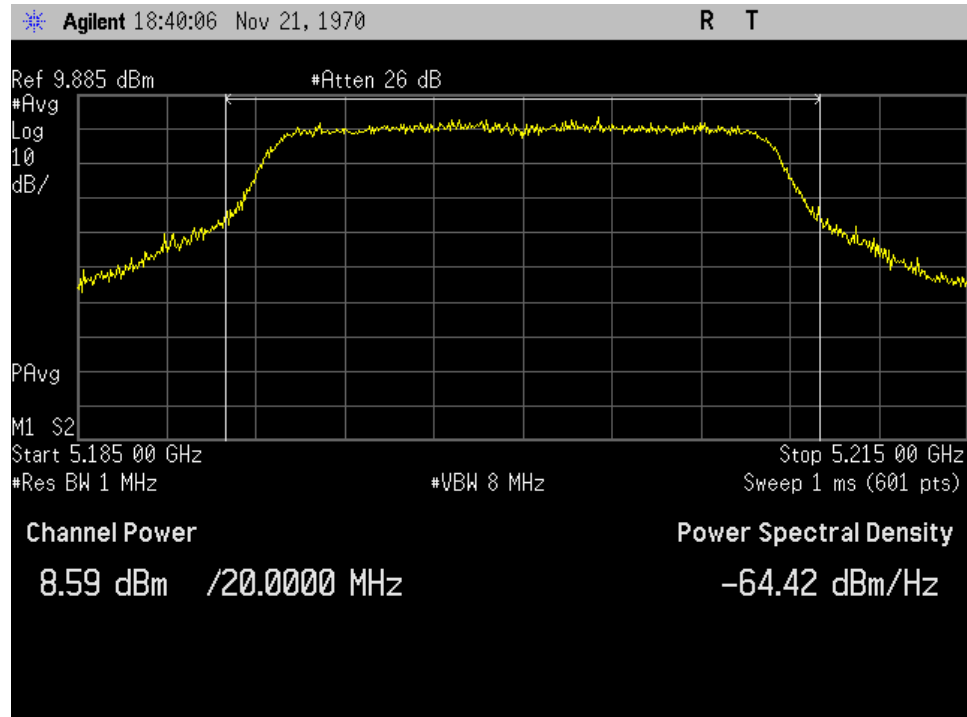
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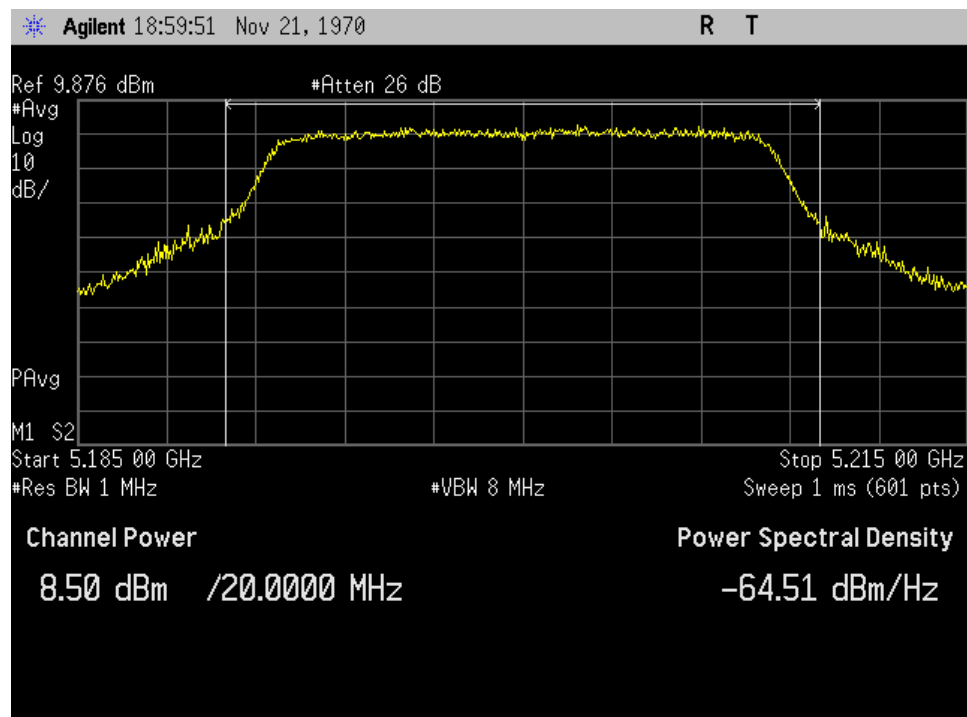
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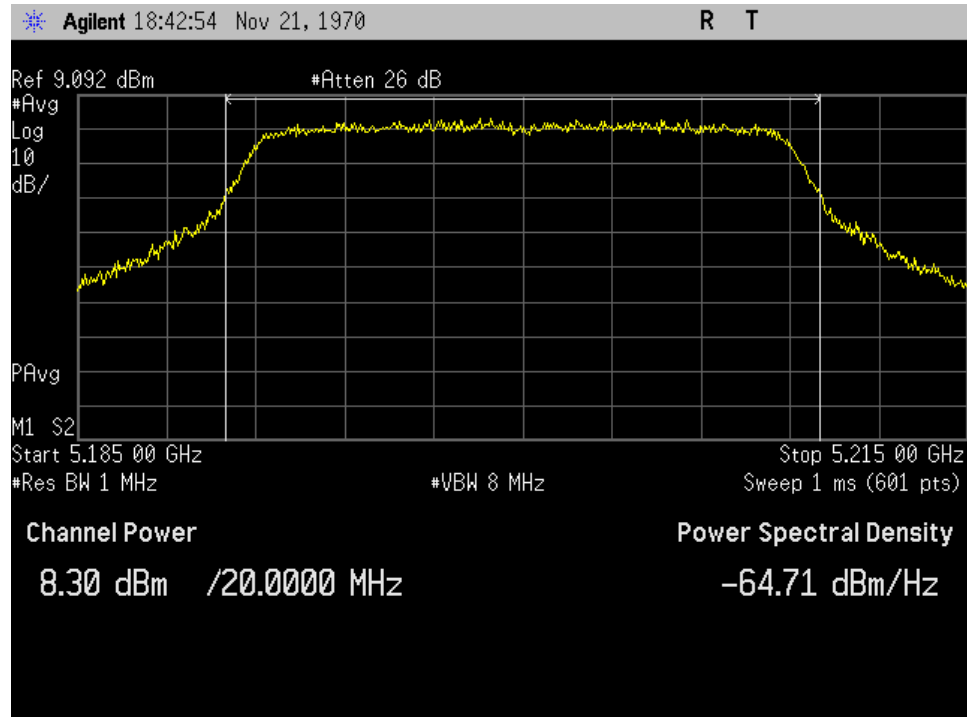
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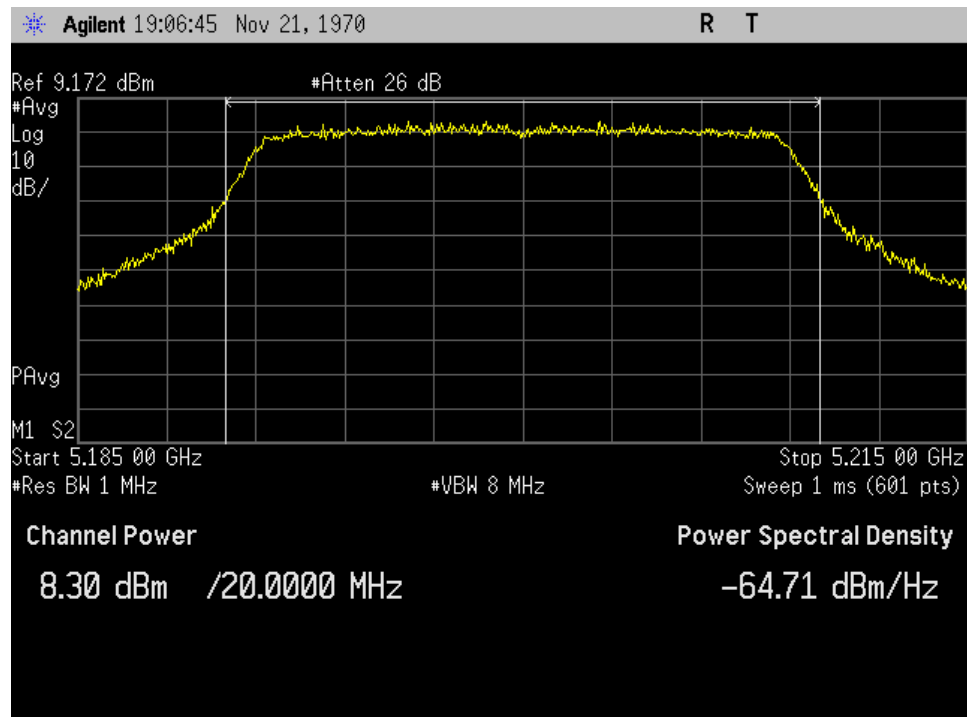
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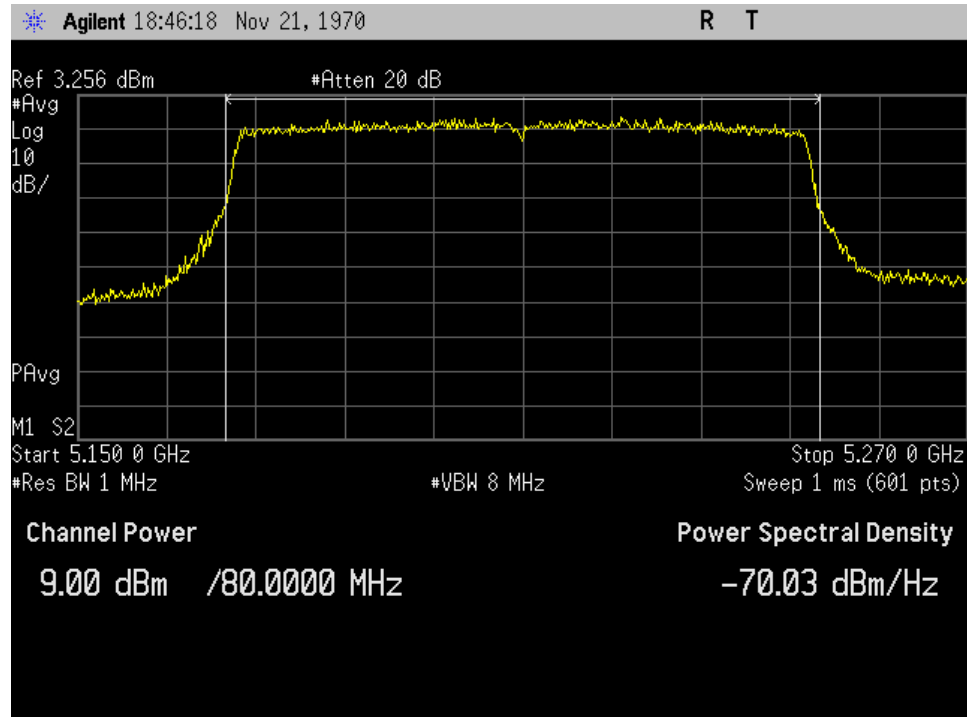
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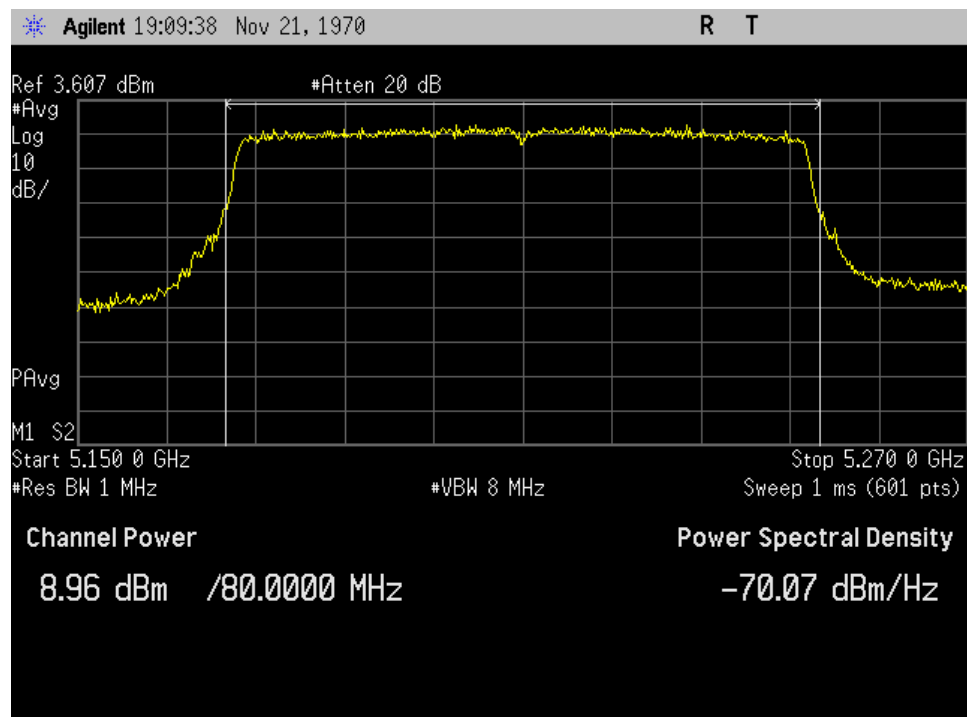
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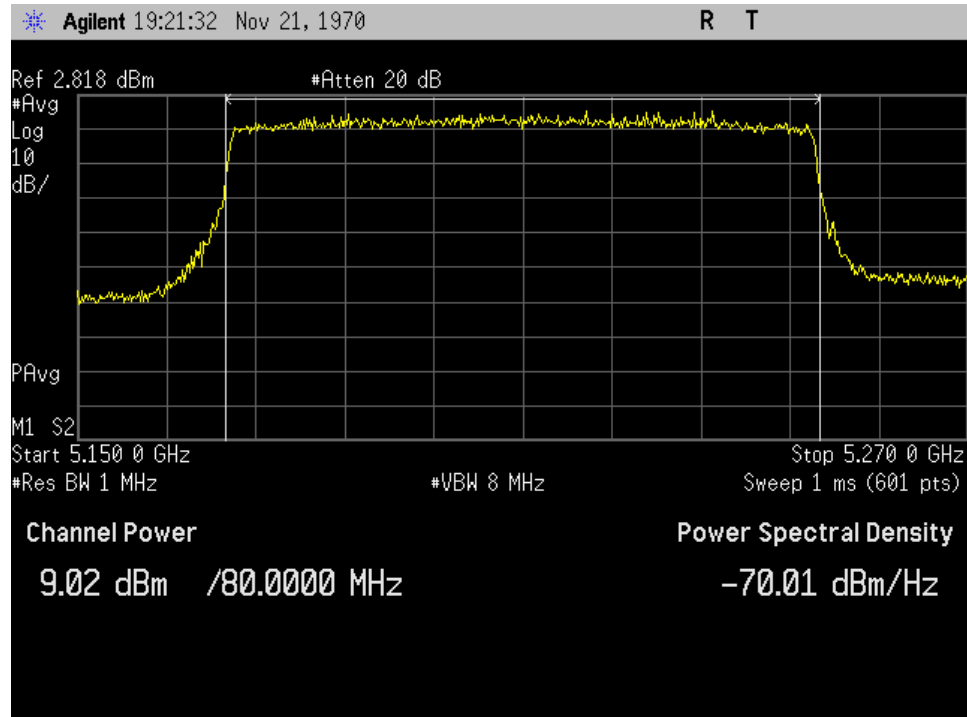
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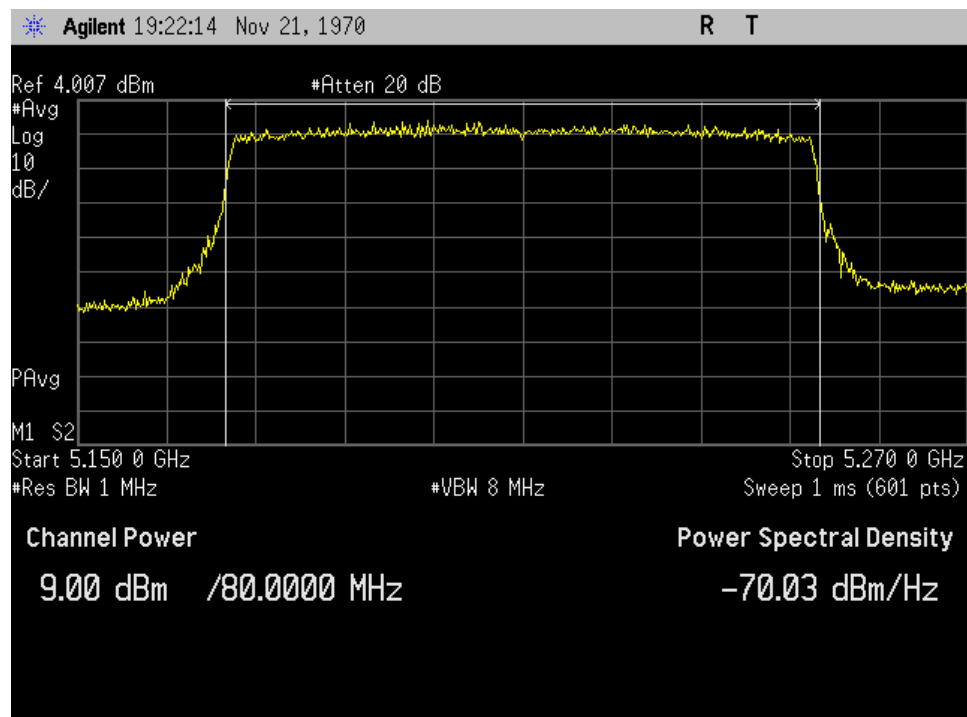
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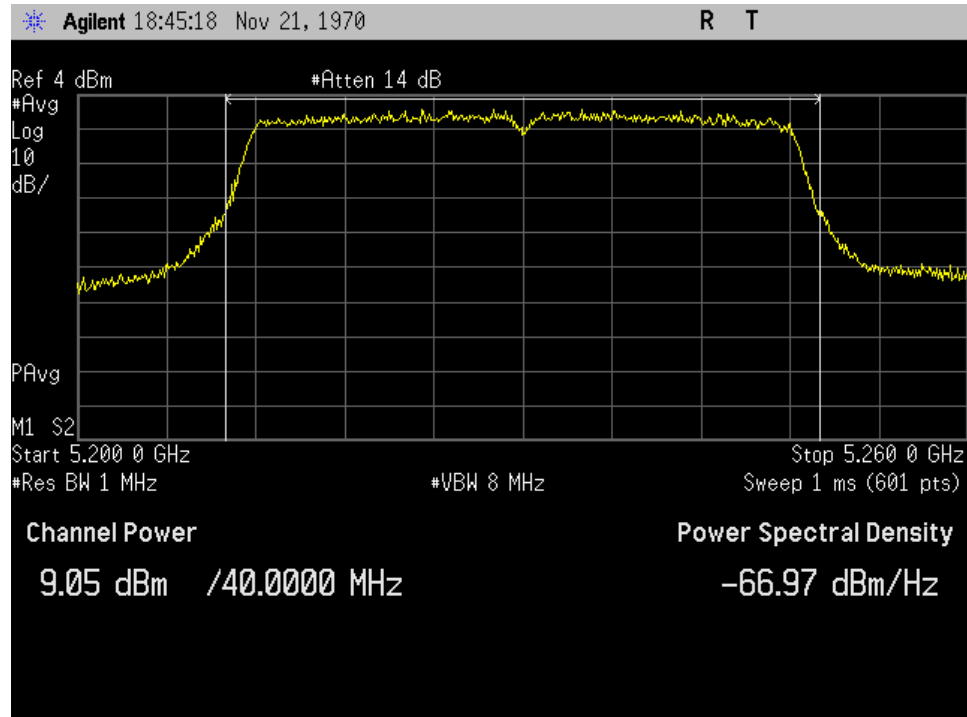
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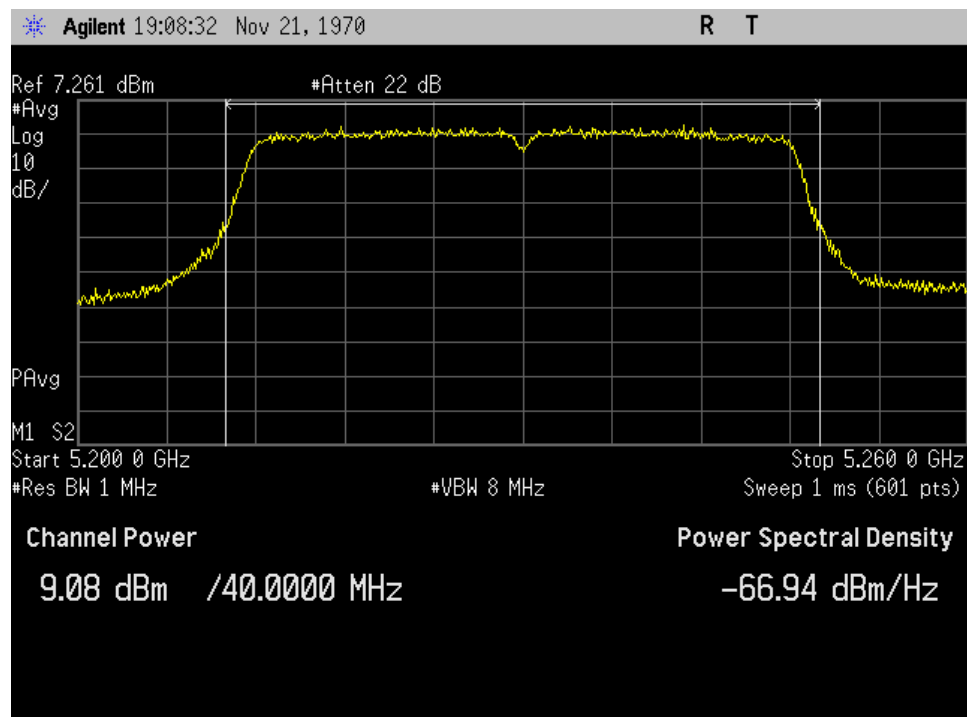
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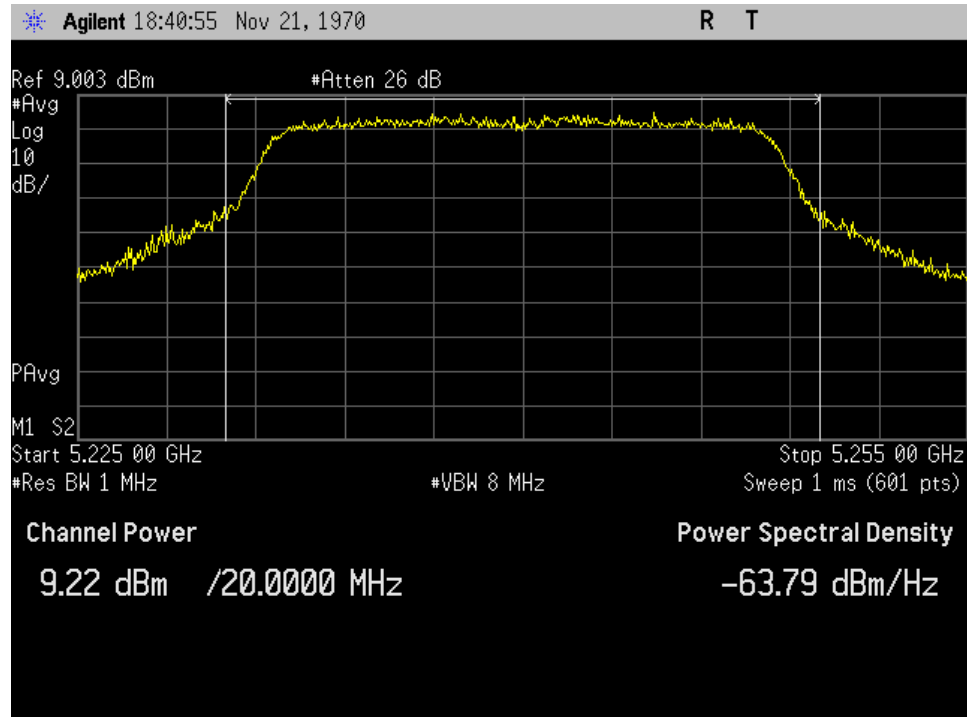
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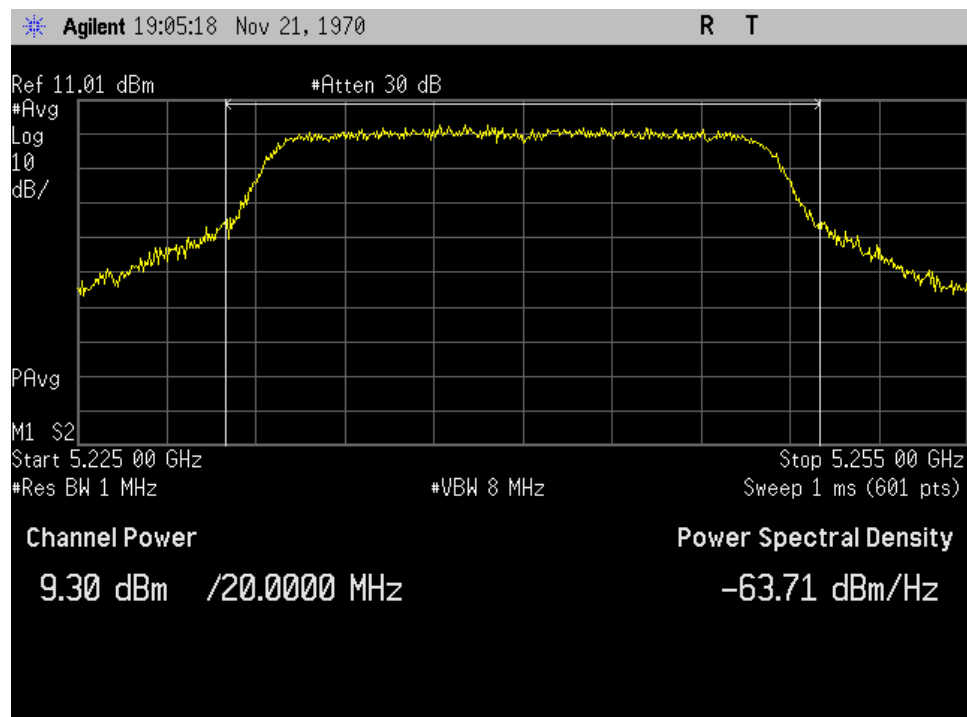
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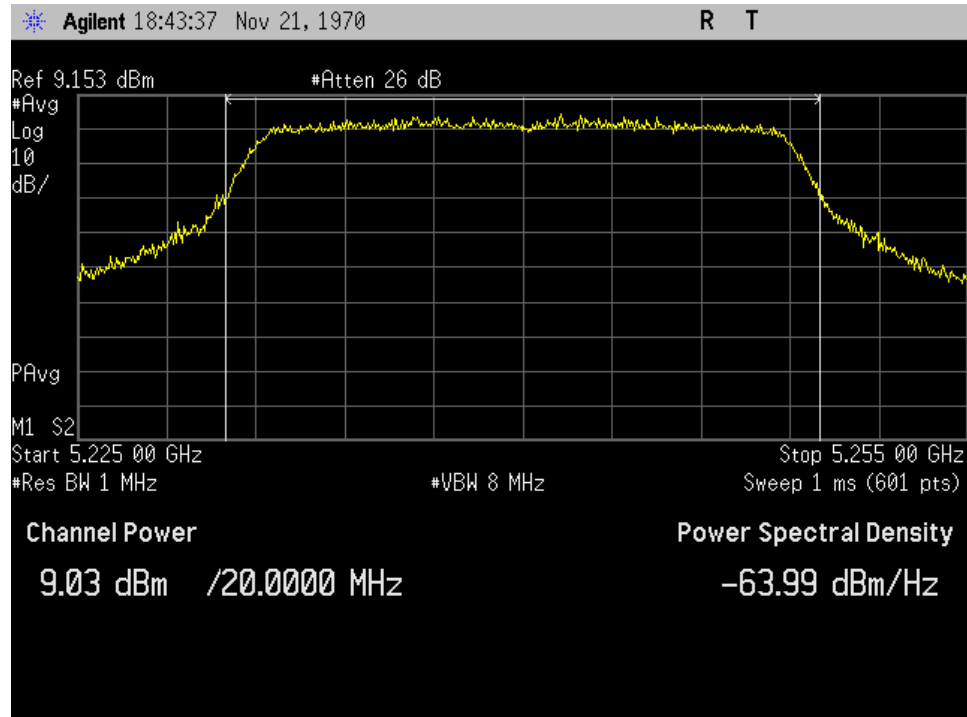
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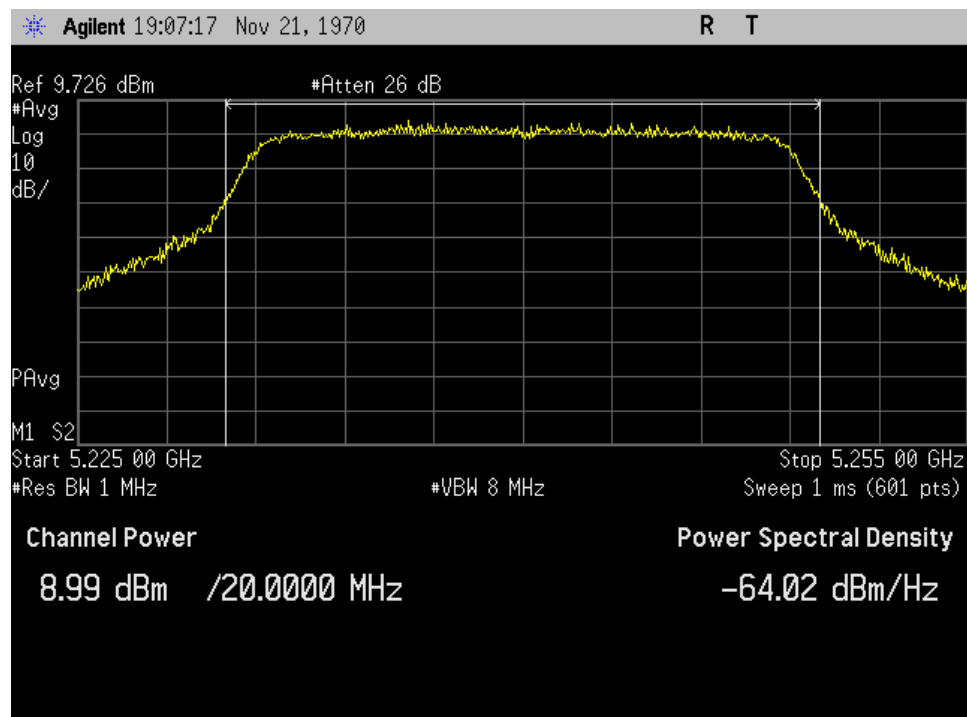
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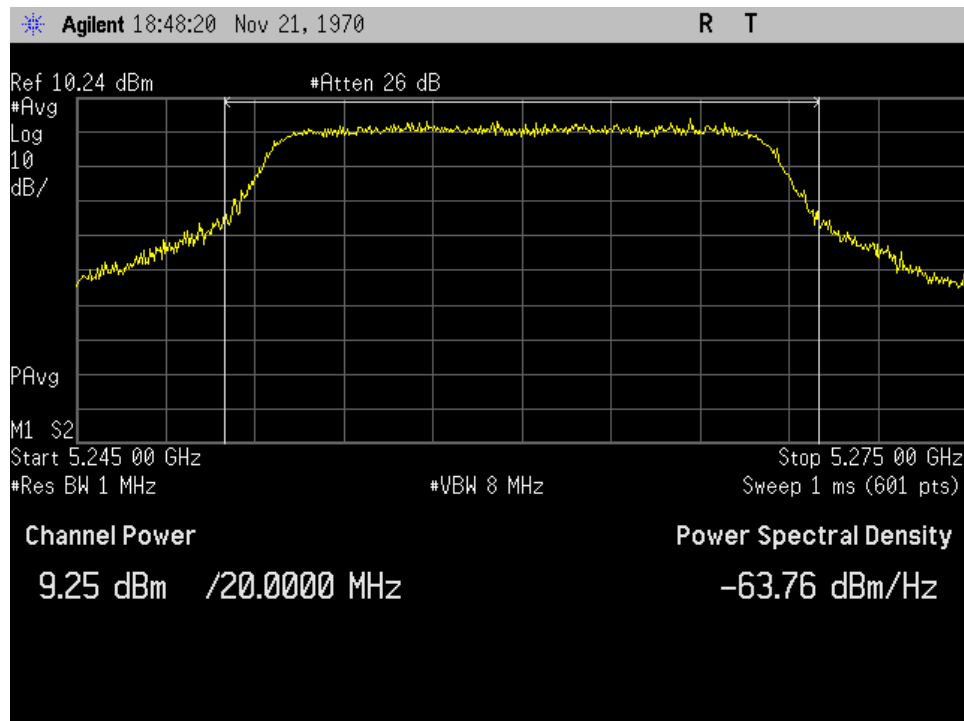


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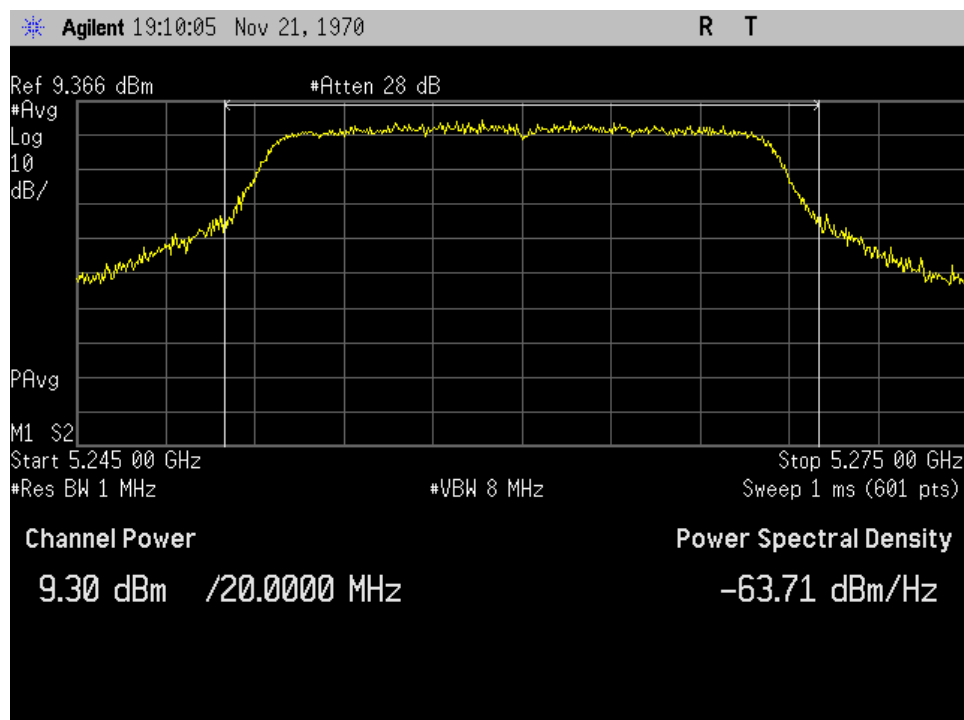


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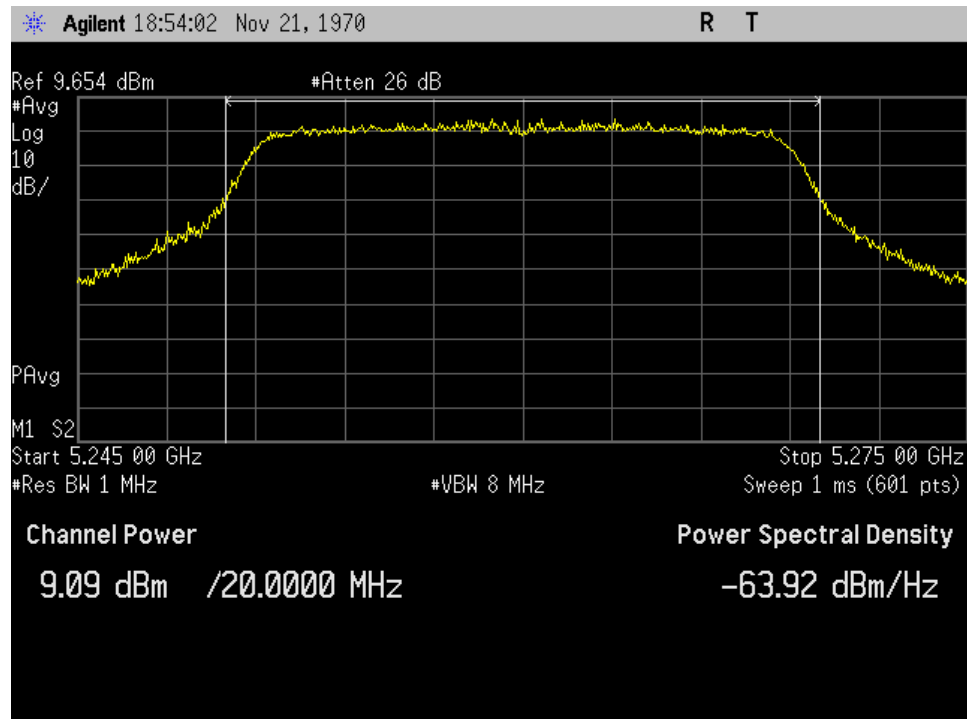
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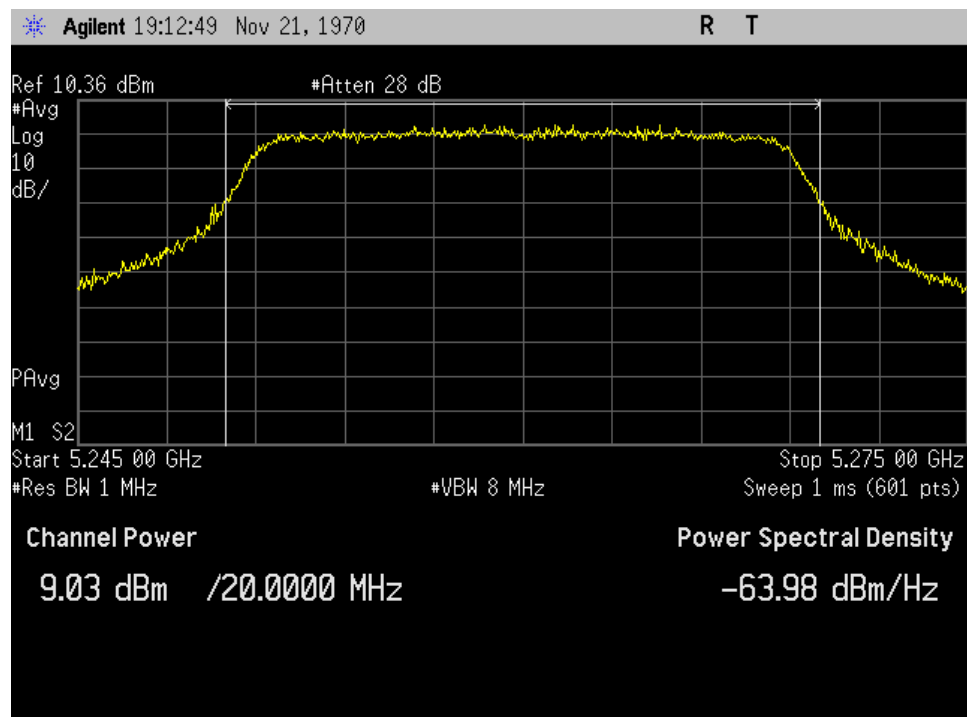
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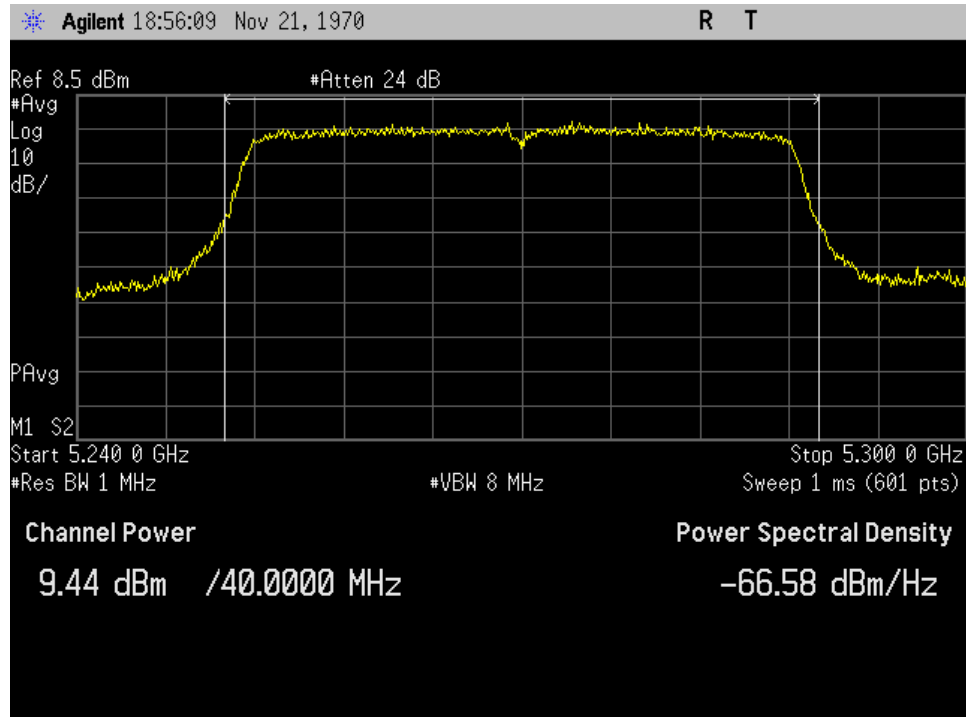
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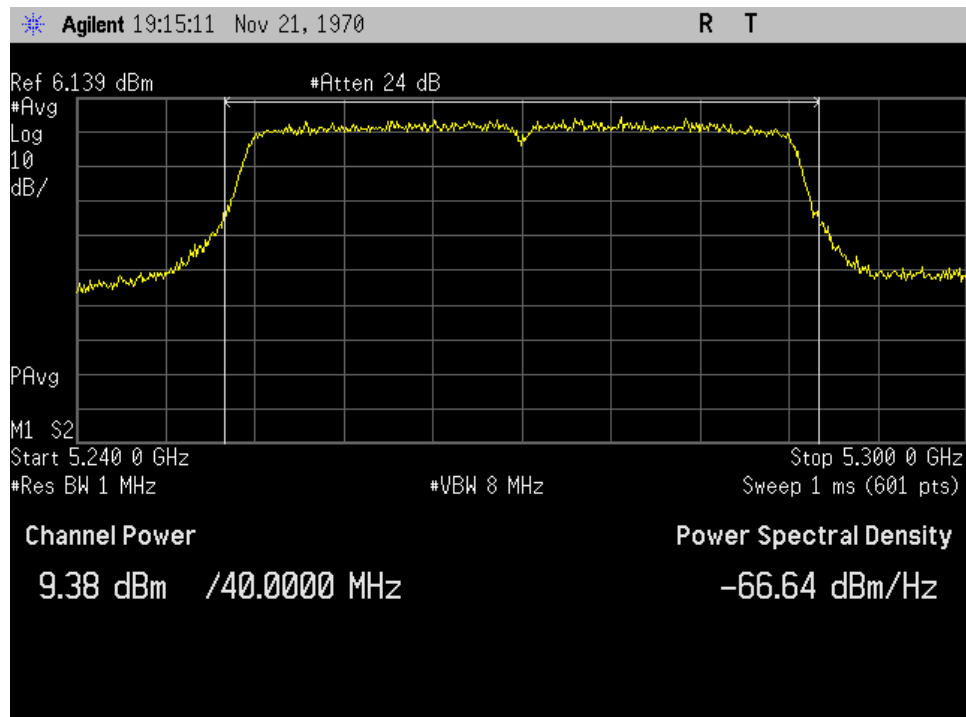
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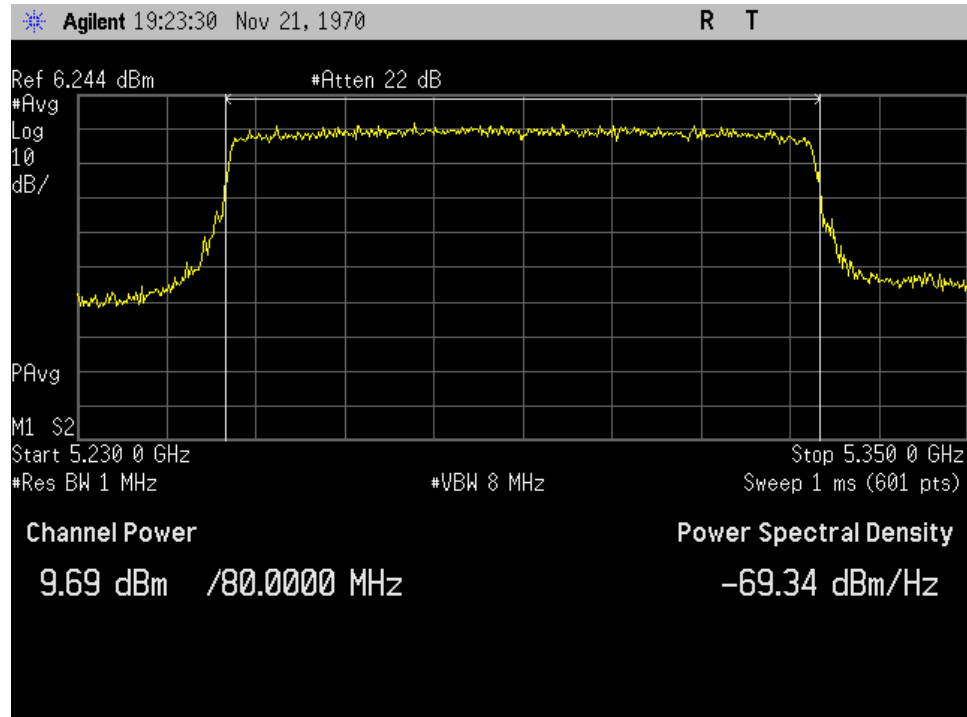
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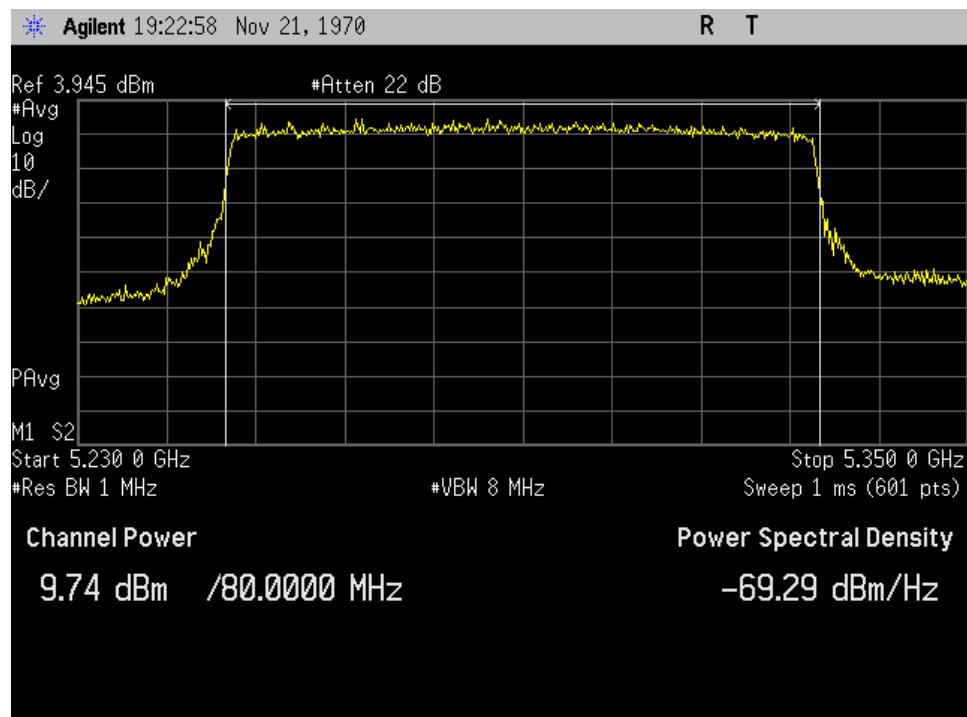
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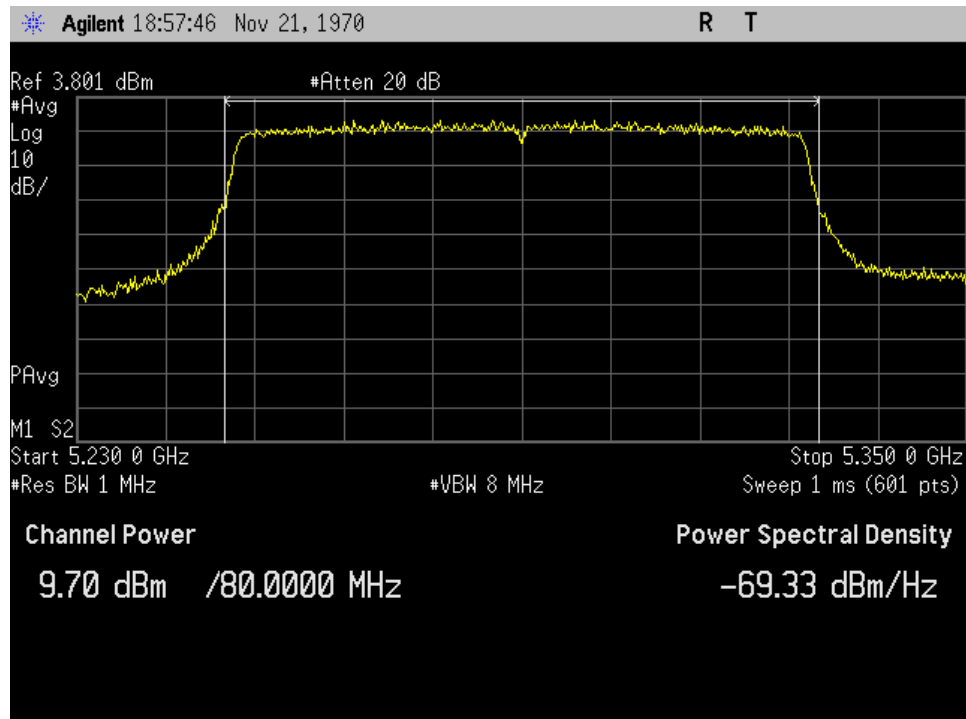
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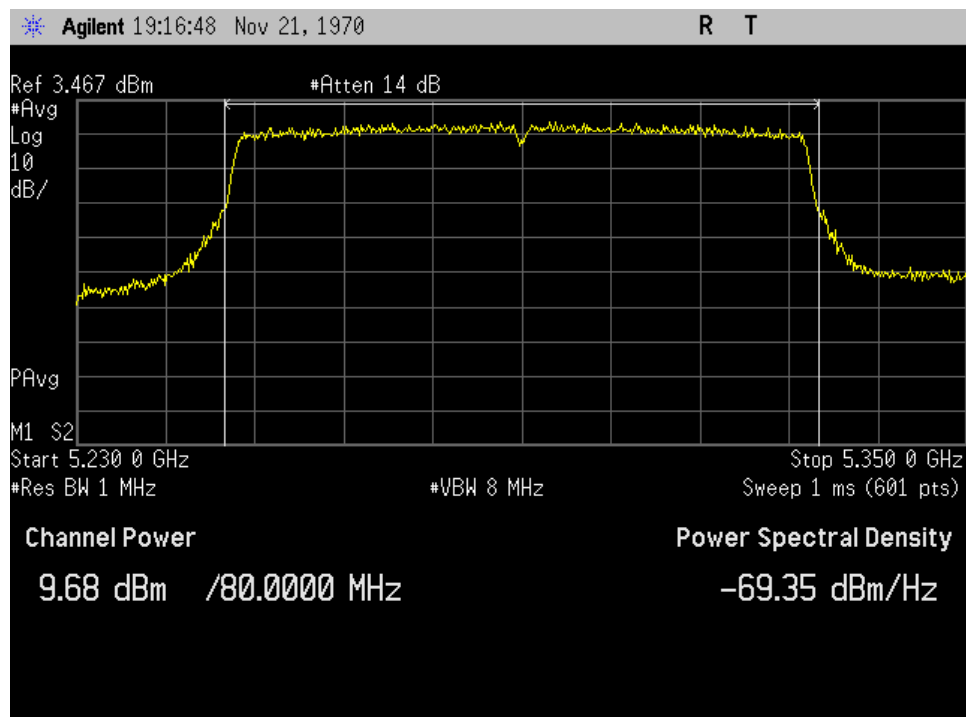
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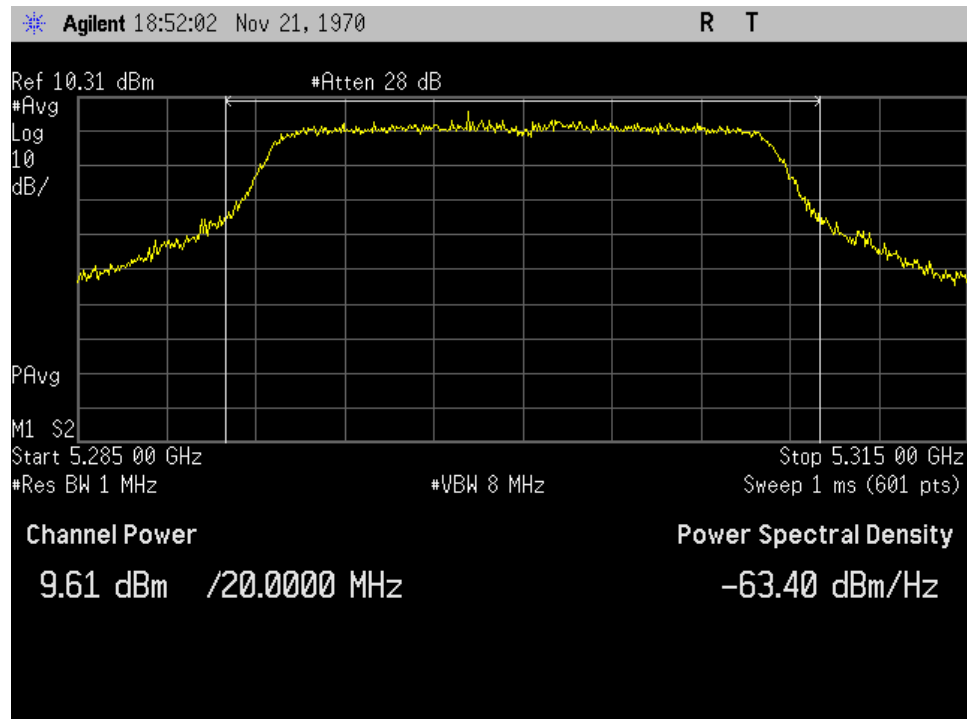
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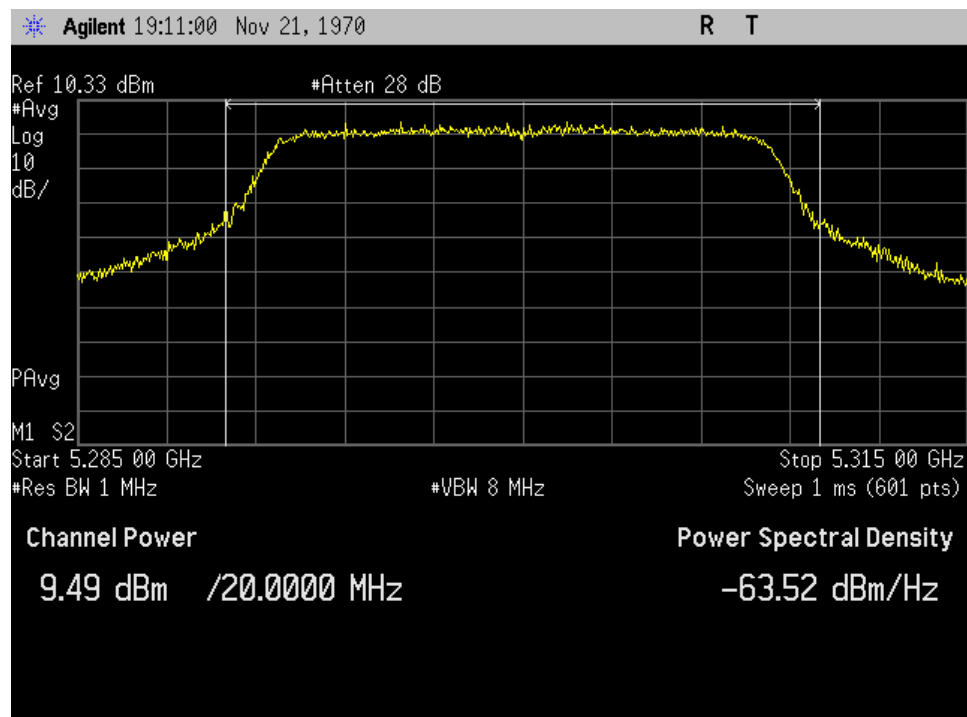
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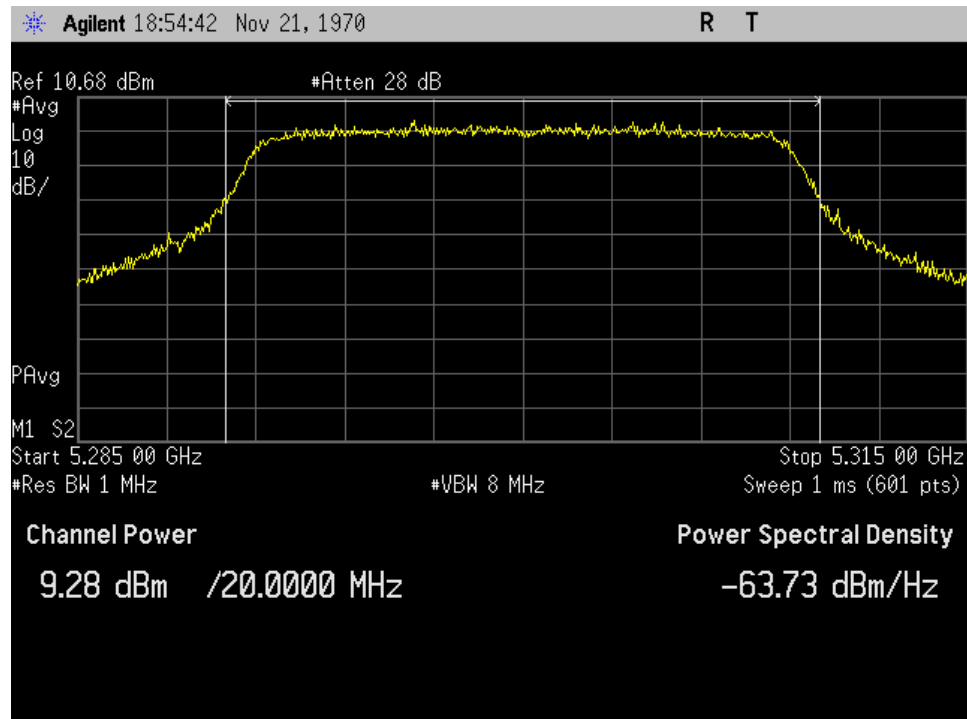
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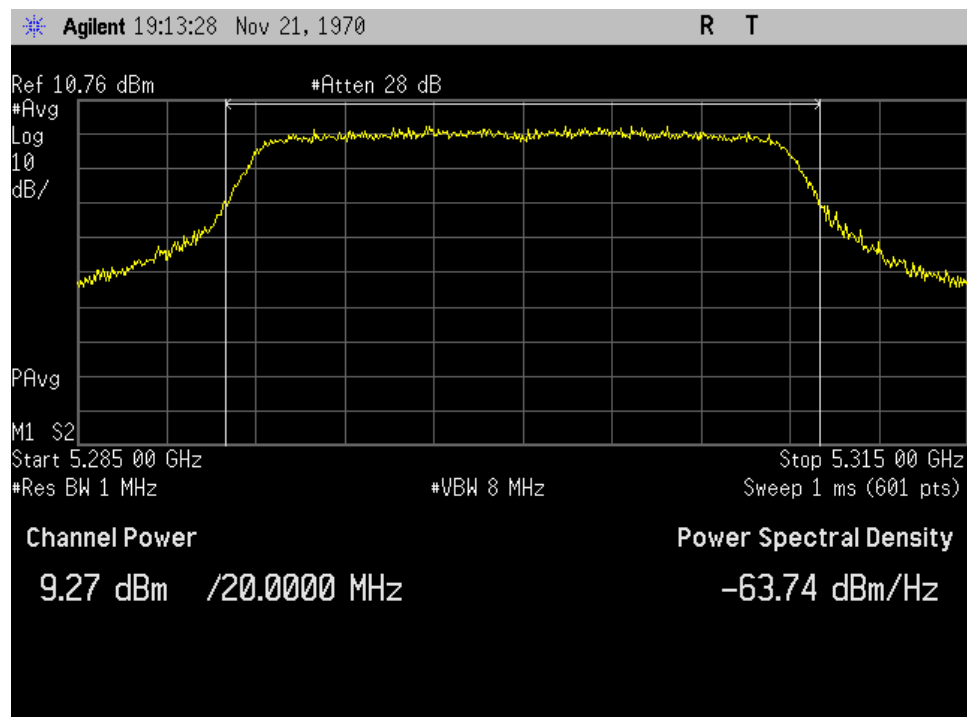
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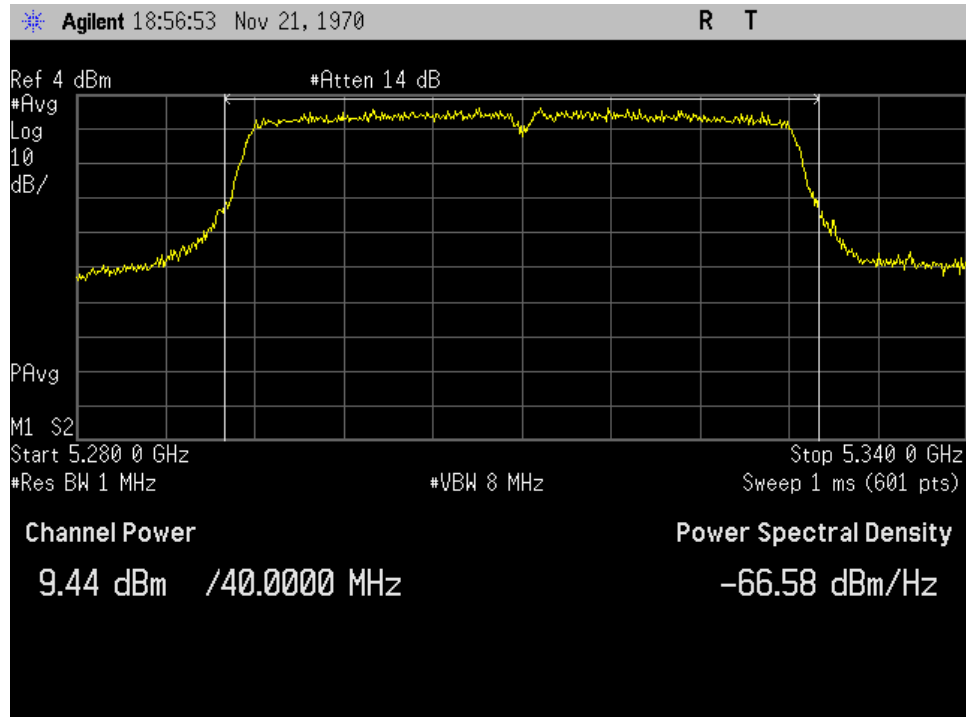
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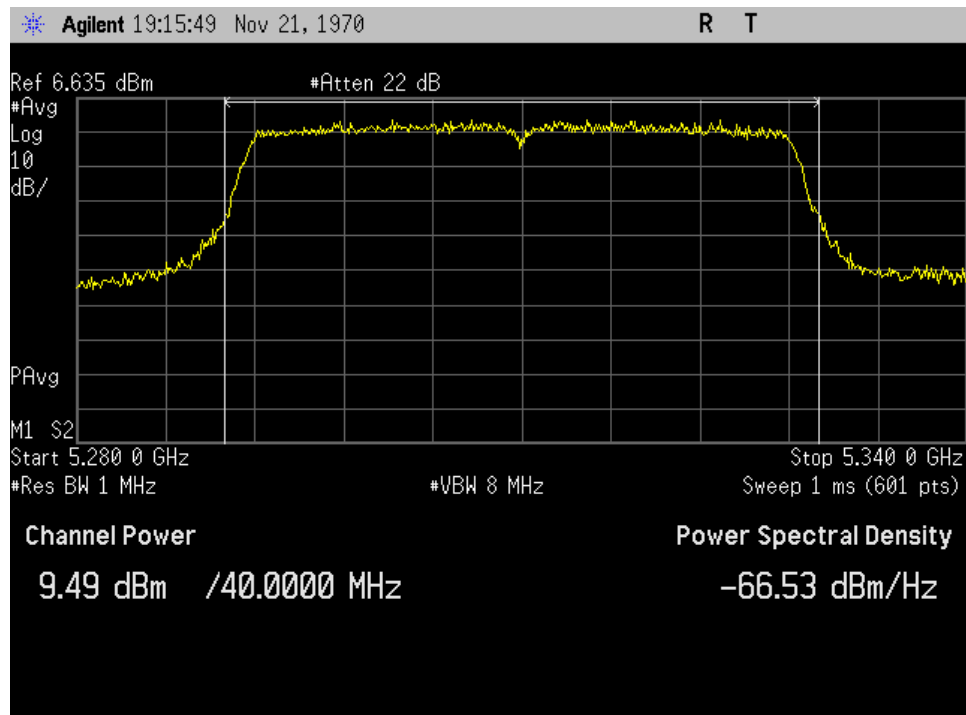
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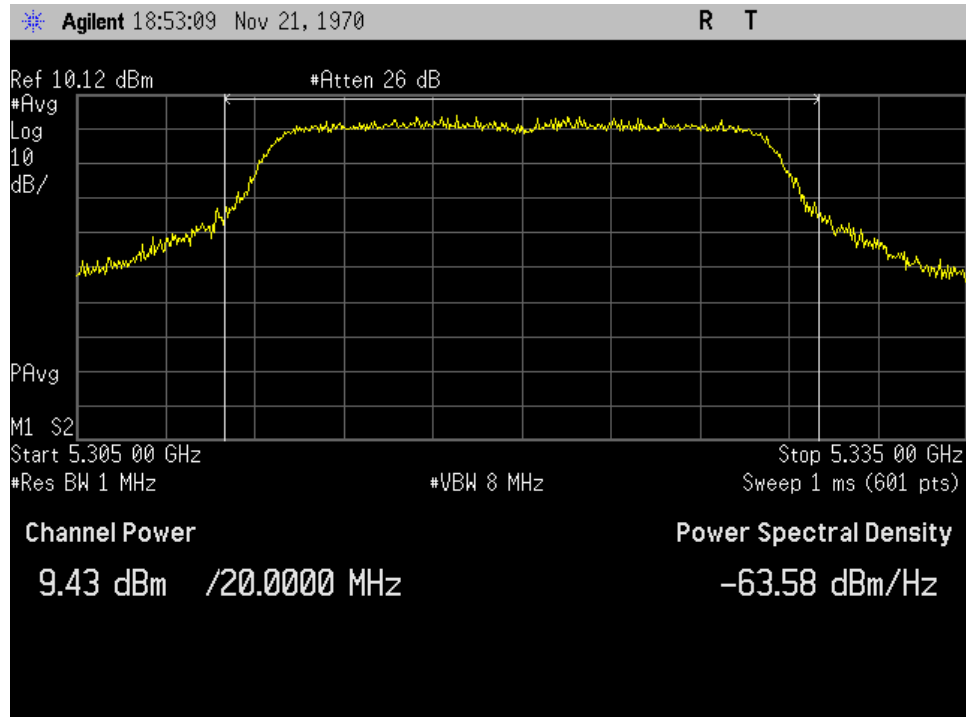
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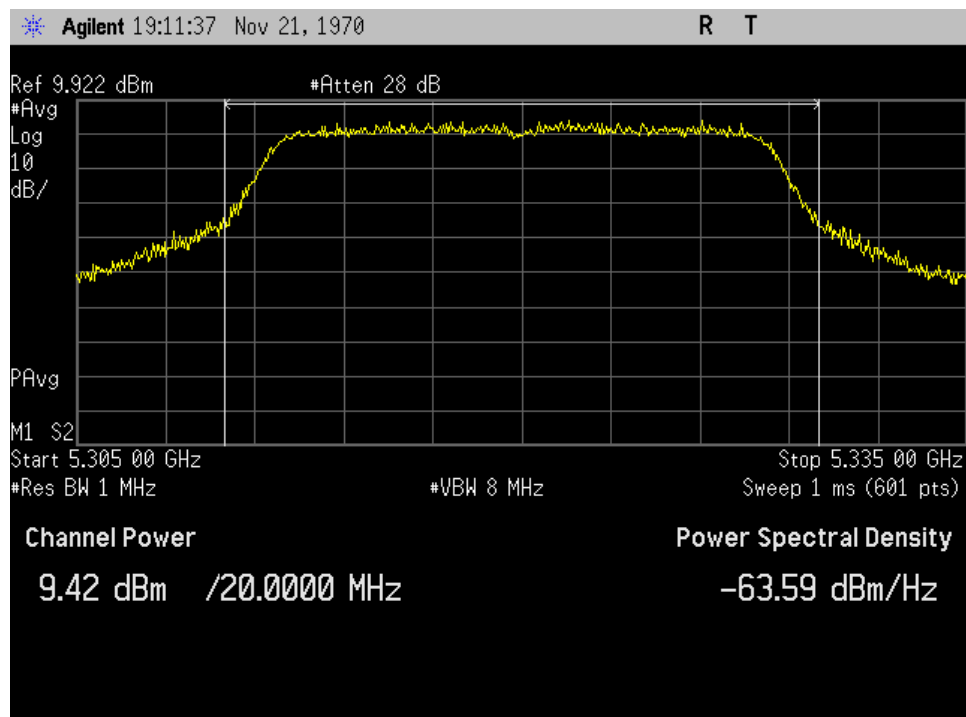
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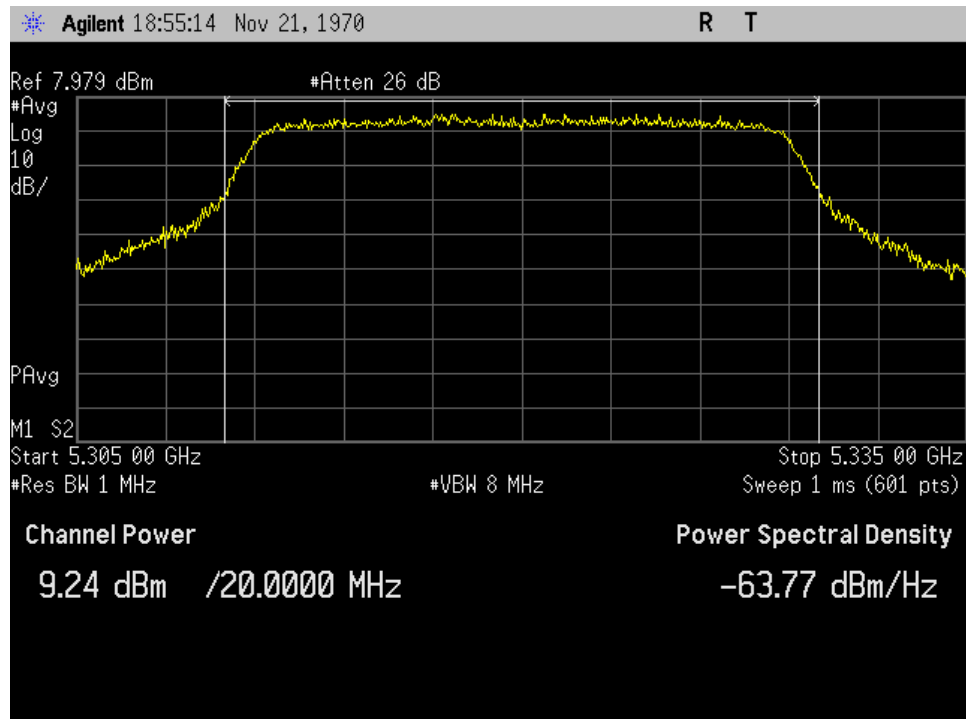
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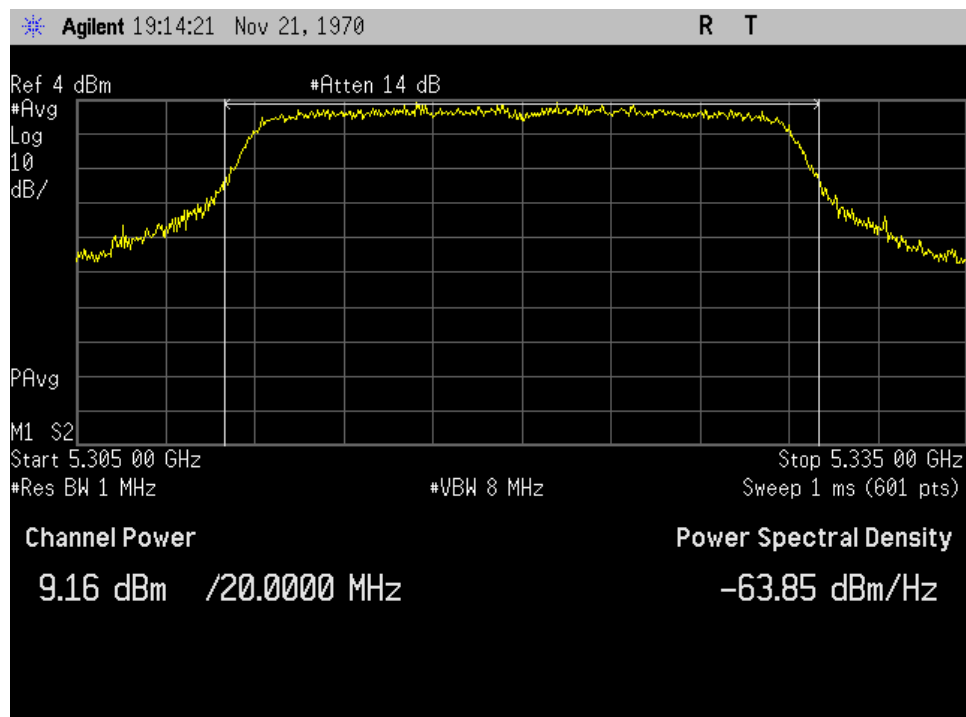
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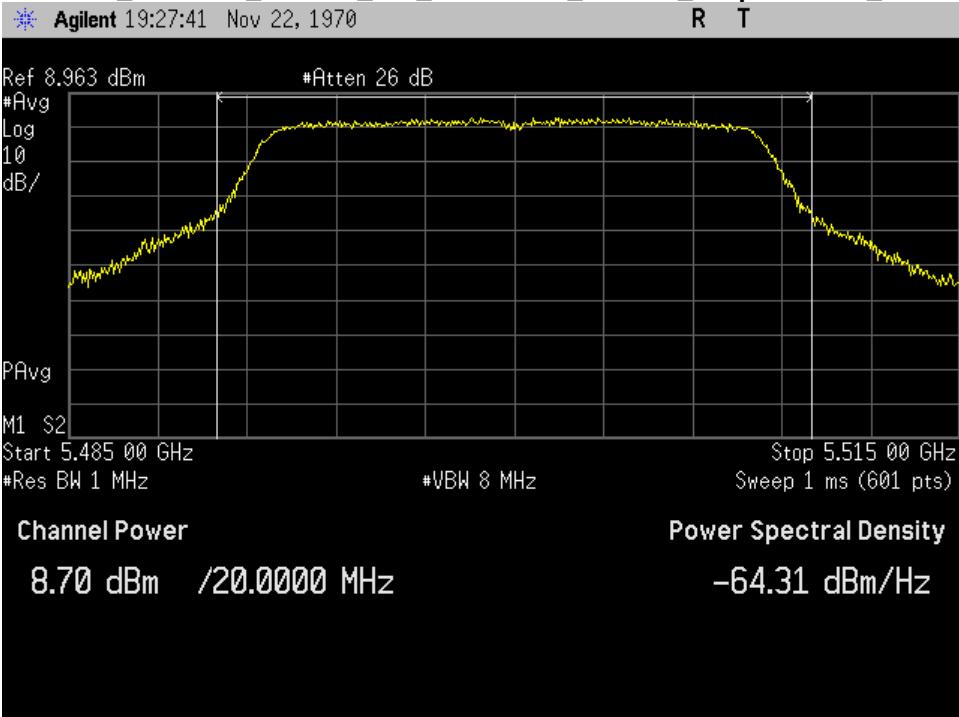


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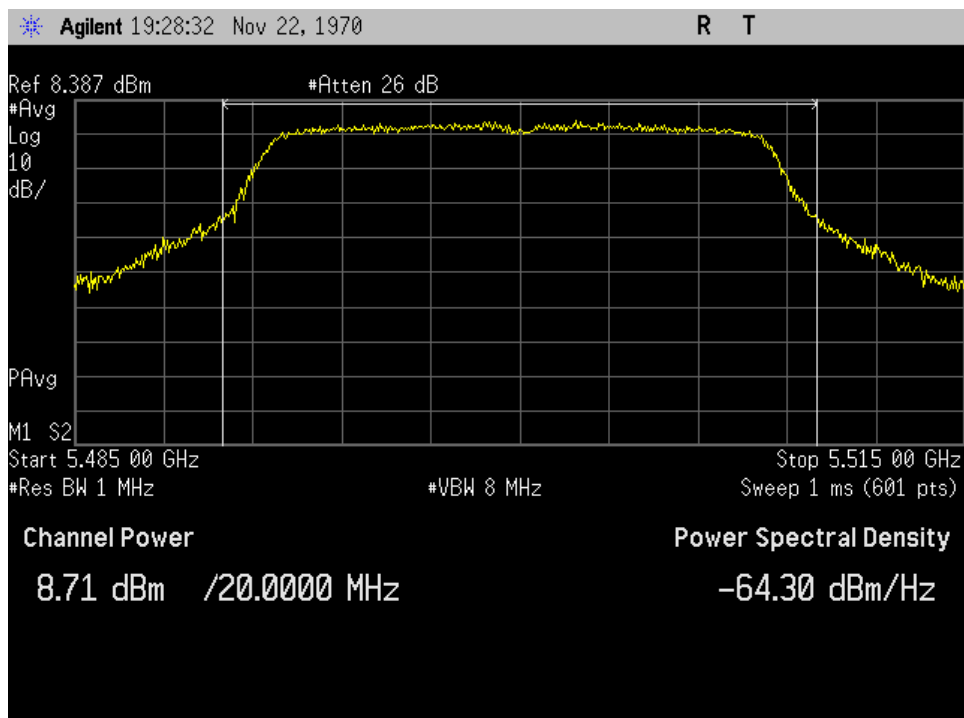


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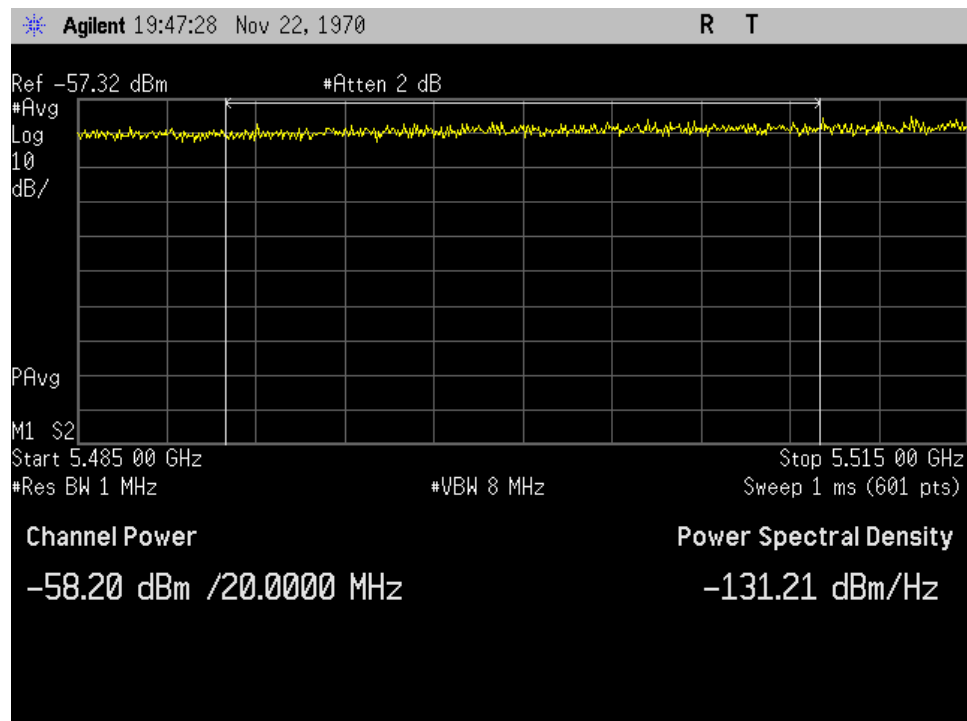
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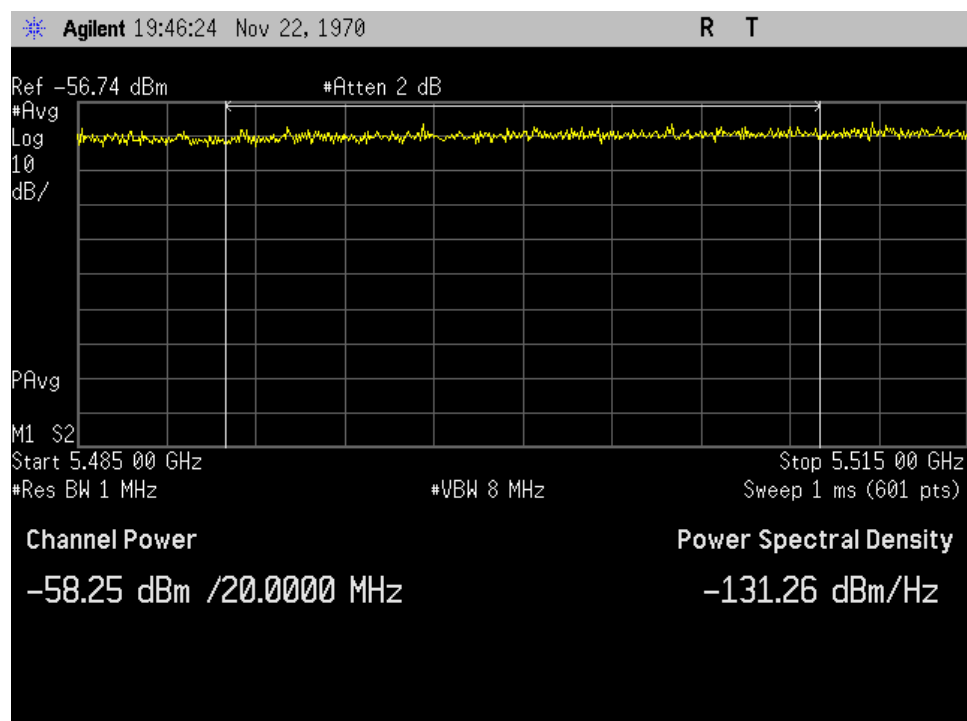
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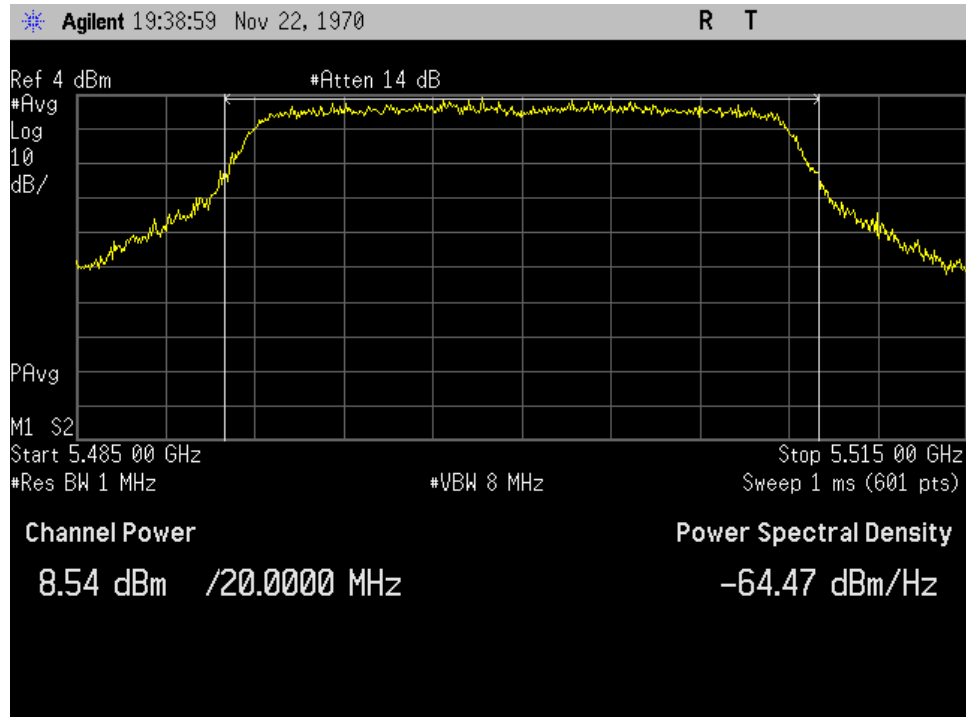
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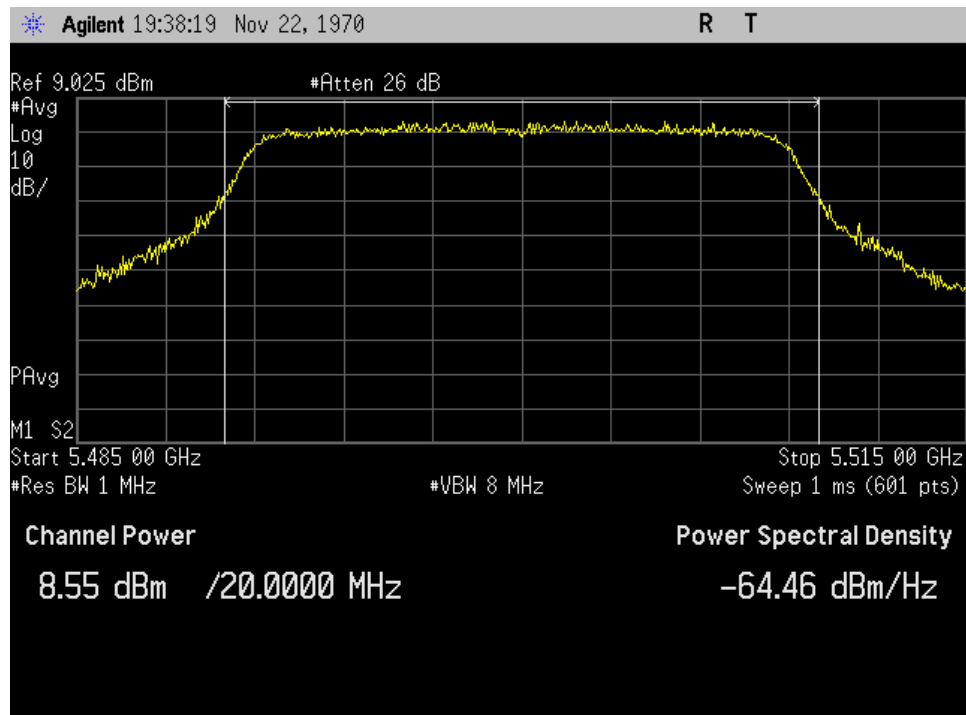
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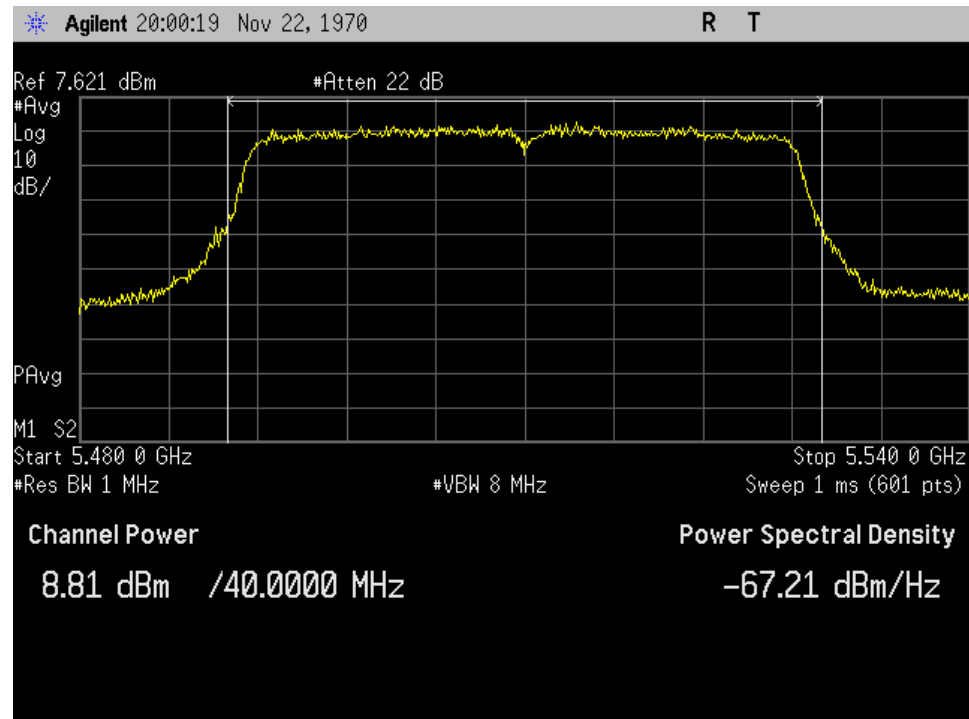
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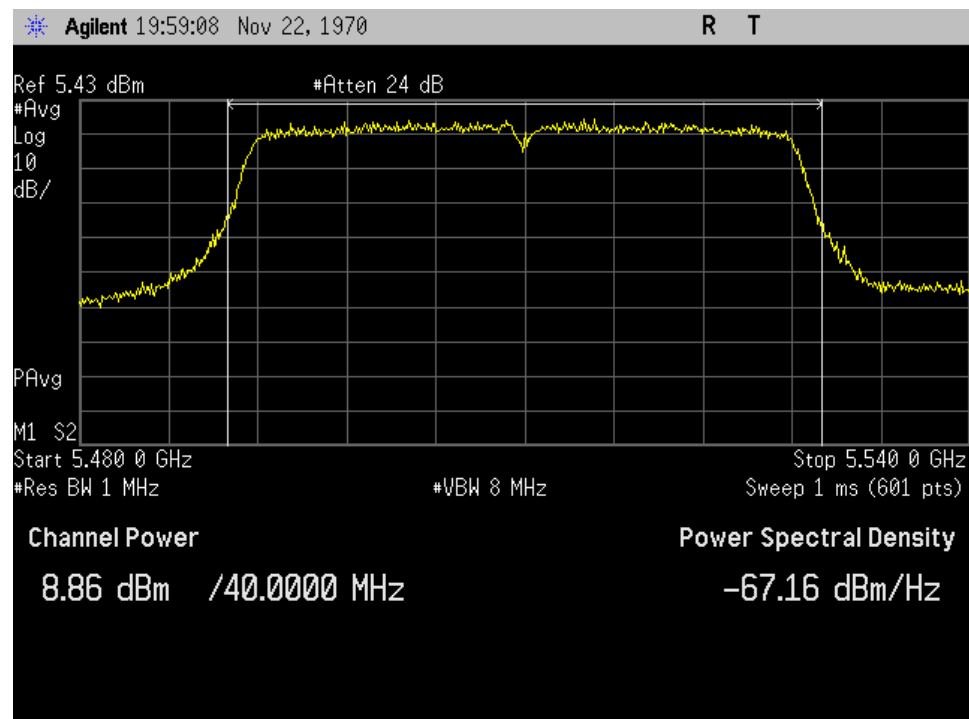
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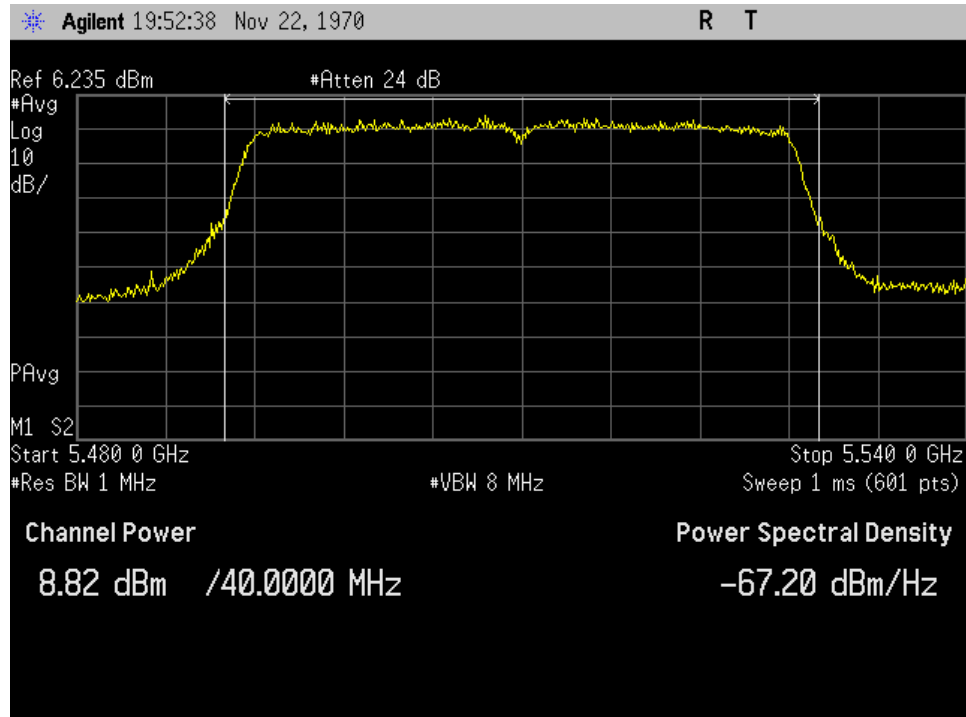
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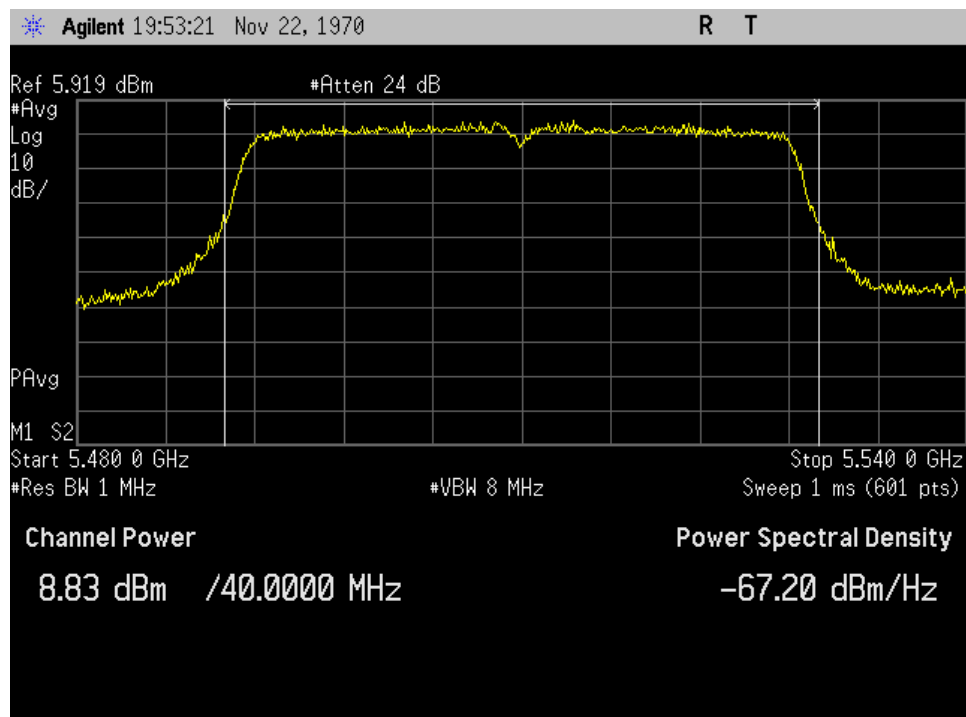
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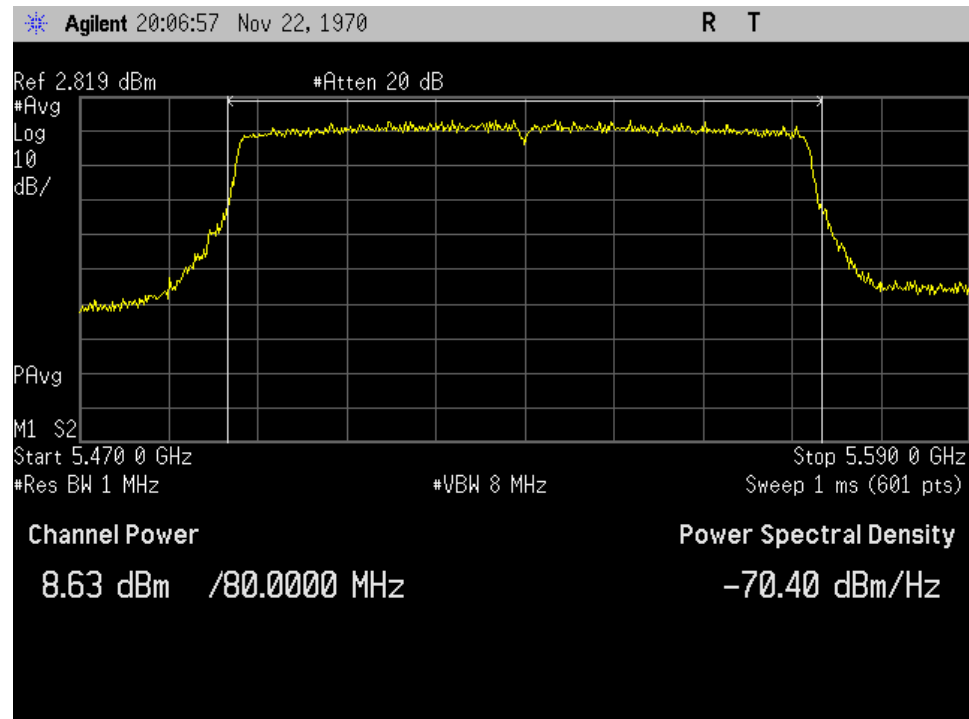
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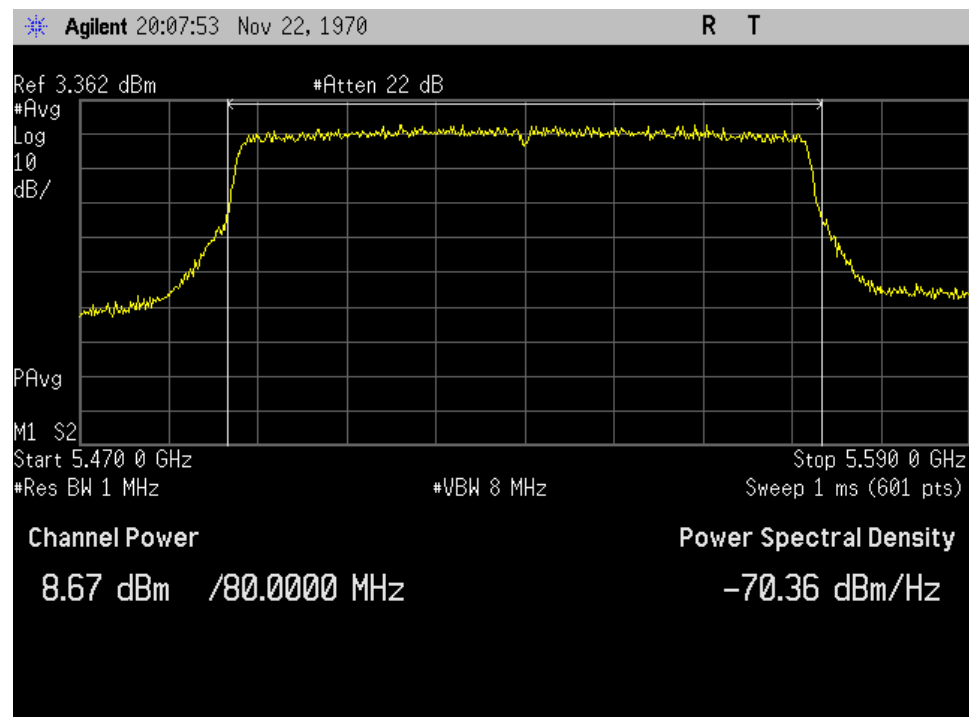
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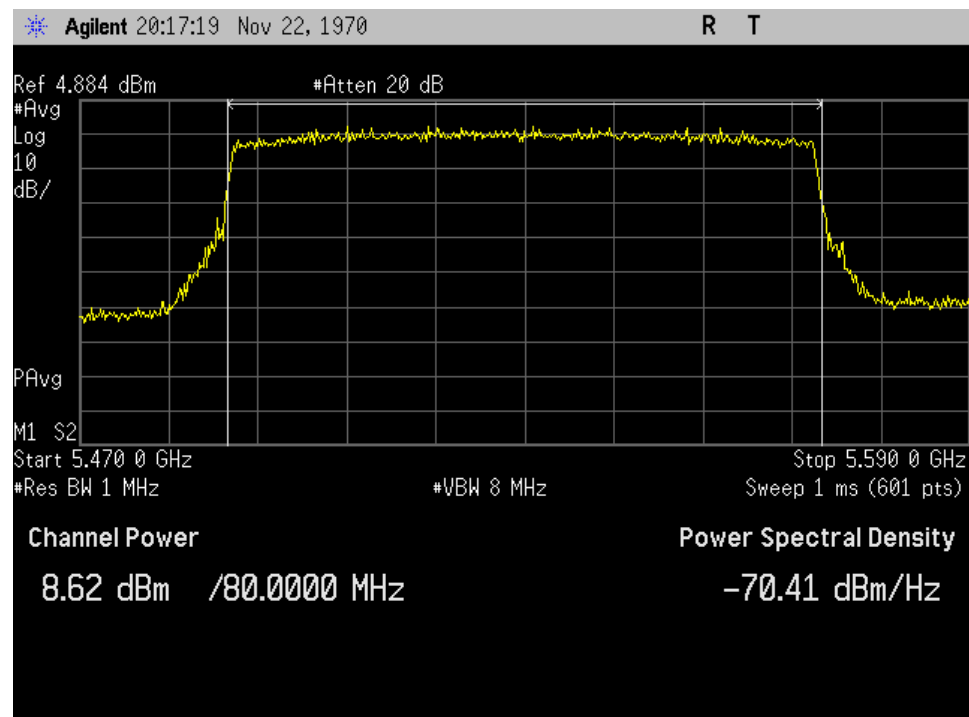
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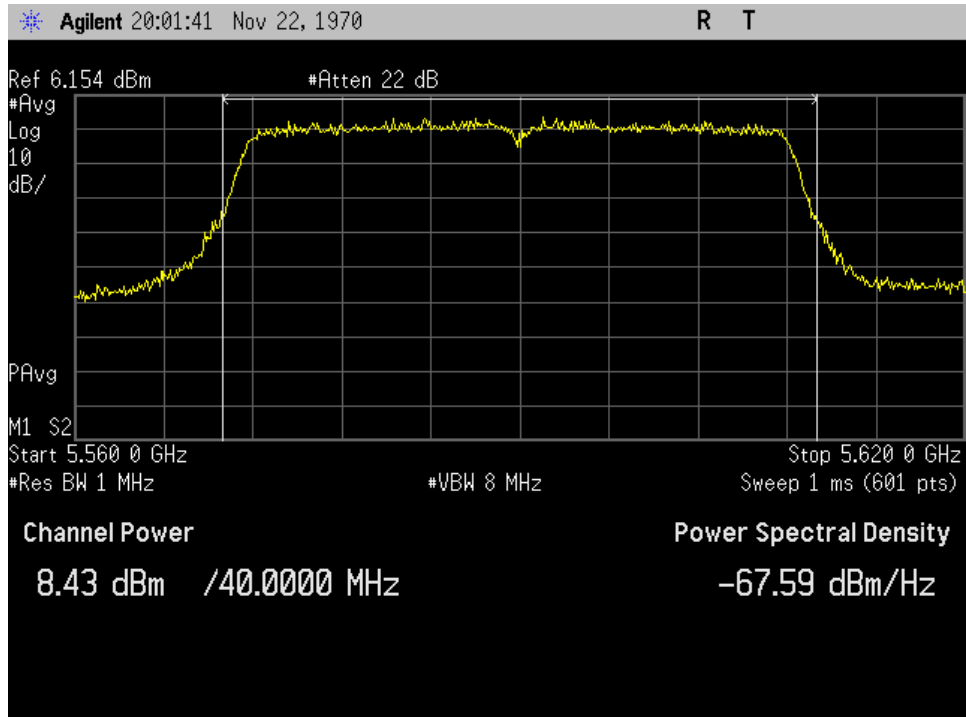
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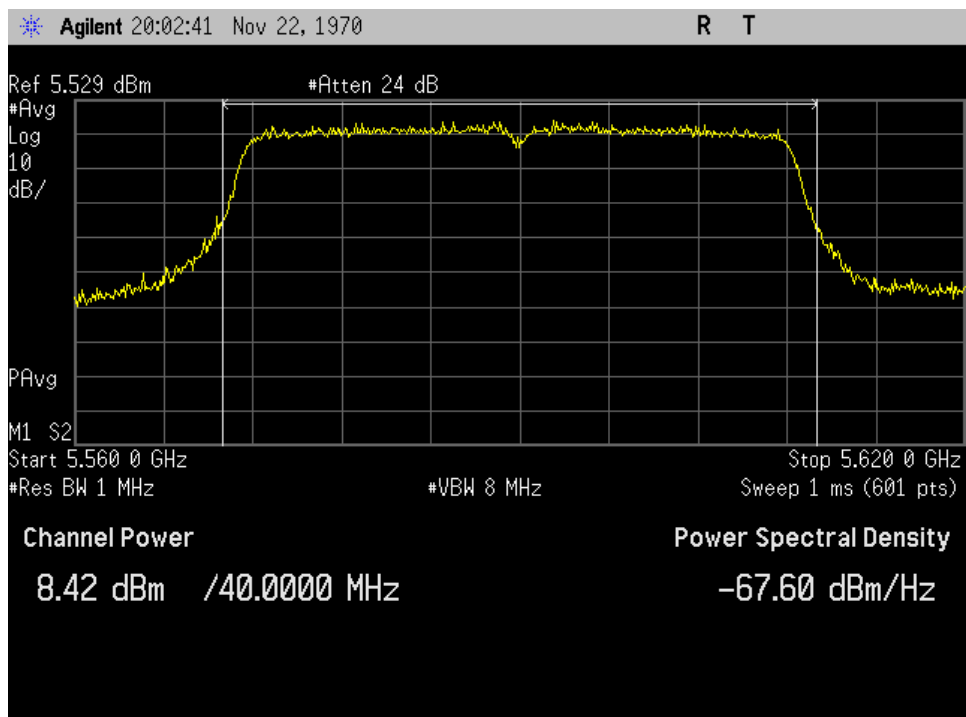
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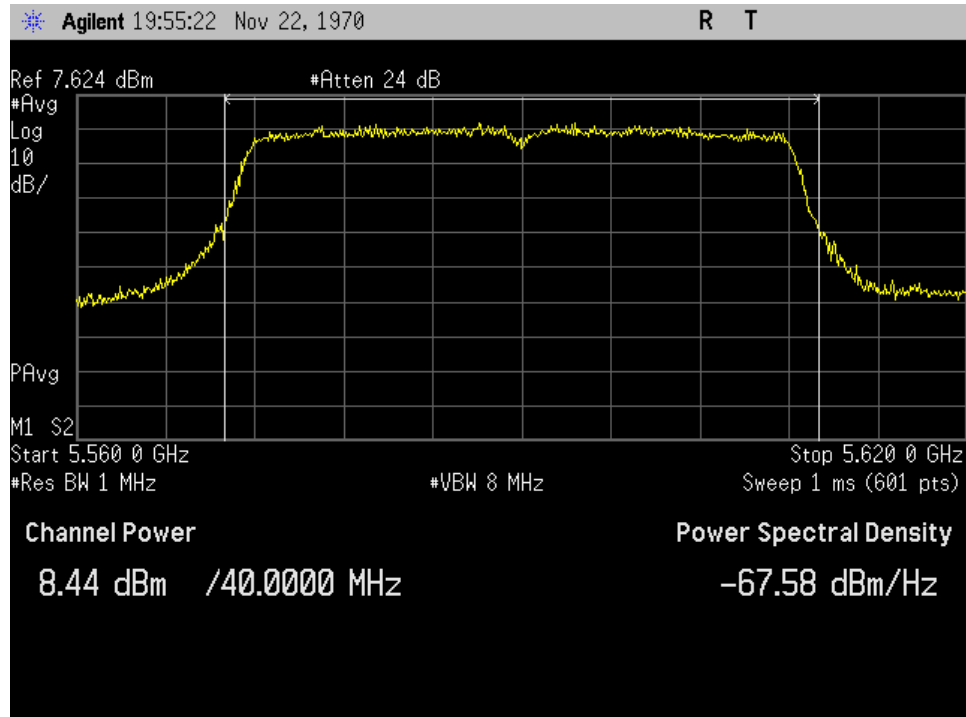
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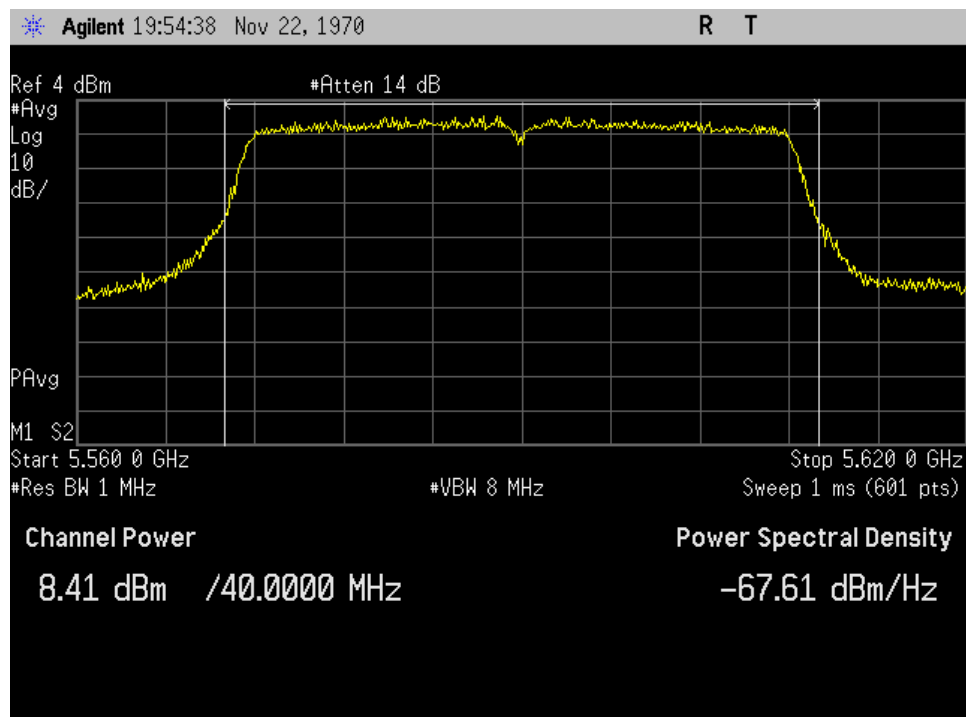
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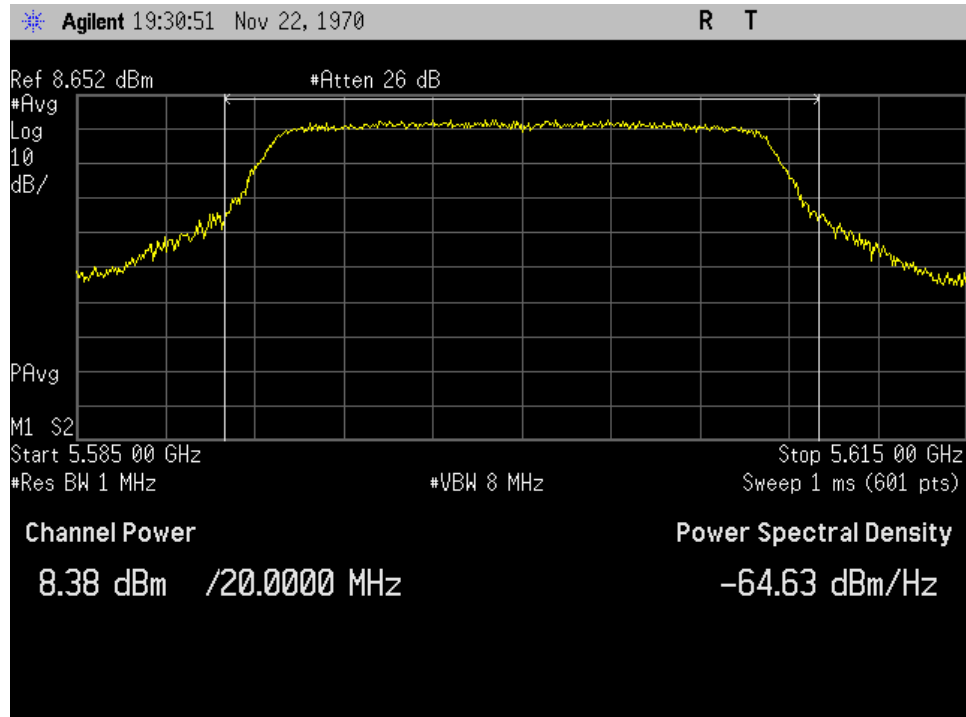
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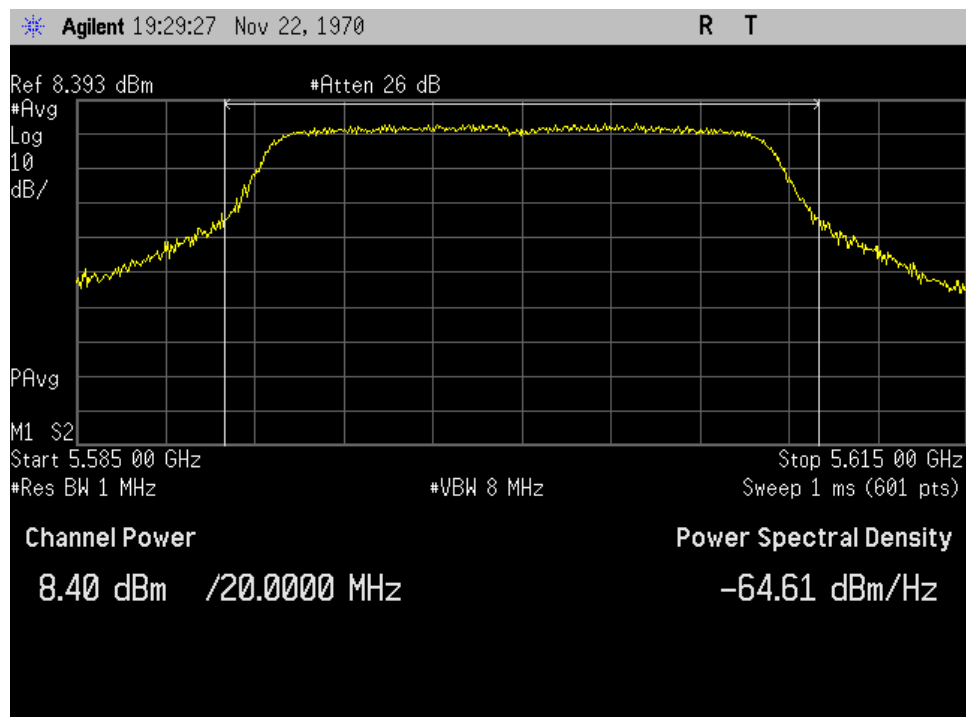
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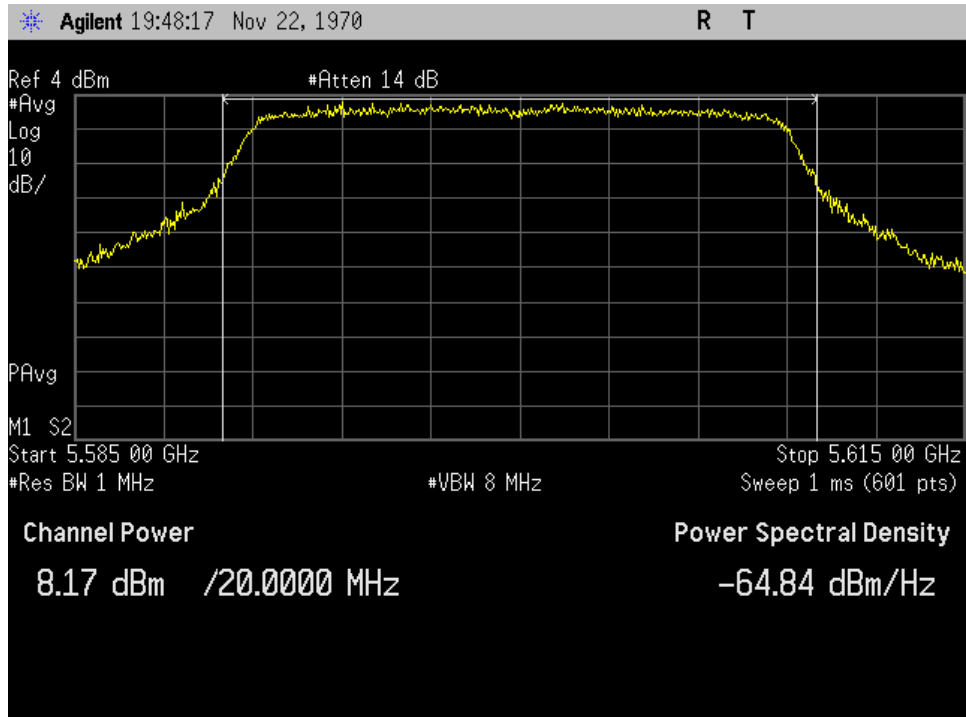
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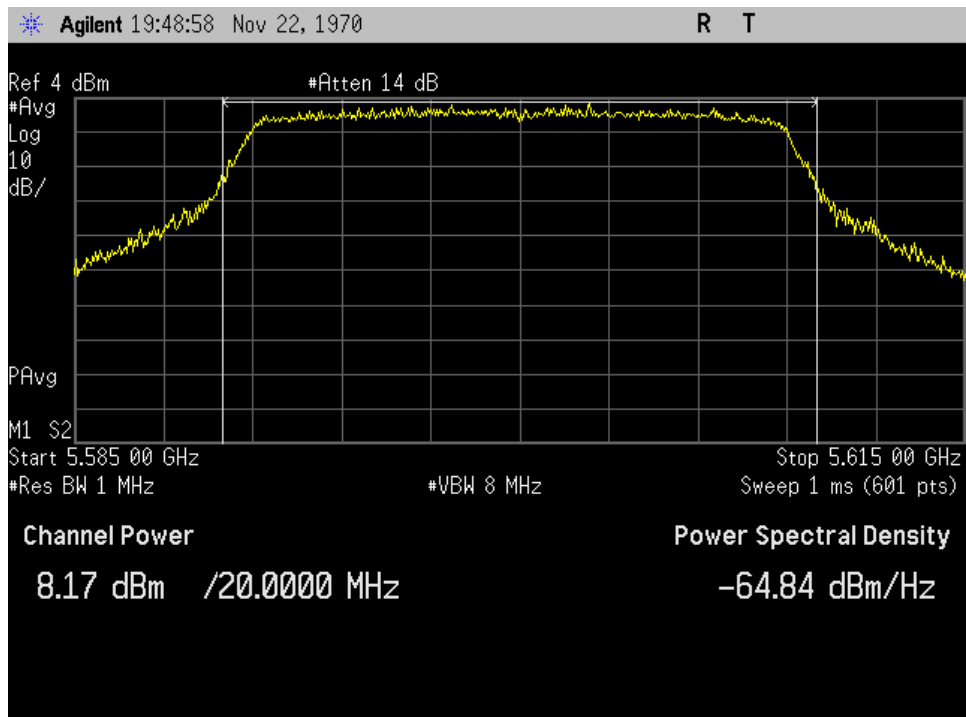
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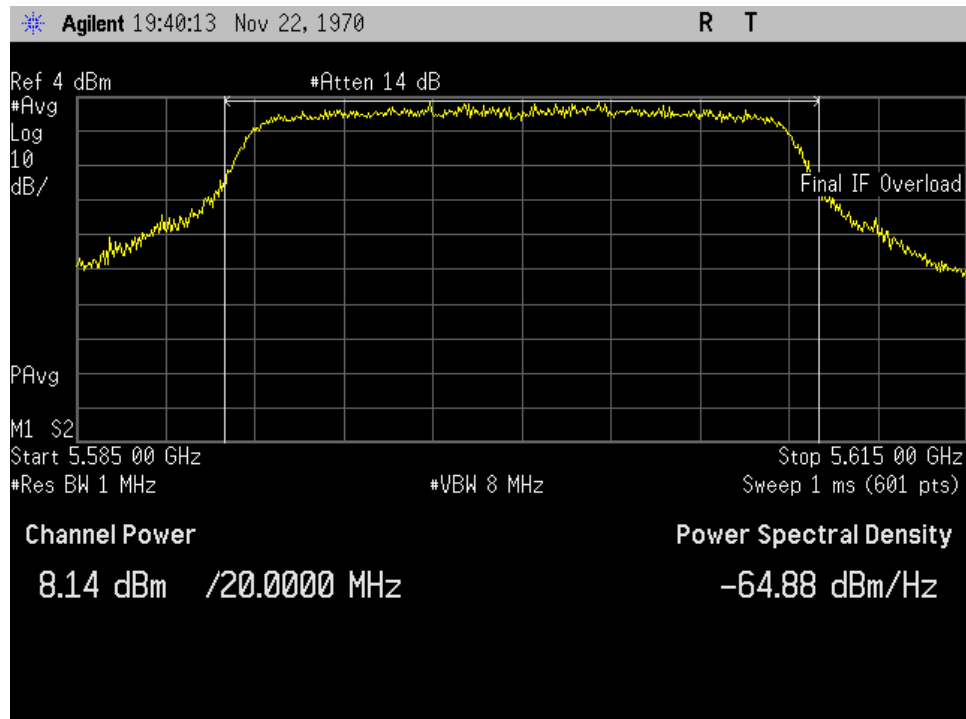
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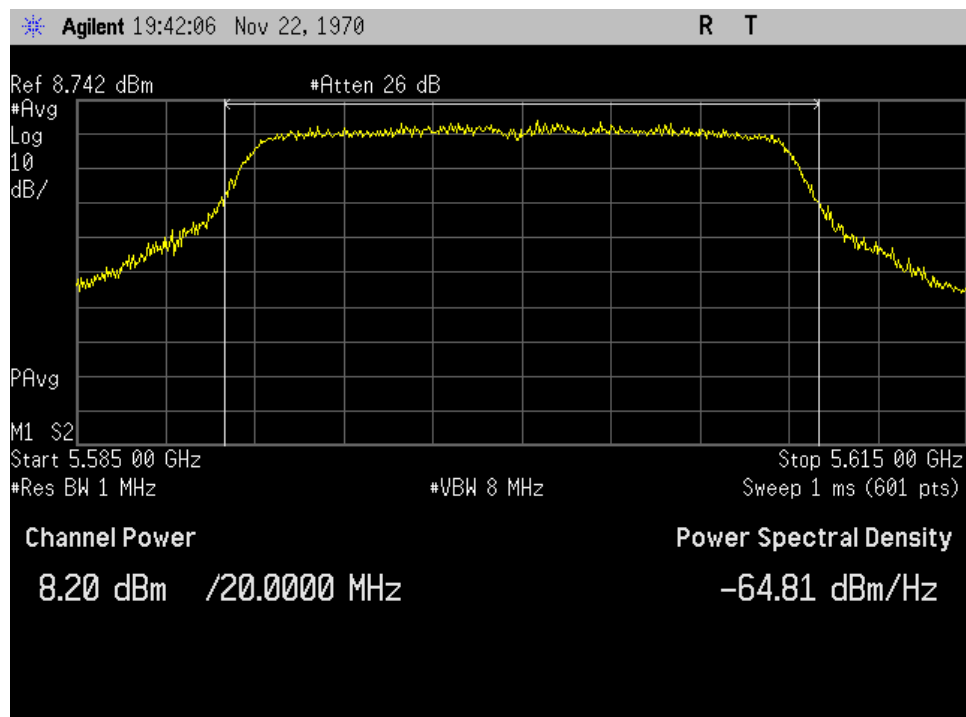
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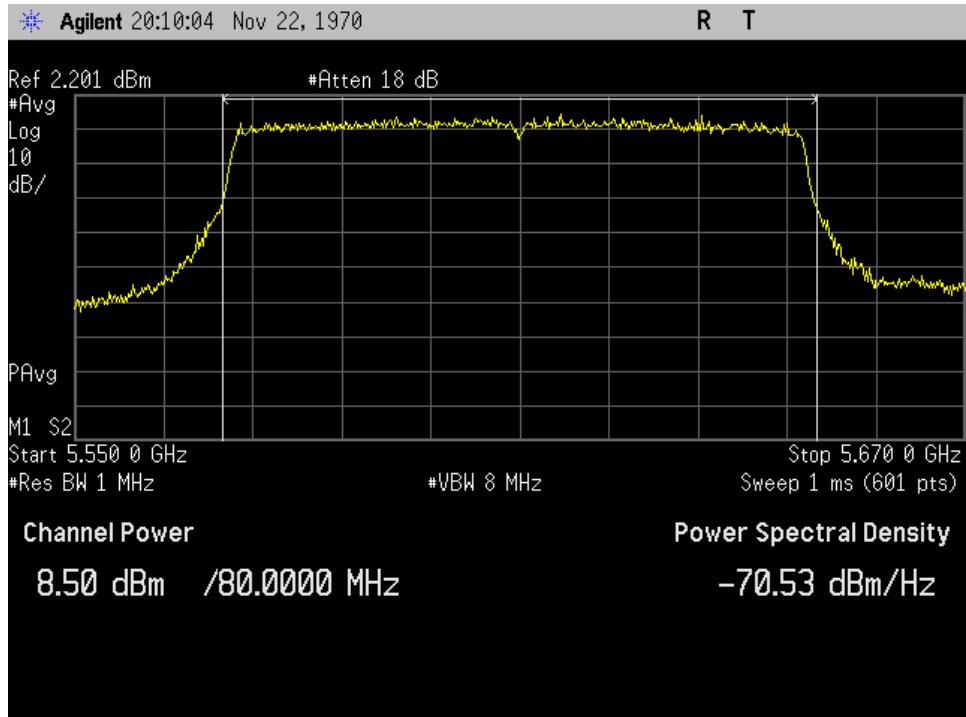
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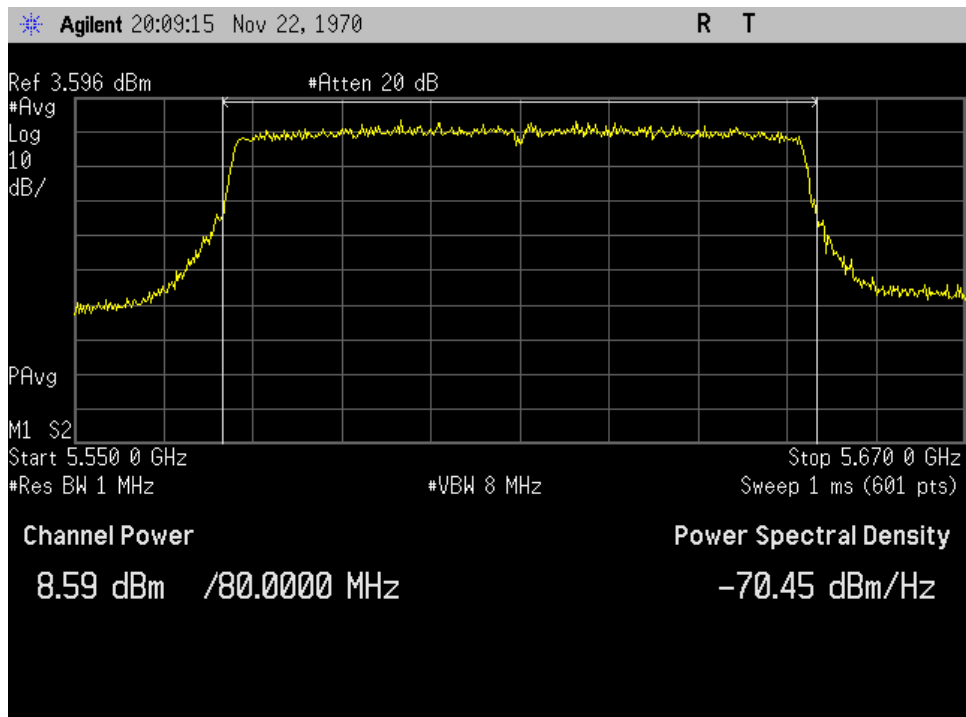
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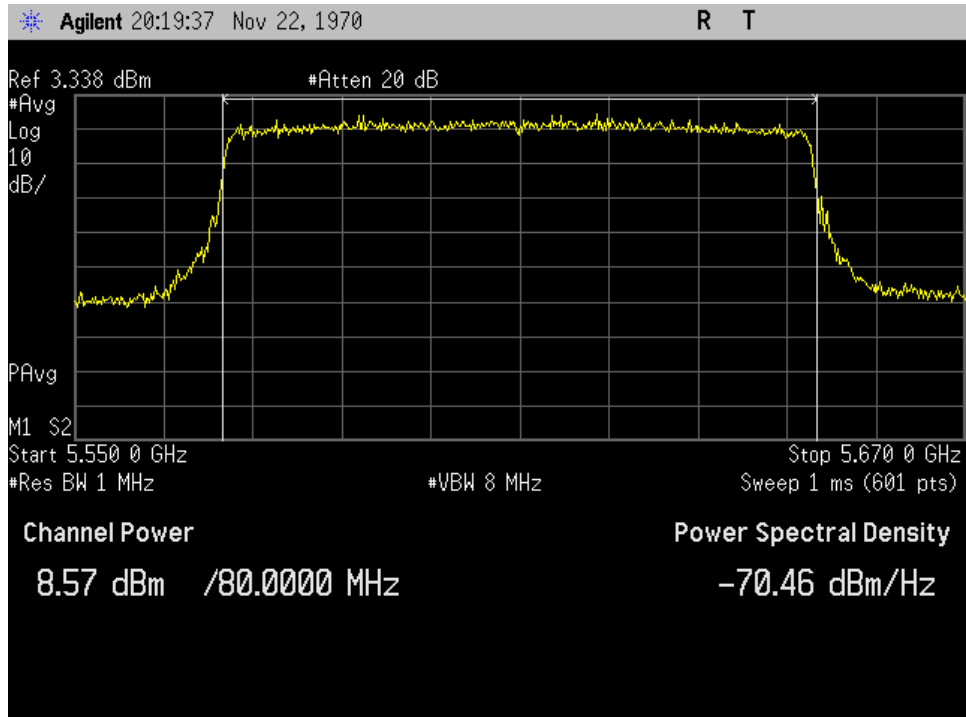
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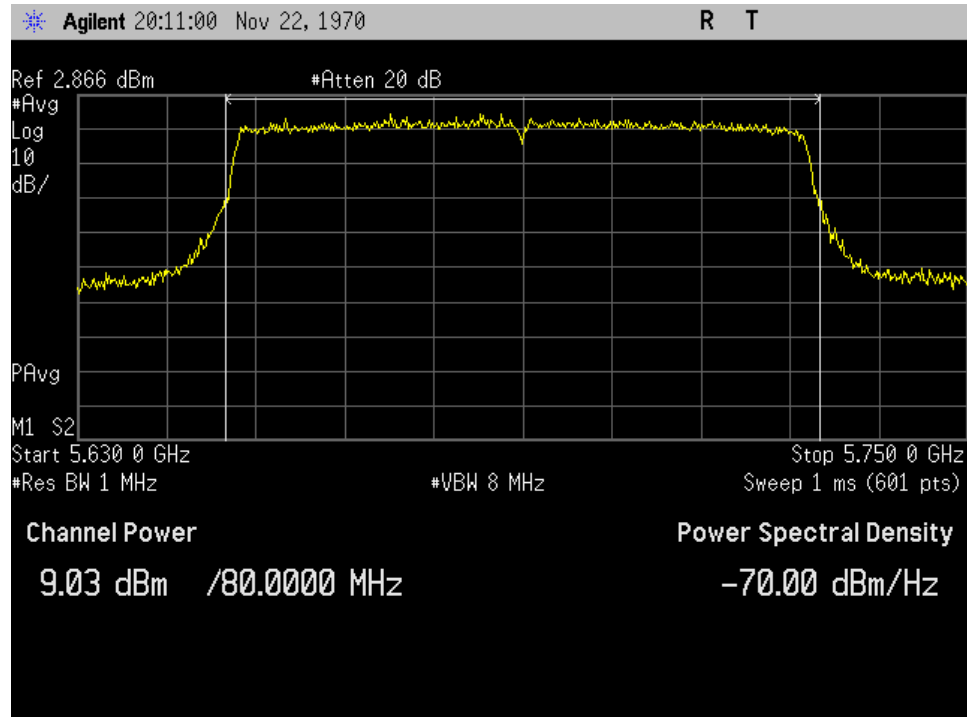
U-NII-2C_5610MHz_Mid Ch_122_80MHz BW_ax-mode_Output Power_Port 1



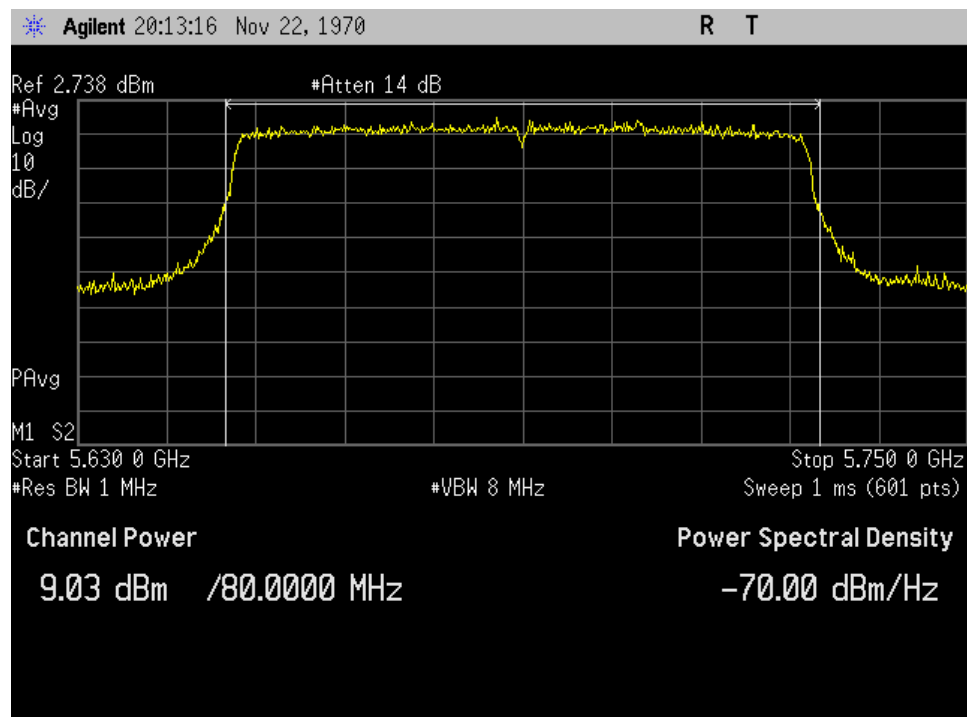
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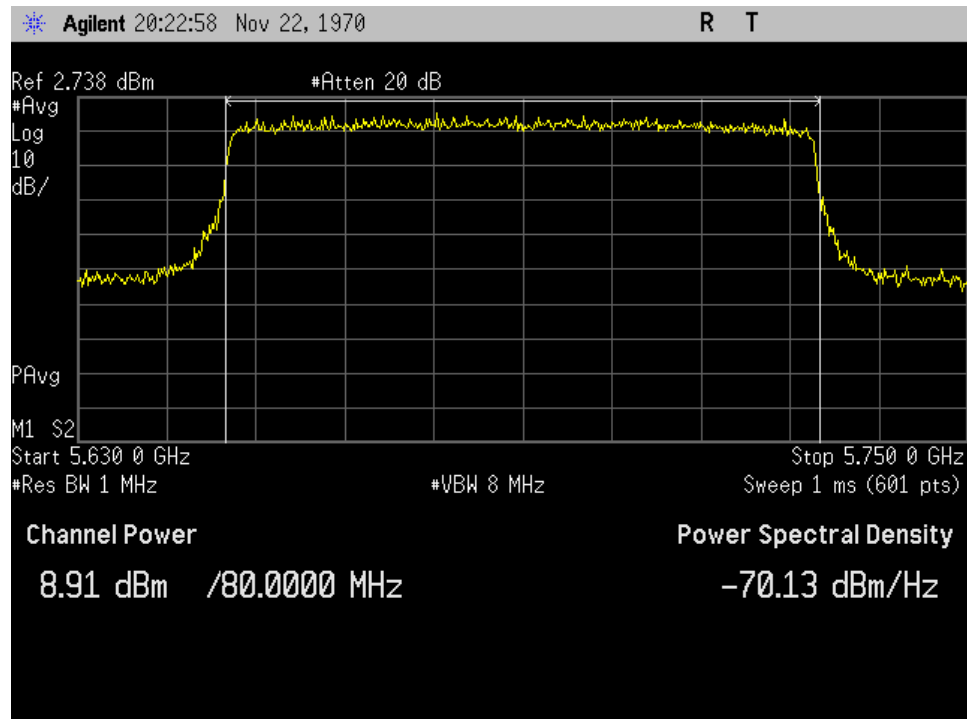
U-NII-2C_5690MHz_High Ch_138_80MHz BW_ac-mode_Output Power_Port 1



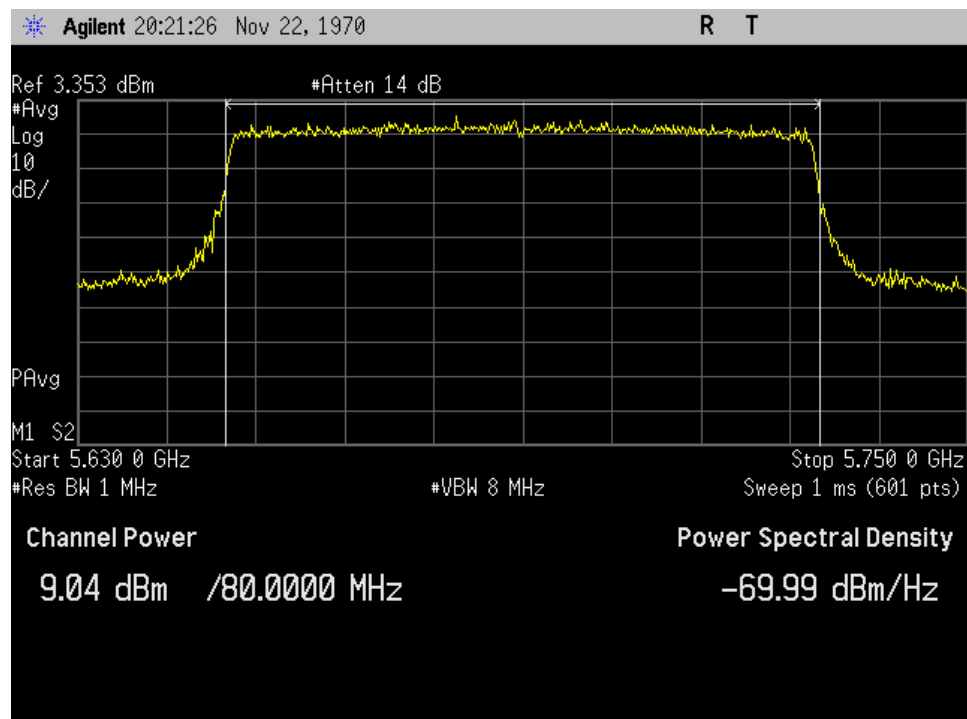
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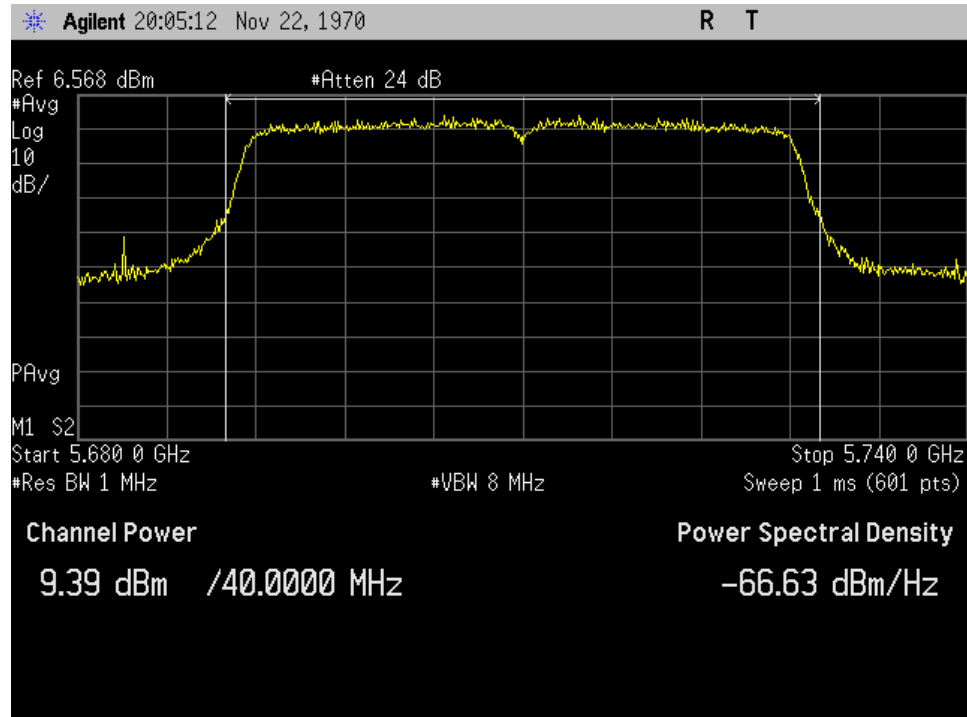
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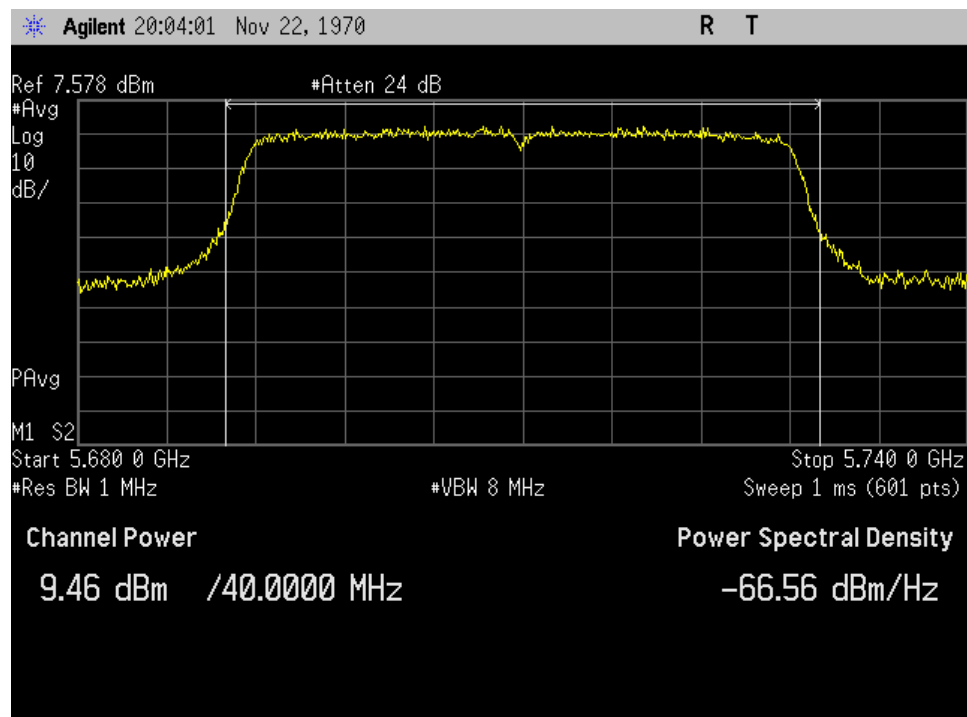
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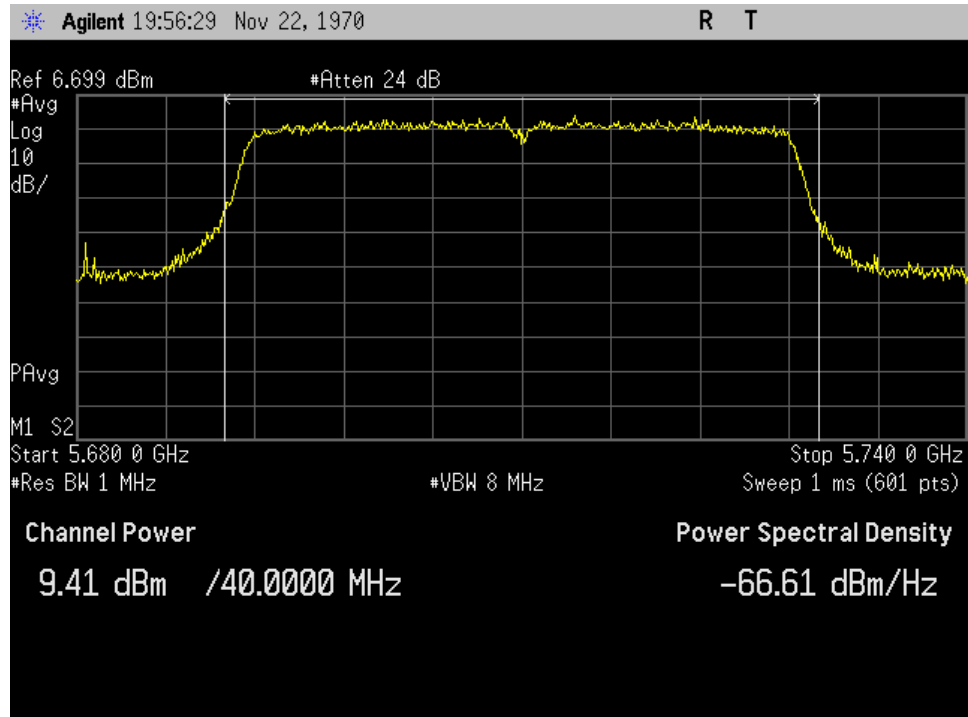
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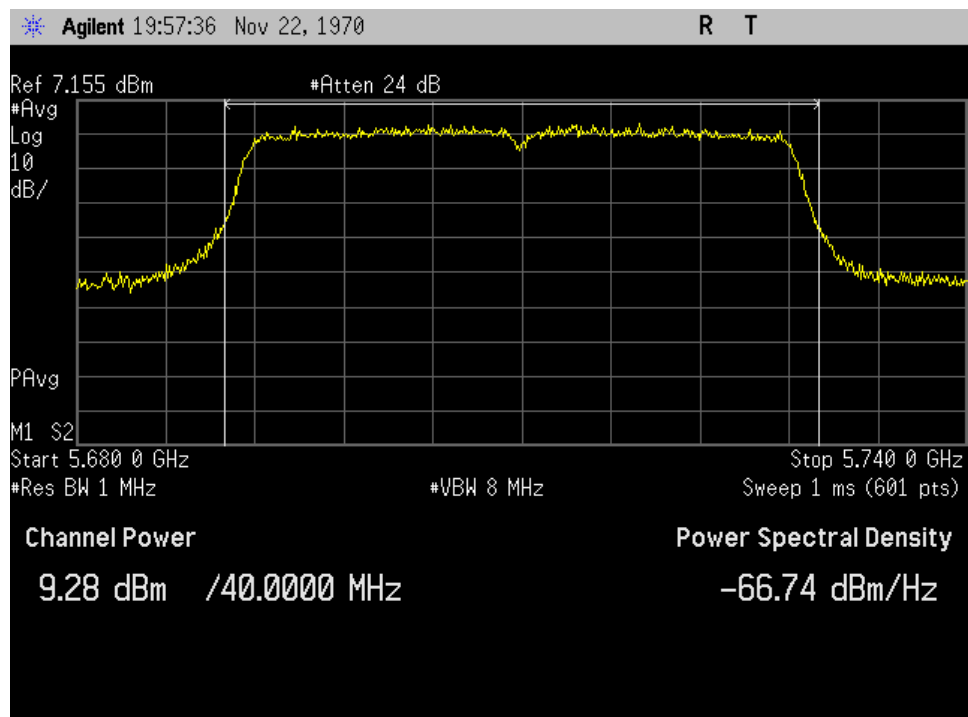
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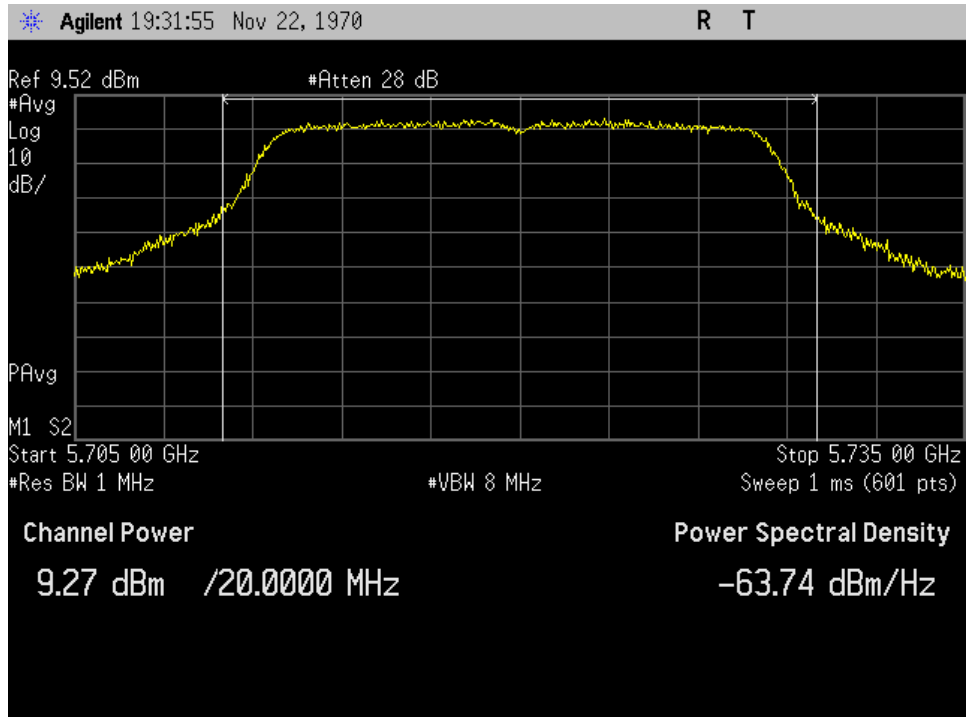
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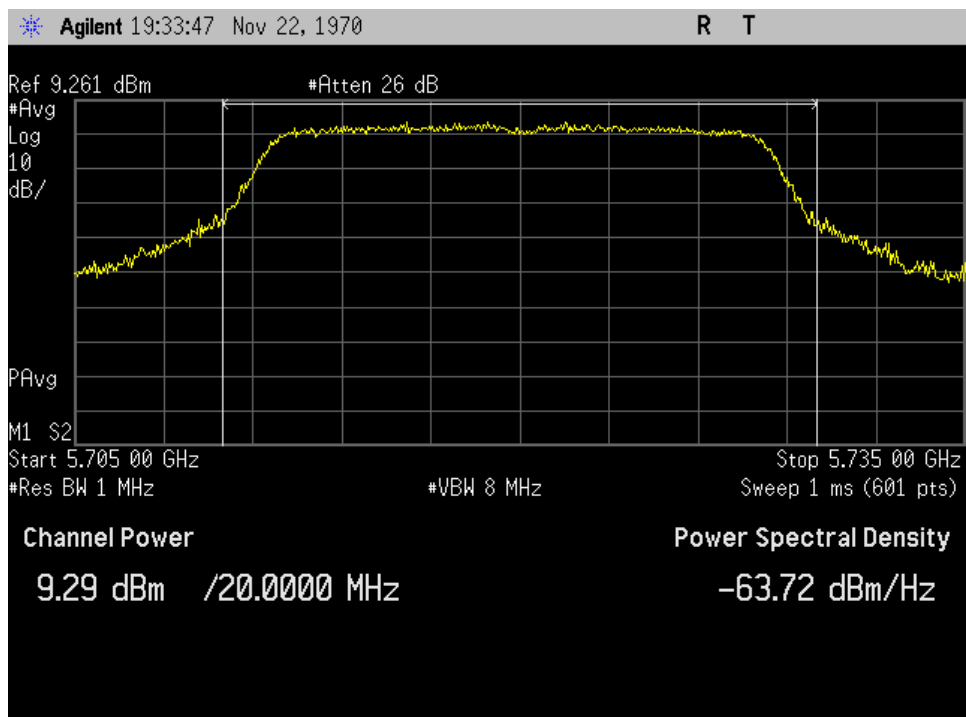
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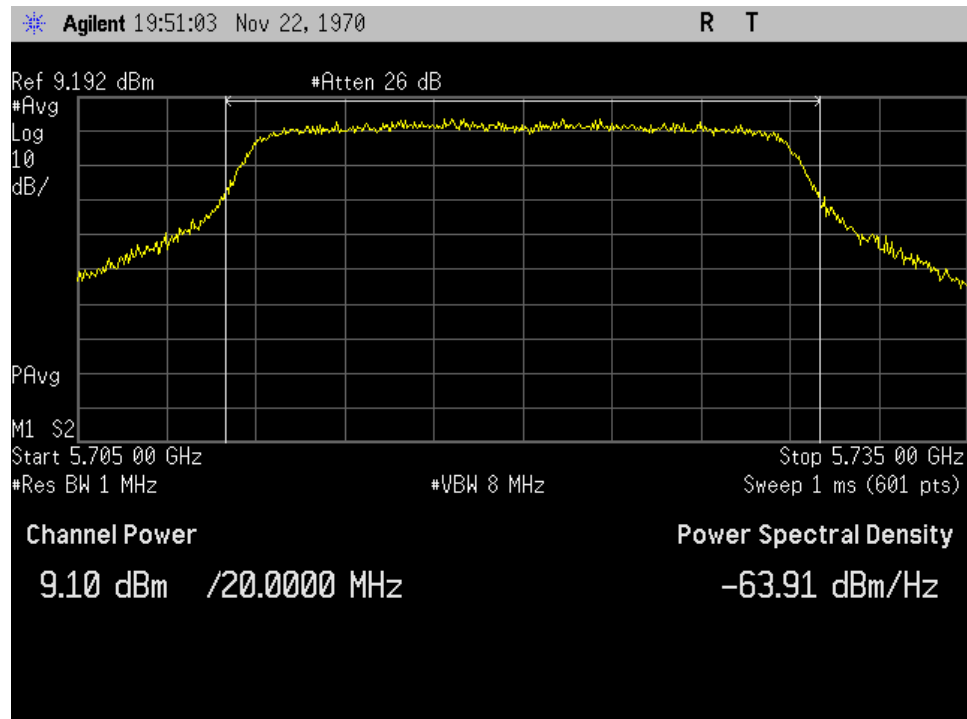
U-NII-2C_5720MHz_High Ch_144_20MHz BW_a-mode_Output Power_Port 1



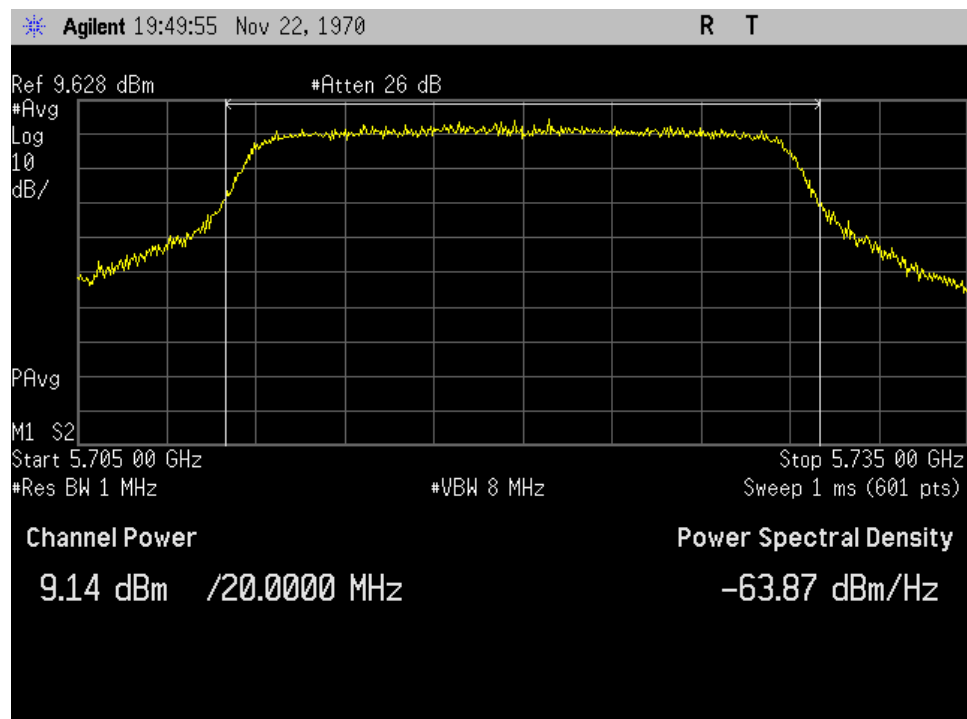
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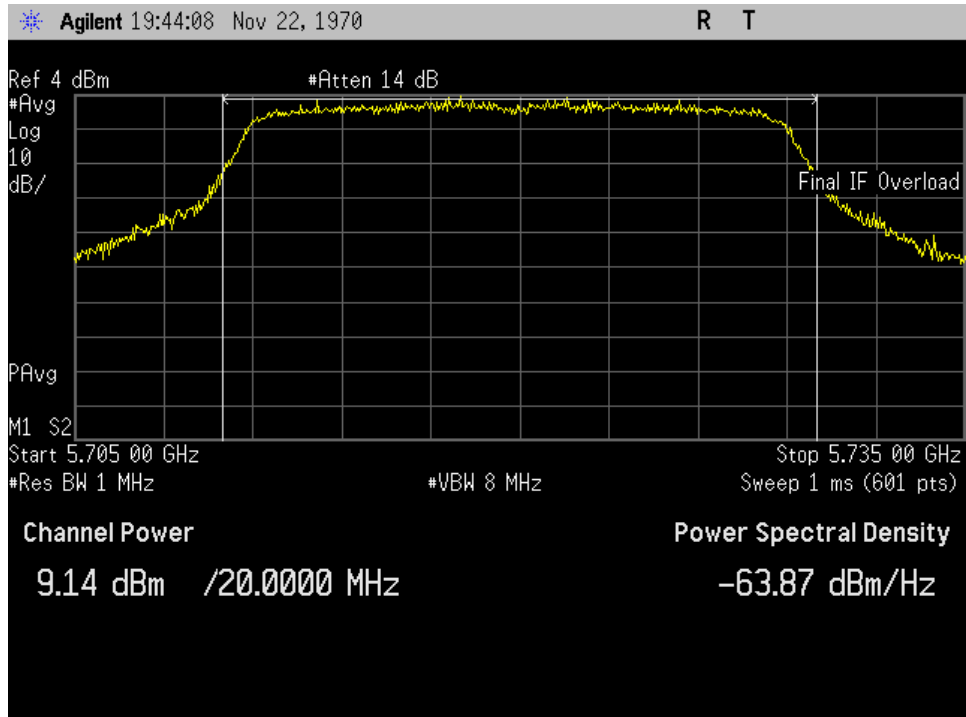
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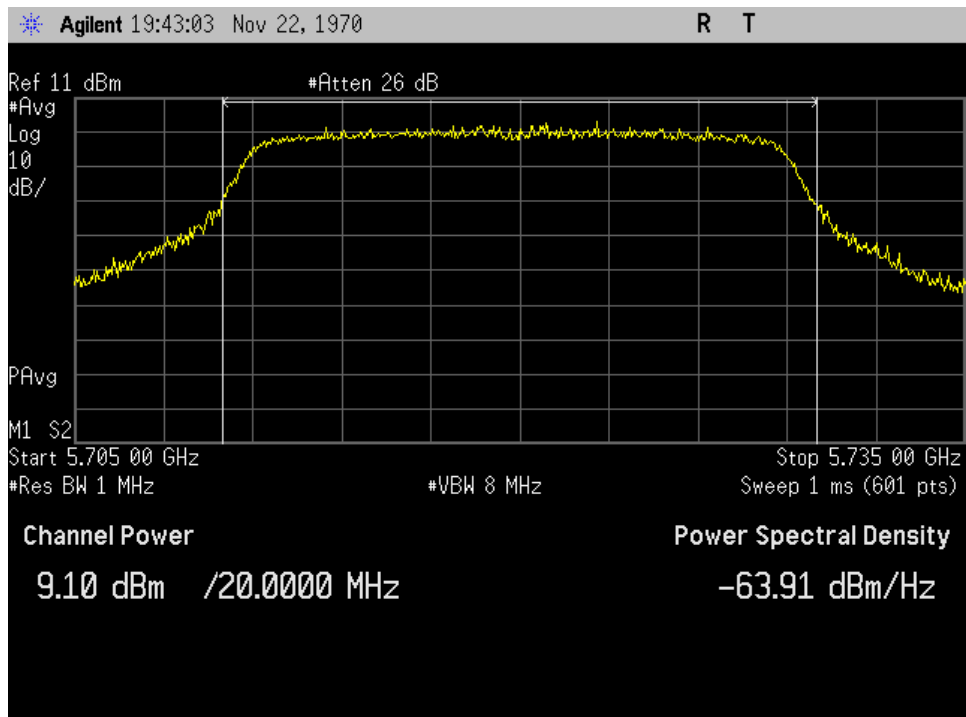
U-NII-2C_5720MHz_High Ch_144_20MHz BW_ac-mode_Output Power_Port 2



U-NII-2C_5720MHz_High Ch_144_20MHz BW_n-mode_Output Power_Port 1



U-NII-2C_5720MHz_High Ch_144_20MHz BW_n-mode_Output Power_Port 2

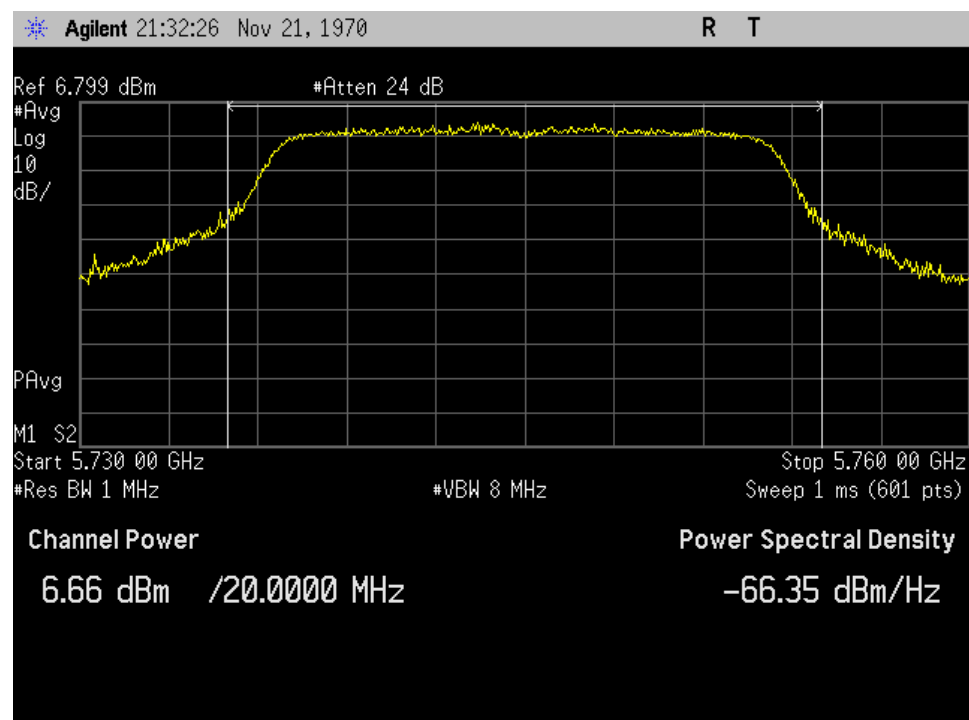


U-NII-3

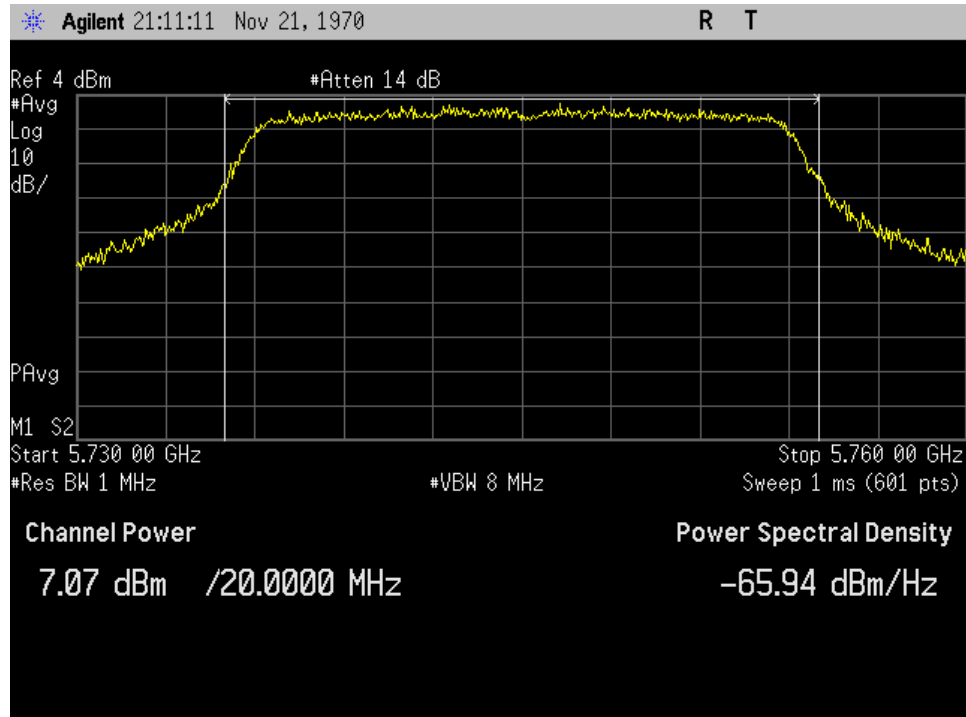
U-NII-3_5745MHz_Low Ch_149_20MHz BW_a-mode_Output Power_Port



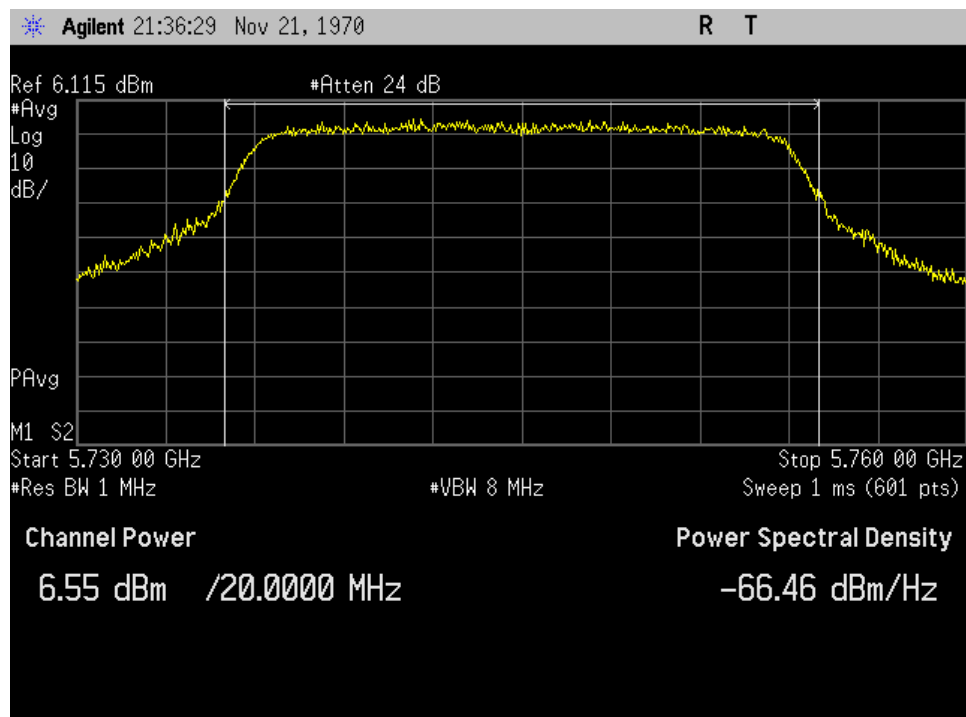
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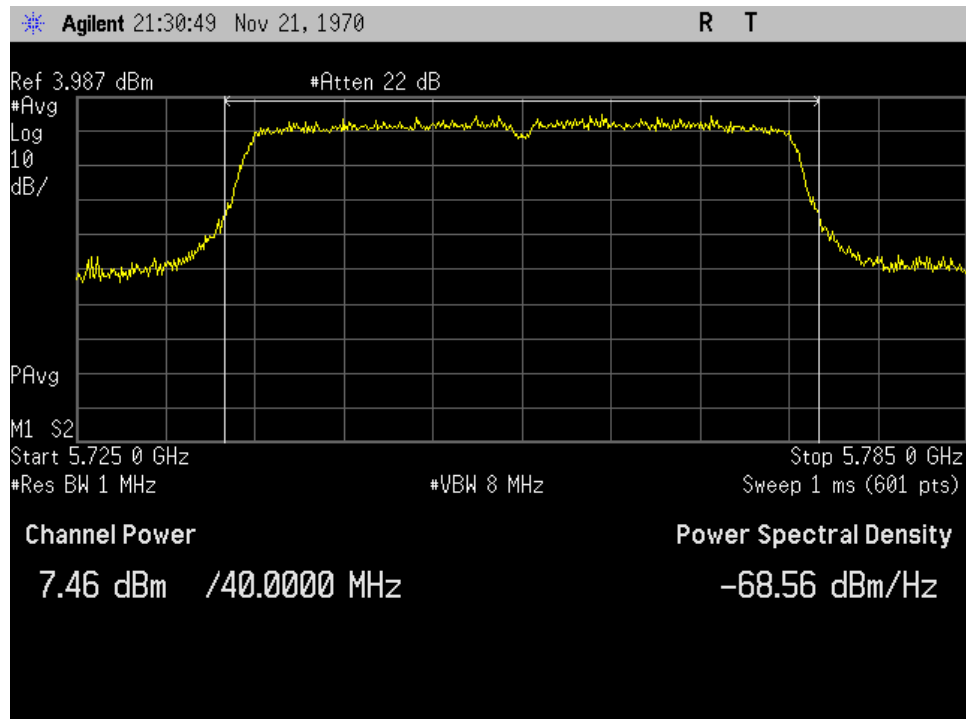
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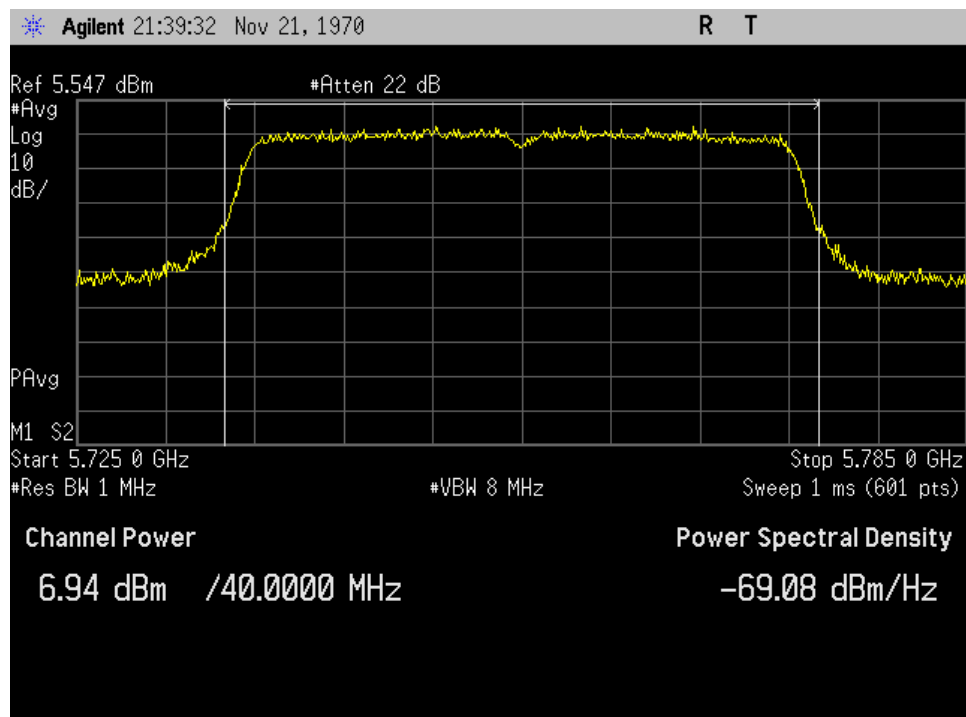
U-NII-3_5745MHz_Low Ch_149_20MHz BW_n-mode_Output Power_Port 2



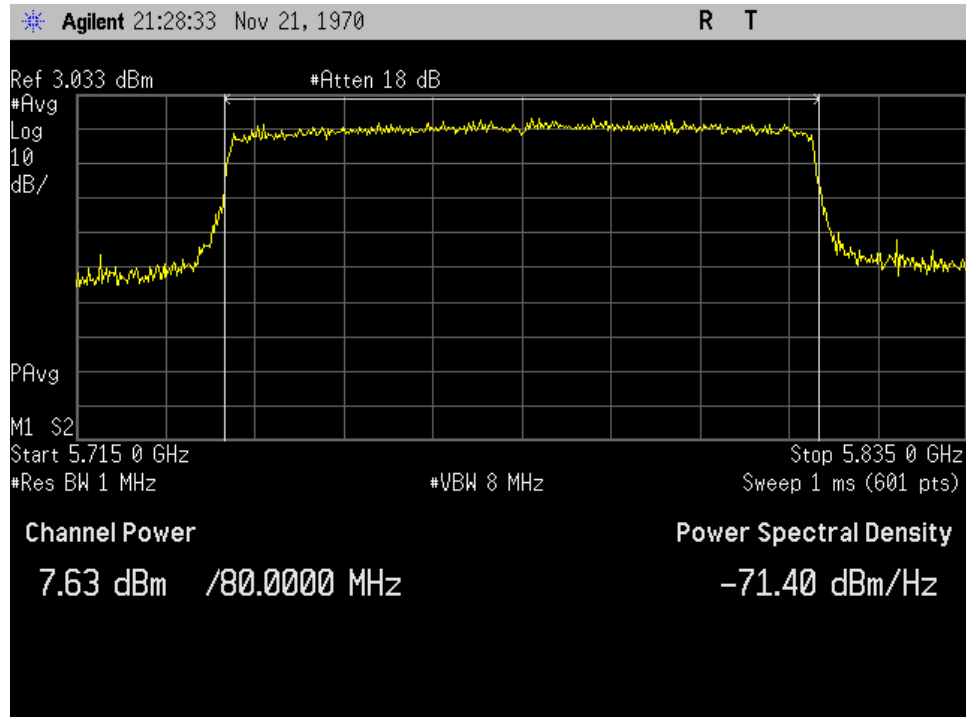
U-NII-3_5755MHz_Low Ch_151_40MHz BW_n-mode_Output Power_Port 1



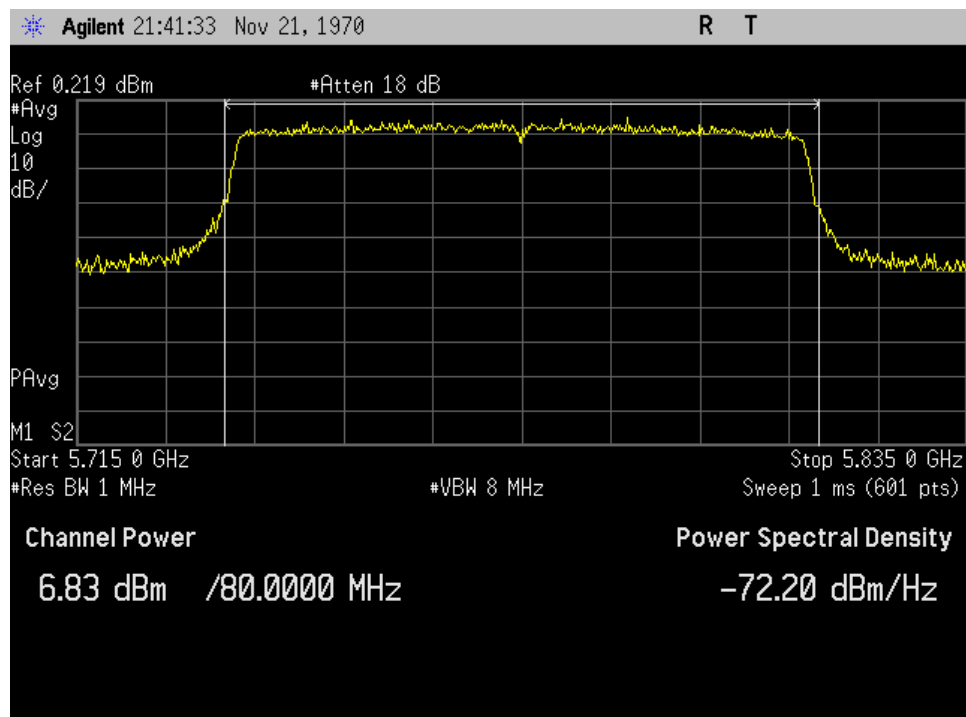
U-NII-3_5755MHz_Low Ch_151_40MHz BW_n-mode_Output Power_Port 2



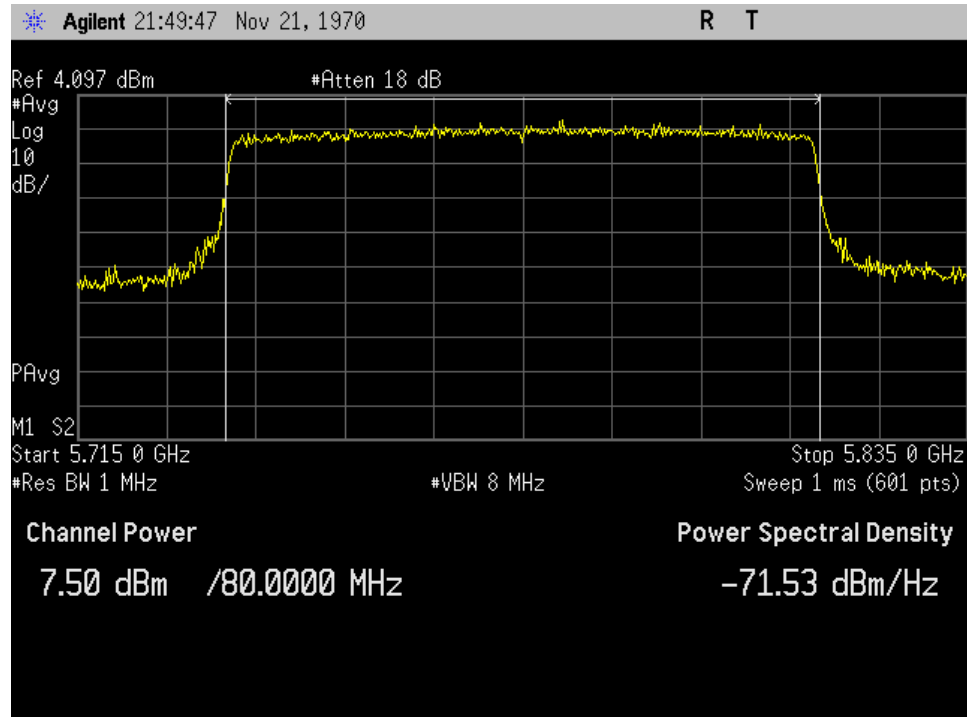
U-NII-3_5775MHz_High_Mid Ch_155_80MHz BW_ac-mode_Output Power_Port 1



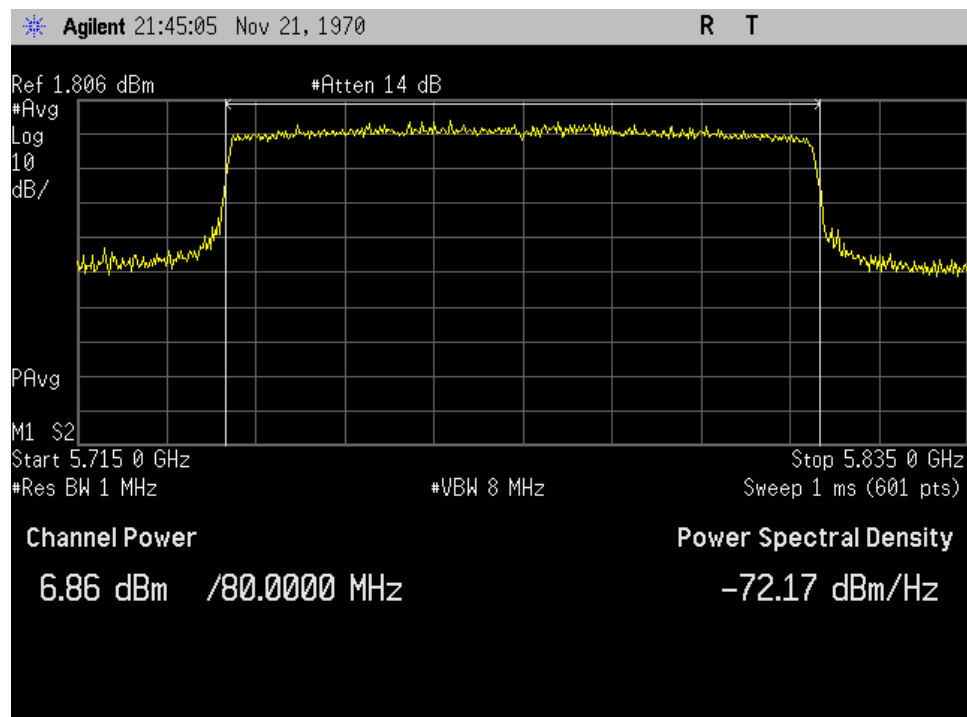
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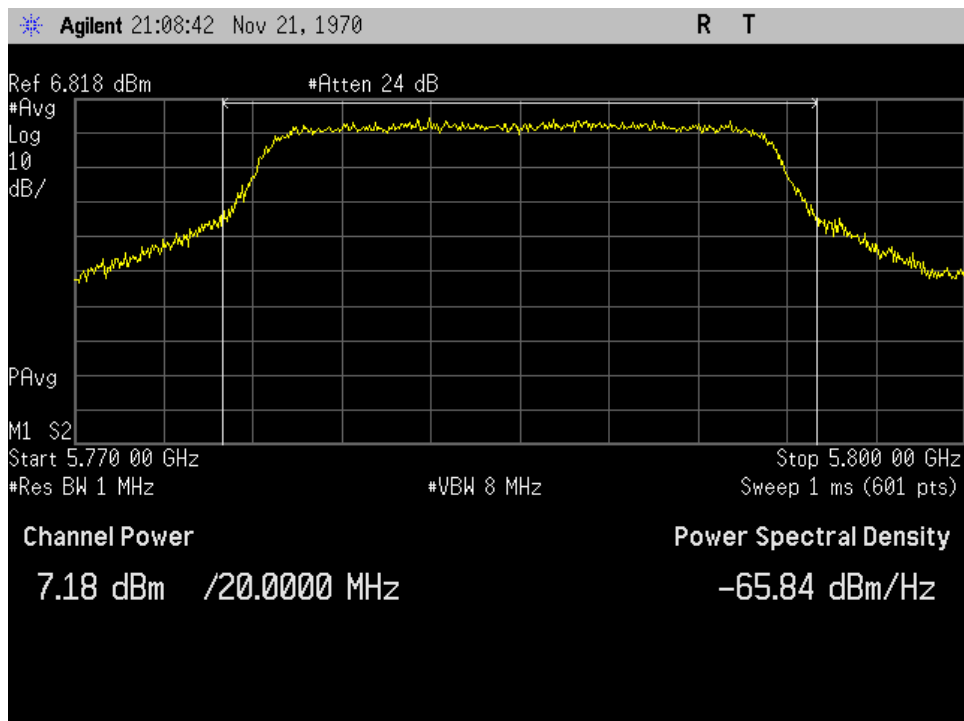
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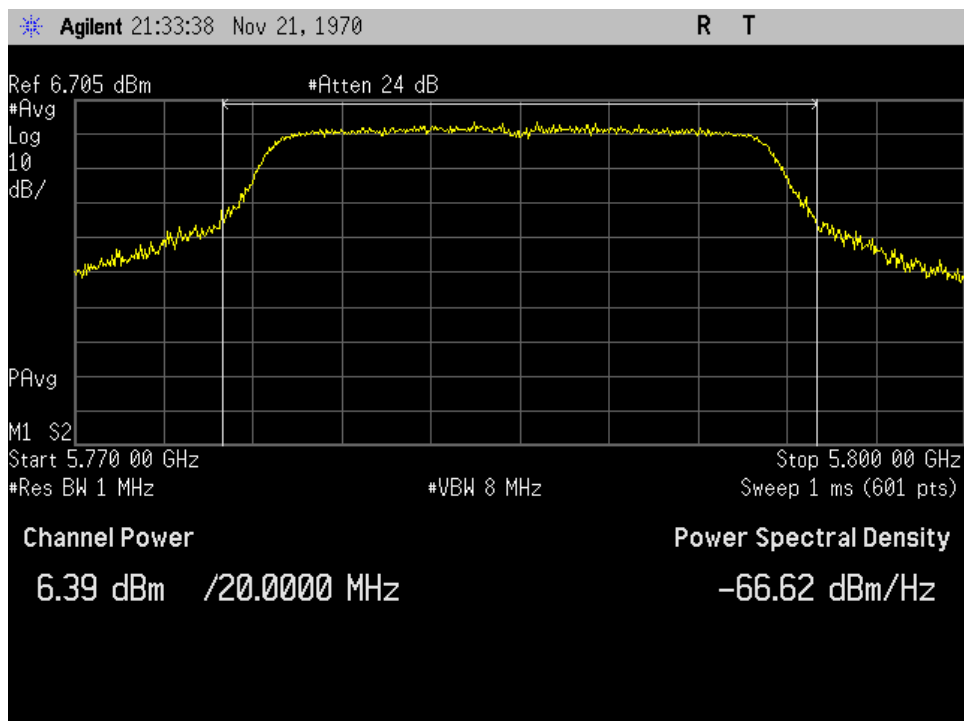
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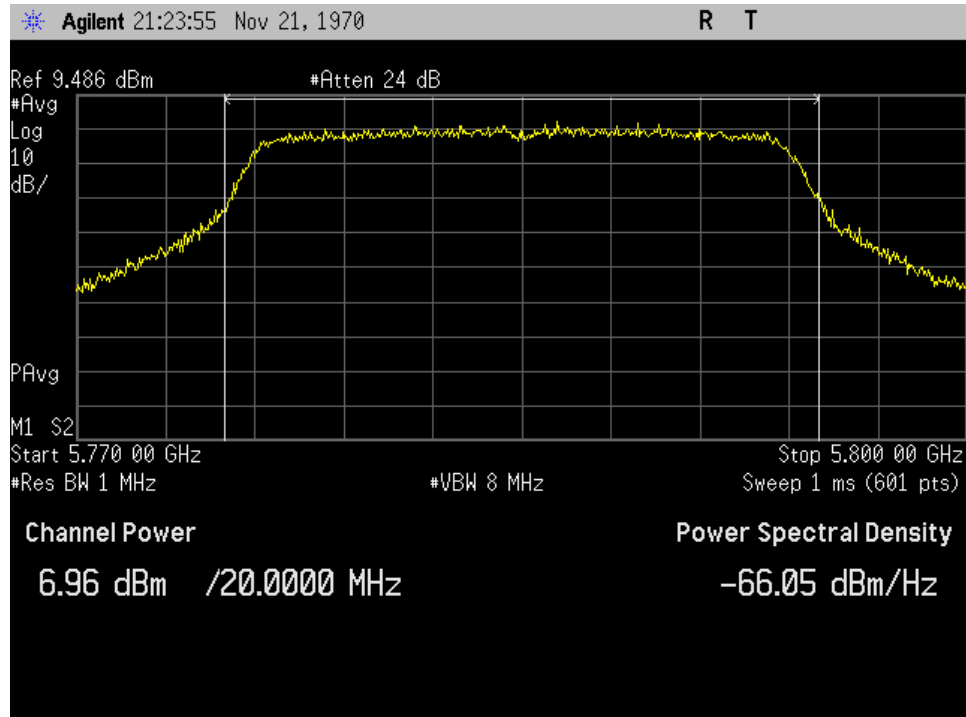
U-NII-3_5785MHz_Mid Ch_157_20MHz BW_a-mode_Output Power_Port 1



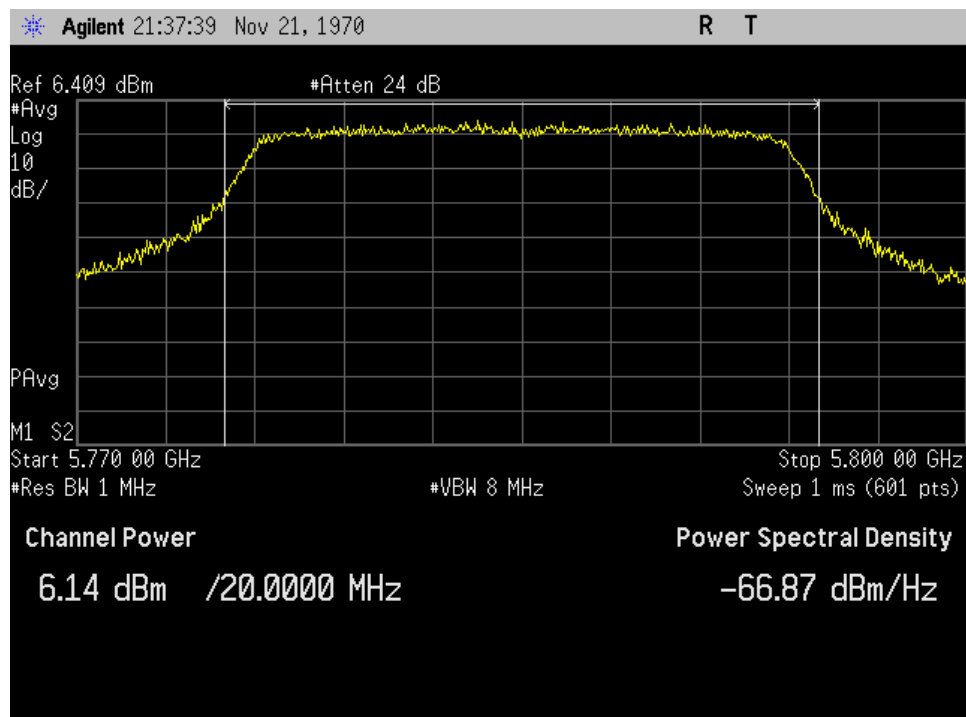
U-NII-3_5785MHz_Mid Ch_157_20MHz BW_a-mode_Output Power_Port 2



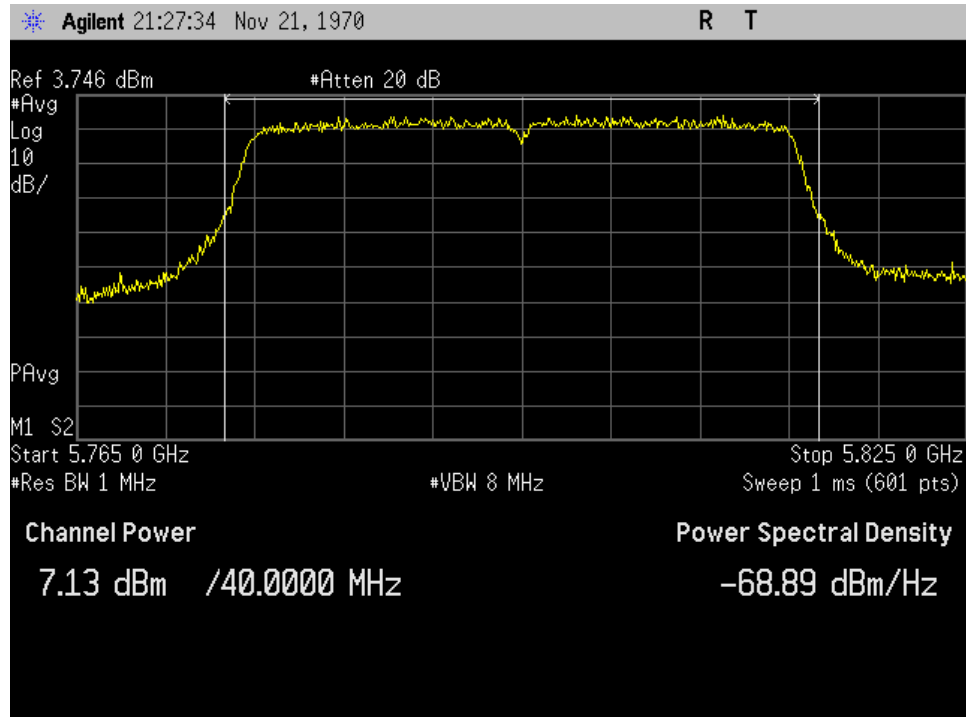
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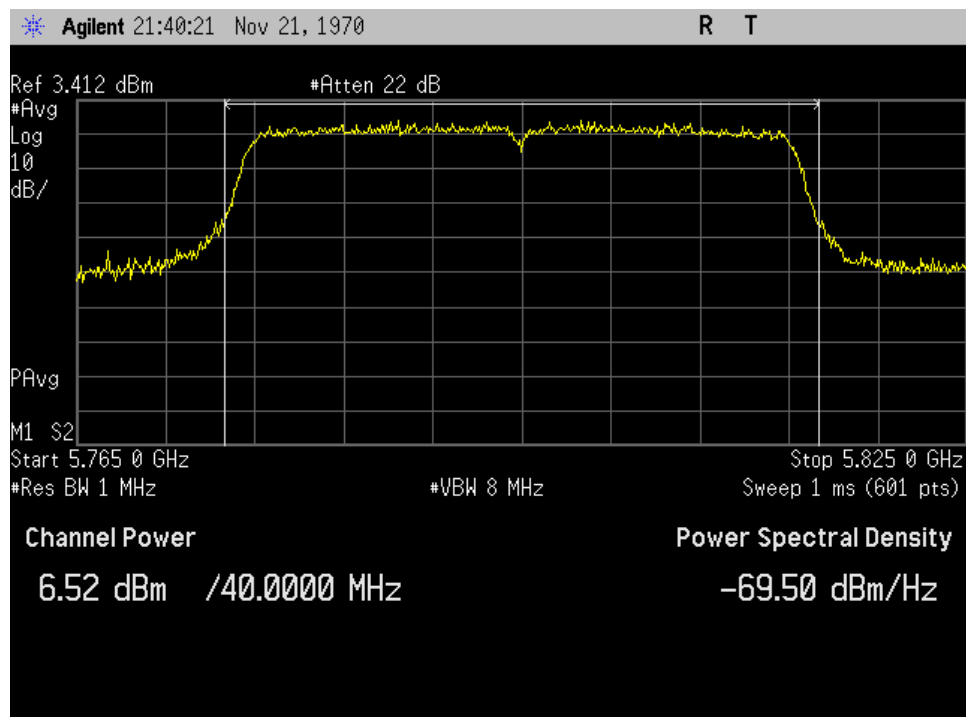
U-NII-3_5785MHz_Mid Ch_157_20MHz BW_n-mode_Output Power_Port 2



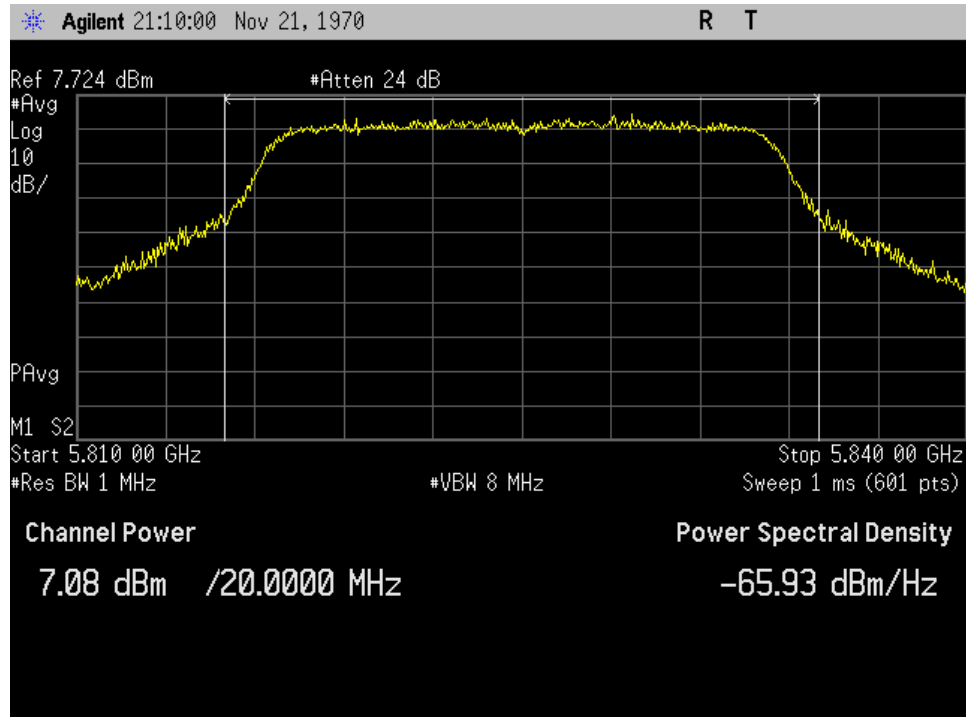
U-NII-3_5795MHz_High_Mid Ch_159_40MHz BW_n-mode_Output Power_Port 1



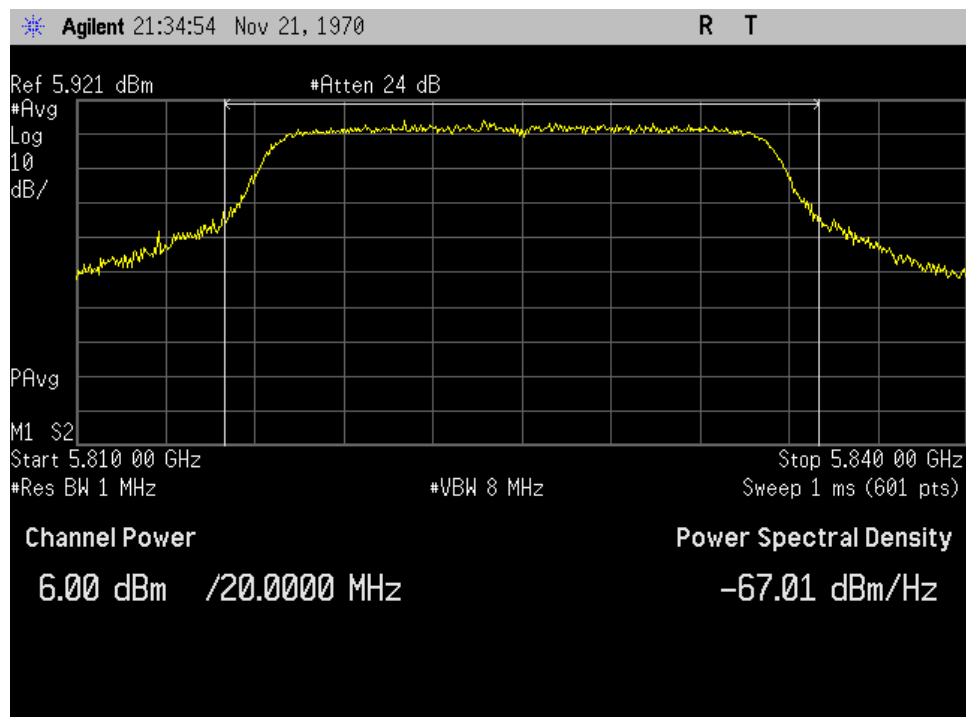
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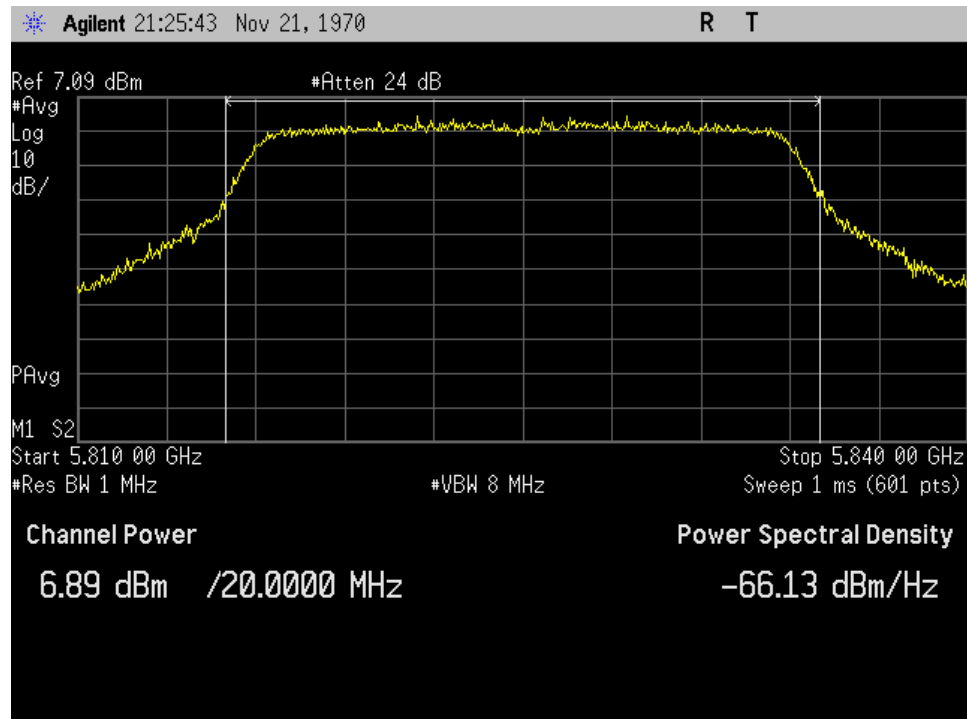
U-NII-3_5825MHz_High Ch_165_20MHz BW_a-mode_Output Power_Port 1



U-NII-3_5825MHz_High Ch_165_20MHz BW_a-mode_Output Power_Port 2



U-NII-3_5825MHz_High Ch_165_20MHz BW_n-mode_Output Power_Port 1



U-NII-3_5825MHz_High Ch_165_20MHz BW_n-mode_Output Power_Port 2

