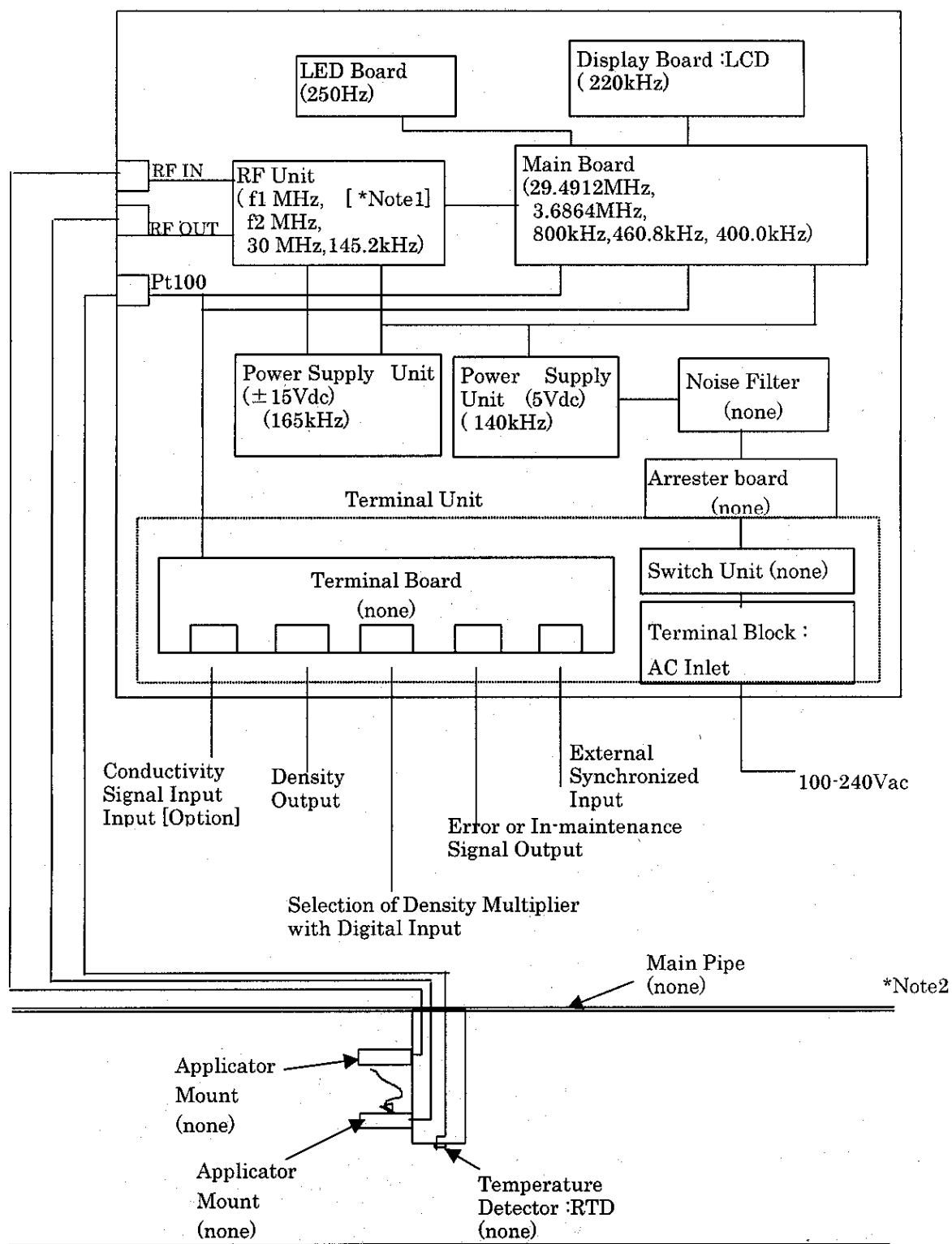


SYSTEM BLOCK DIAGRAM

Note1: F1 is any of adjusted frequency between from 1825MHz to 1975MHz.

F2 is any of fixed frequency between from 1824.850MHz to 1974.850MHz.

The following relational expression consists in f1 and f2.

$$f2 = f1 \cdot 0.150 \text{ [MHz]}$$