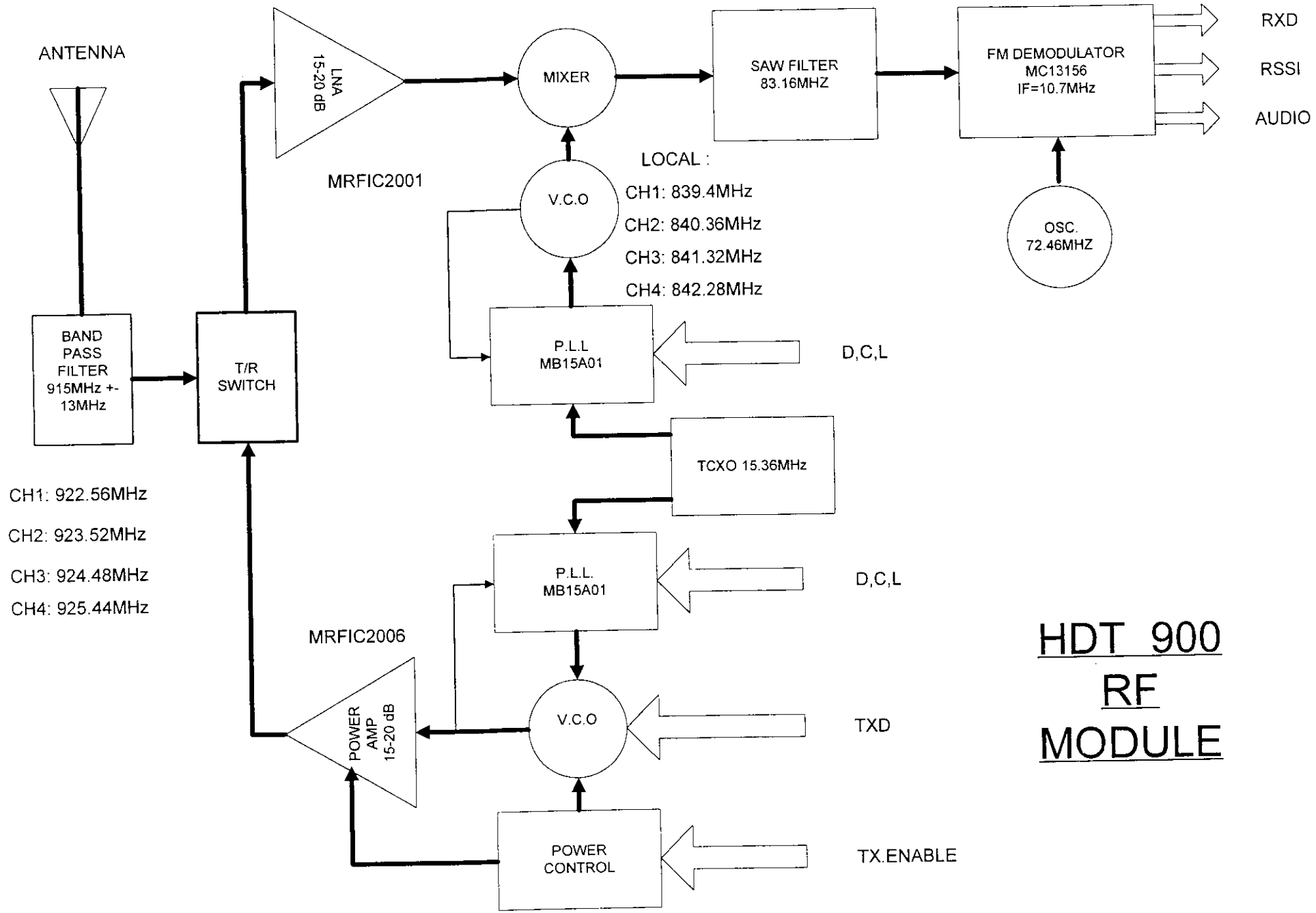
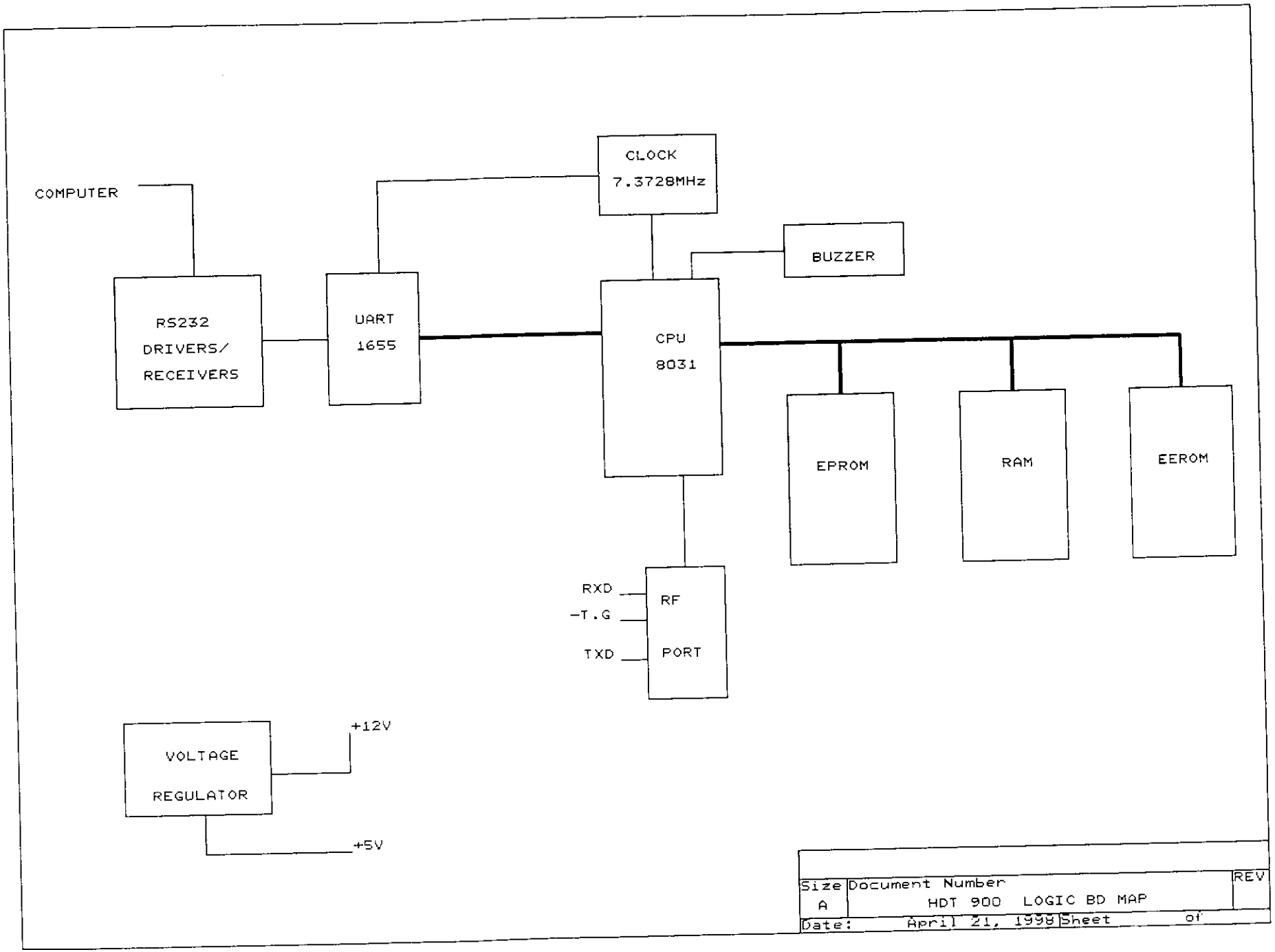
BLOCK DIAGRAM & CIRCUIT DIAGRAM

NTN COMMUNICATIONS, INC.

FCC ID. No.: M8SNTN900H

Wireless Host Data Terminal


ANTENNA
BAND PASS FILTER 915MHz +- 13MHz
CH1: 922.56MHz
CH2: 923.52MHz
CH3: 924.48MHz
CH4: 925.44MHz
T/R SWITCH
LNA 15-20 dB
MRFIC2001
MIXER
SAW FILTER 83.16MHZ
FM DEMODULATOR MC13156 IF=10.7MHz
RXD
RSSI
AUDIO
OSC. 72.46MHZ
LOCAL :
CH1: 839.4MHz
CH2: 840.36MHz
CH3: 841.32MHz
CH4: 842.28MHz
V.C.O
P.L.L MB15A01
D,C,L
TCXO 15.36MHz
P.L.L. MB15A01
D,C,L
MRFIC2006
POWER AMP 15-20 dB
V.C.O
TXD
POWER CONTROL
TX.ENABLE
HDT 900
RF
MODULE


COMPUTER
CLOCK
7.3728MHz
BUZZER
RS232
DRIVERS/
RECEIVERS
UART
1655
CPU
8031
EPROM
RAM
EEROM
RXD
-T.G
TXD
RF
PORT
VOLTAGE
REGULATOR
+12V
+5V
Size
A
Document Number
HDT 900 LOGIC BD MAP
REV
Date: April 21, 1998
Sheet of