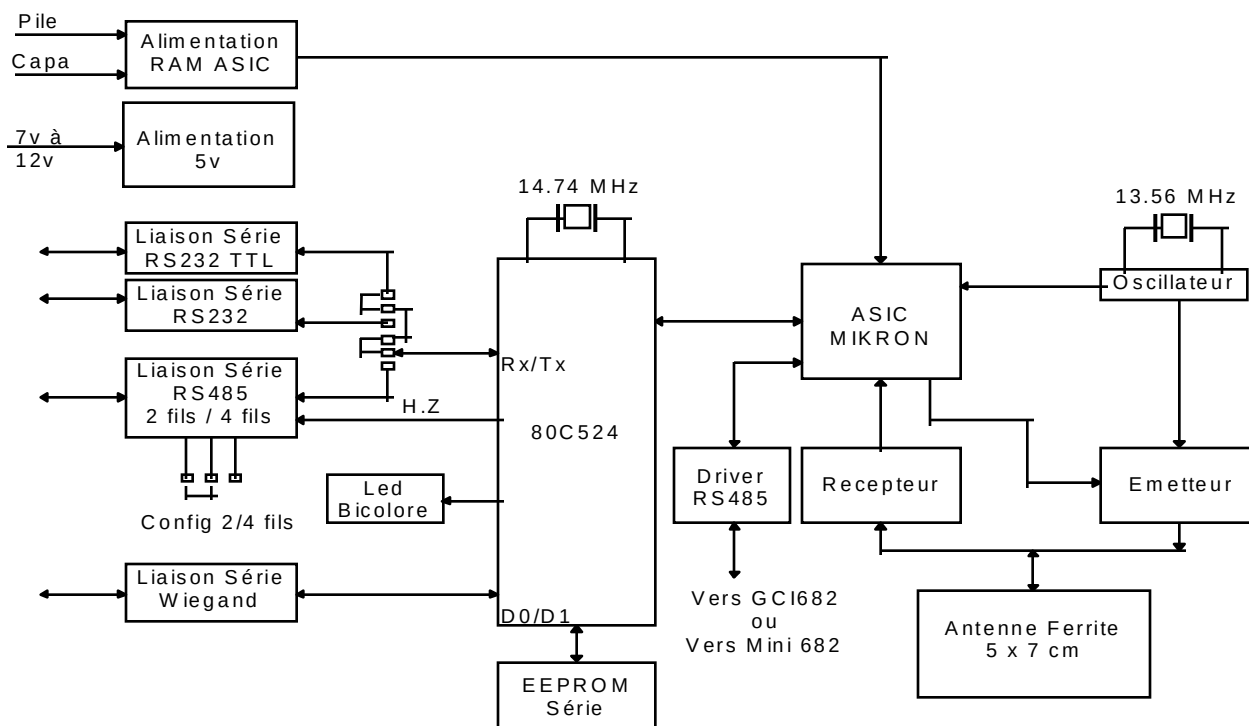


1 BLOCK DIAGRAM(S) OF EQUIPMENT

1.1 Clock list of GemEasyLink 680SP product

Crystal: 14.74 MHz
13.56 MHz

1.2 Block diagram



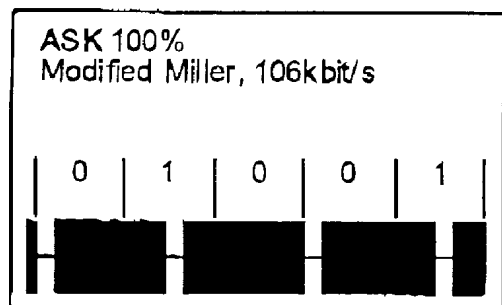
1.3 Pulse width and PRF

The PRF of the device is 106KHz.

The width of the pulse is between 2 and 3 μ s.

The following show the kind of signal interface (issue from ISO 14443, Art 7) and the kind of coding (issue from the manufacturer):

Type A



The coding meets the following rules:

1. A pause at the beginning of the bit grid means '0'
2. A pause in the center of the bit grid means '1'
3. If '1' preceeds '0' the pause indicating '0' is cancelled
4. Communication starts with the start bit '0'
5. If the last bit to transmit is '0' a stop bit '0' is appended

The following example shows the output of the NPAUSE pin when transmitting the 7-bit value 'REQUEST STD' which is 0x26_{hex}.

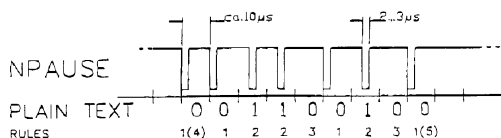


Figure 2