

scott

To: kyle@celectronics.com
Cc: Mike
Subject: FCC ID: MG32081

Mr. Fujimoto:

In order for processing of this application (FCC ID: MG32081) to continue, the following issue(s) will have to be addressed:

1) The block diagram submitted is not sufficient. There is at least one oscillator in the RF section which is shown on the schematics which is not reflected in the block diagram submitted. As per section 2.1033(b)(5) of the rules, the block diagram shall show "the frequency of all oscillators in the device. The signal path and frequency shall be indicated at each block. The tuning range(s) and intermediate frequency(ies) shall be indicated at each block." Please submit a more detailed diagram.

The item(s) indicated above must be submitted before processing can continue on the above referenced application. Failure to provide the requested information within 60 days of the original e-mail date may result in application dismissal and forfeiture of the filing fee. Also, please note that partial responses increase processing time and should not be submitted. Please upload responses and/or exhibits to the electronic filing website. Your correspondence number is MG32081-1.

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Correspondence by Project

Project Number:

1099066944

Correspondence Number	Memo
MG32081-1	1) The Revised Block Diagram is shown - - the MicroProcessor speed is 8 MHz.