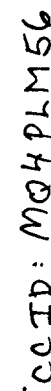


50442

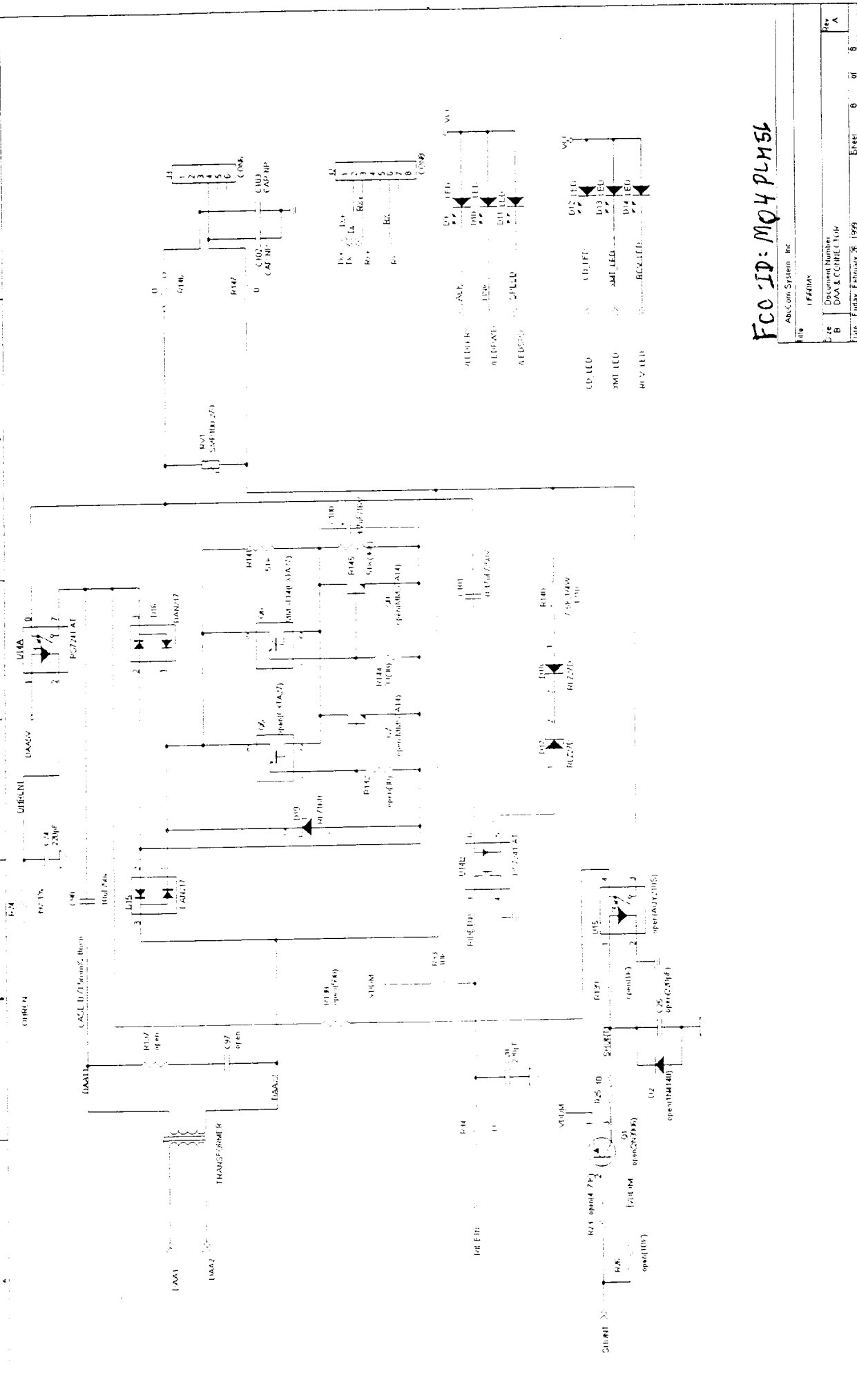


Abacus System, Inc.
 d/b/a

1622MC

Doc #	Doc Number	Page	of
4	BURK HANCOCK	2	8

Date: Tuesday, April 08, 1997



FCO ID: MQ4 PLH5L

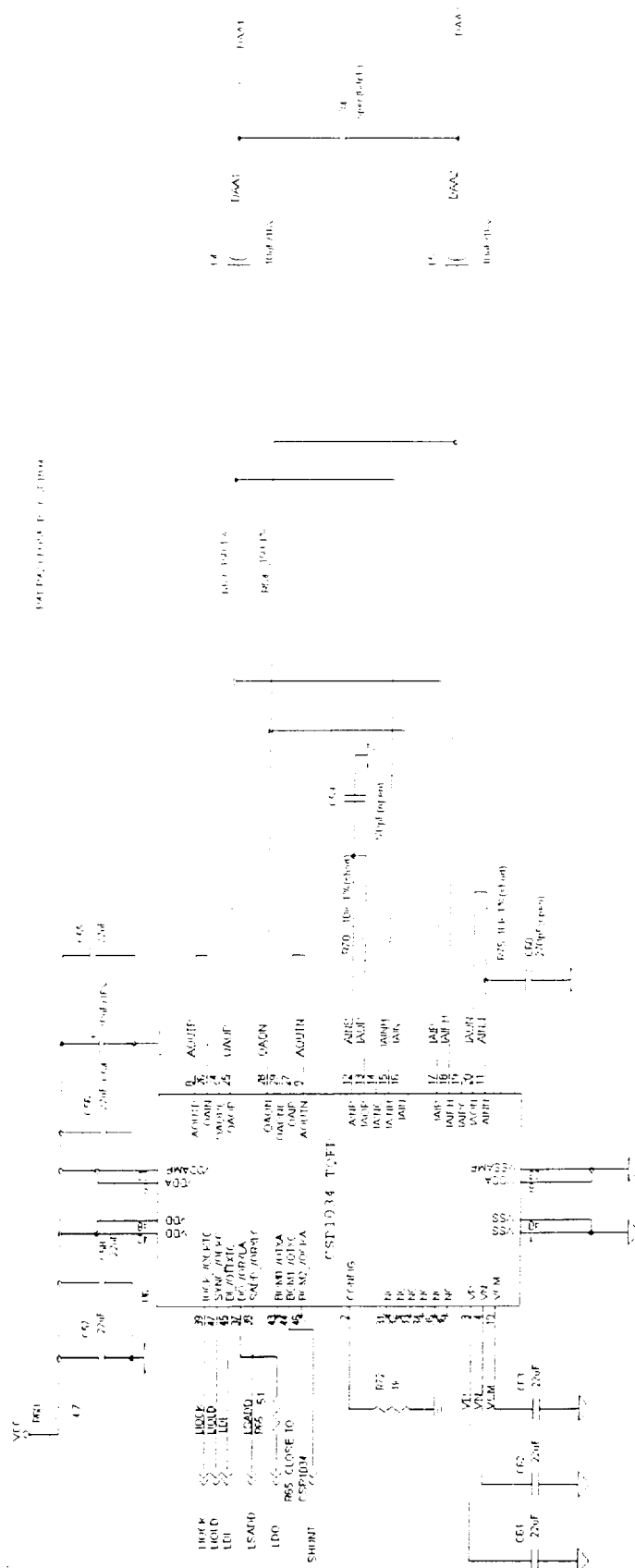
Title		AbCom System, Inc.
Revision		1.0
Document Number		DAQ & CONNECTION
Date		February 26, 1992
Page		8 of 8

1) MII Data Address: add(3, 0) to add(3, 15)
2) Addressed into a 100K, set to an appropriate register

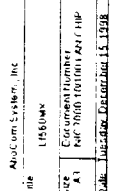
LR is for connection digital 50K and 10K and 10K 20
LR is for connection digital 50K and 10K and 10K 20

LB is for connection digital GTE and U12 Analog GTE.
 L2 is for connection digital GTE and U12 Analog VFC

NOTE THE RECOMMENDATION THAT WE BE PLAYED DIRECTLY WITH ANALOG SIGNALS AND THAT ORIGINAL RECORDS BE CONNECTED TO THE POWER LAYER. ALSO, LOWER SHOULD ALSO BE CONNECTED TO THE COVER A PRIVATE POWER LAYER. POWER IS FLOWING INTO HIS WHICH RUNS TO THE NEW POWER LAYER. CHAIN IS IN PLACE IN THE NEW POWER LAYER AND A FINAL SIGNAL IS RUNNING TO THE LAYER SHOULD ALSO RESULT IN THE SAME LAYER. AND CROSSES THE DIGITAL ANALOG SIGNALS EFFECTIVELY BELOW THE SOUNDING OF THE

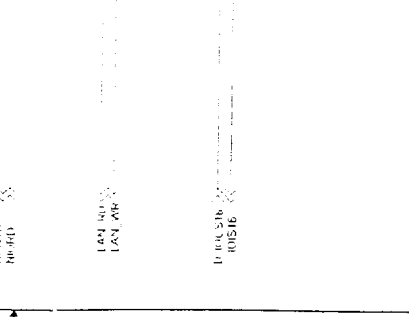
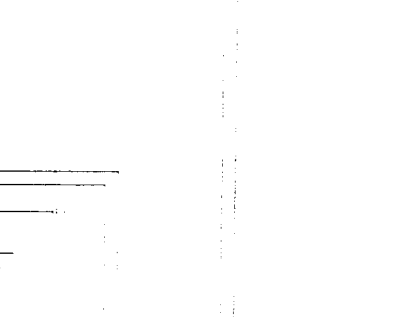
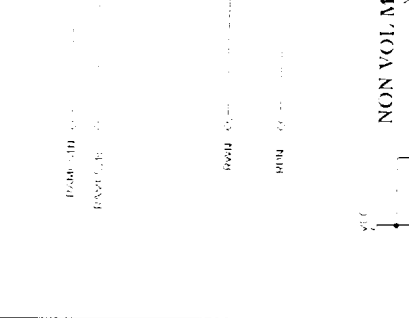
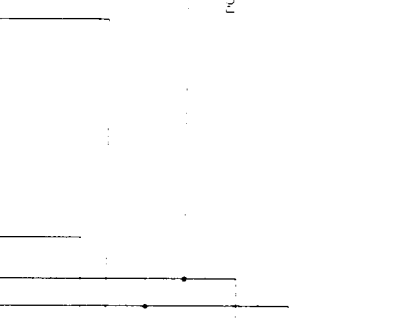
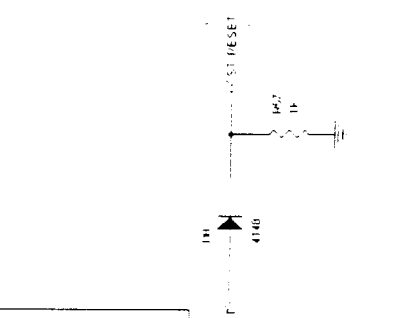
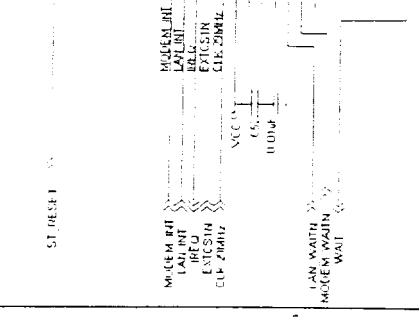
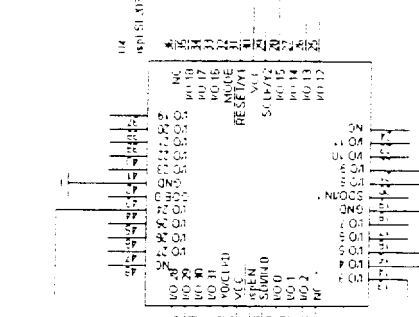
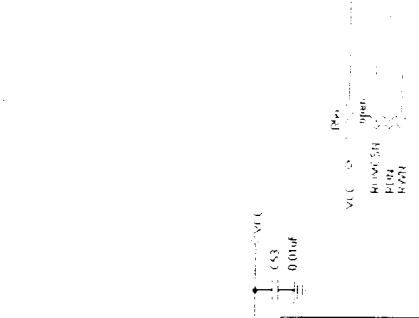
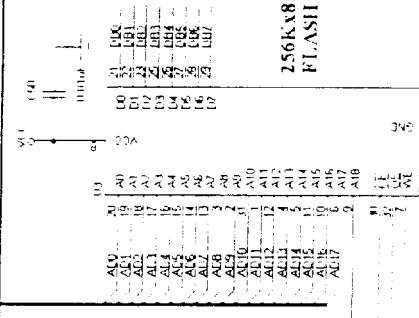
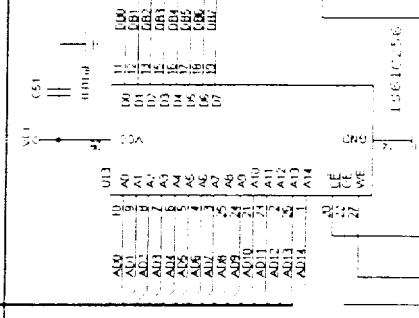
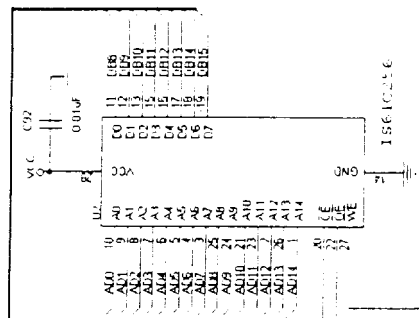


NOTE: THIS SCHEMATIC SYMBOL
REFERS TO DIGITAL CIRCUITS.
HOWEVER, THESE NETS ARE
CONNECTED TO ANALOG CIRCUITS IN
THE NETLIST FILE.

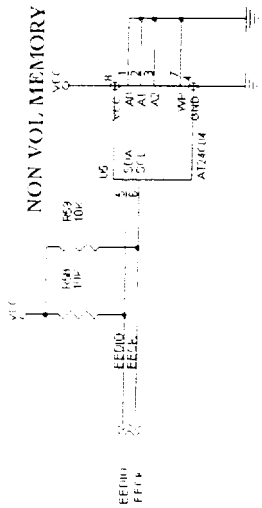


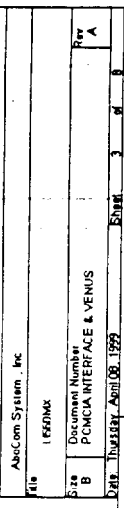
AL00 19

AL00 19



NON VOL MEMORY





AbiCom System, Inc	File	155DMX	Document Number	PCMCIA INTERFACE & VENUS	Date	Thursday April 08, 1999
	Size	B				