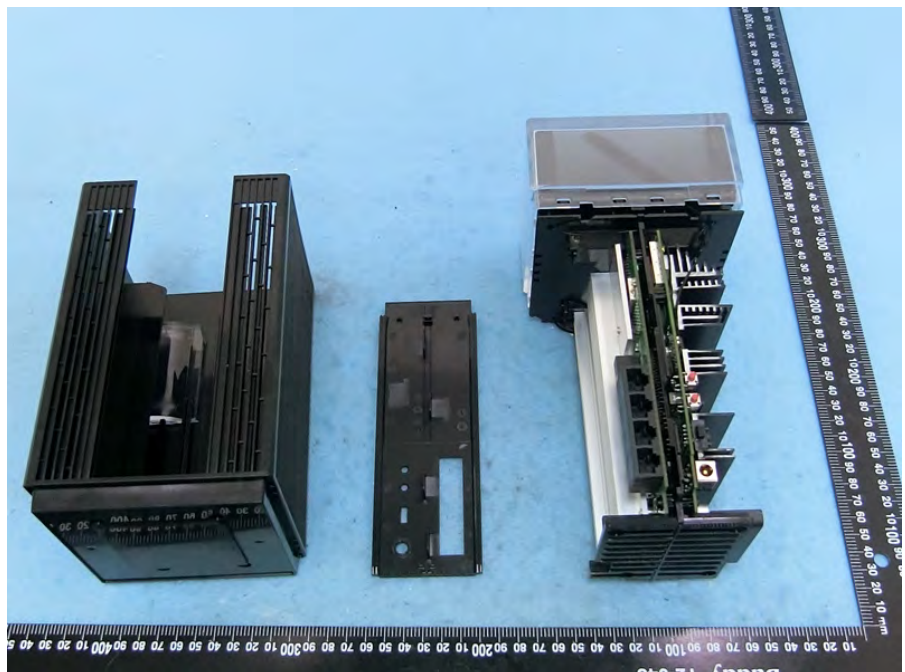
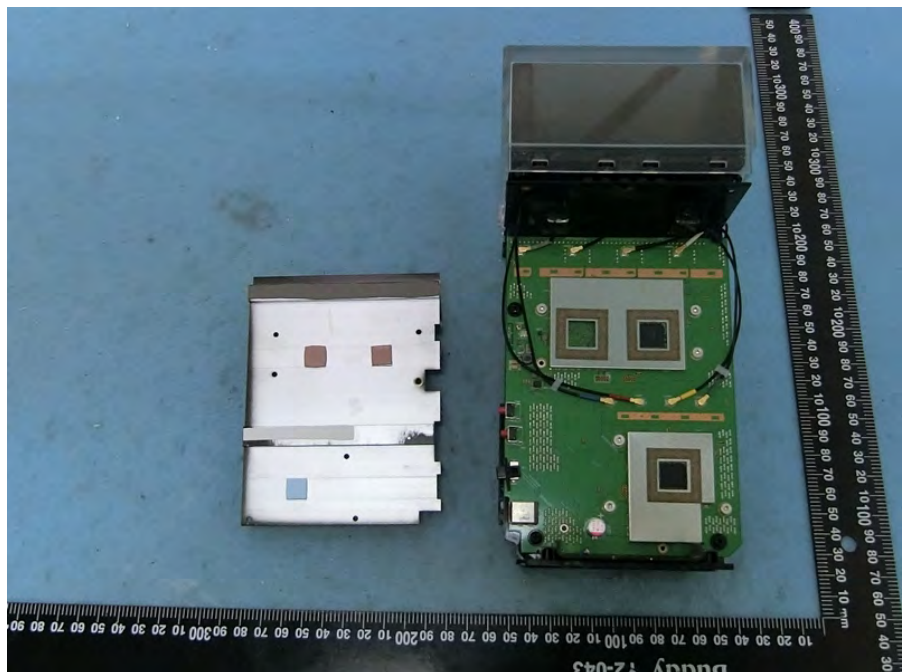
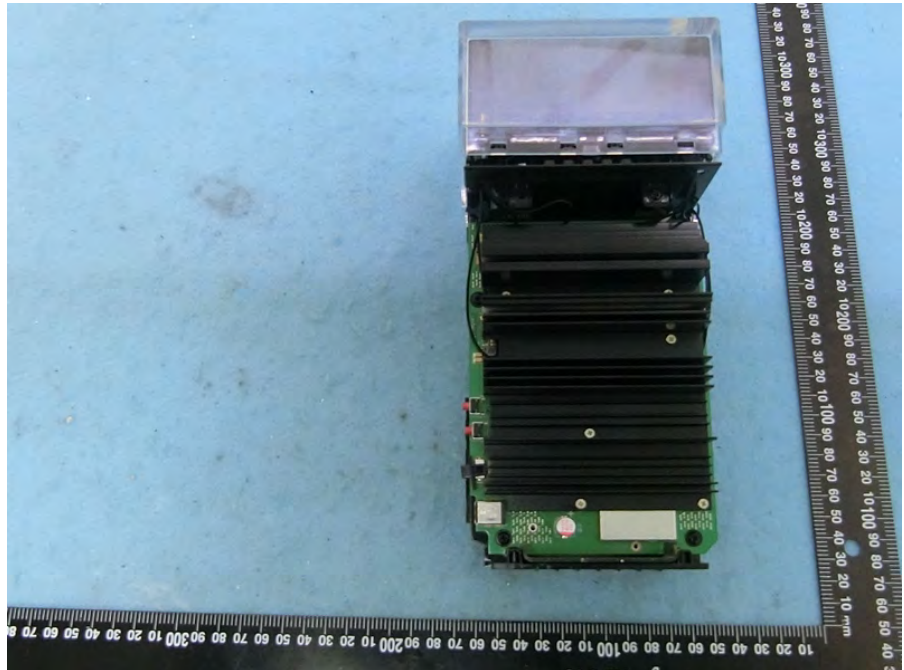
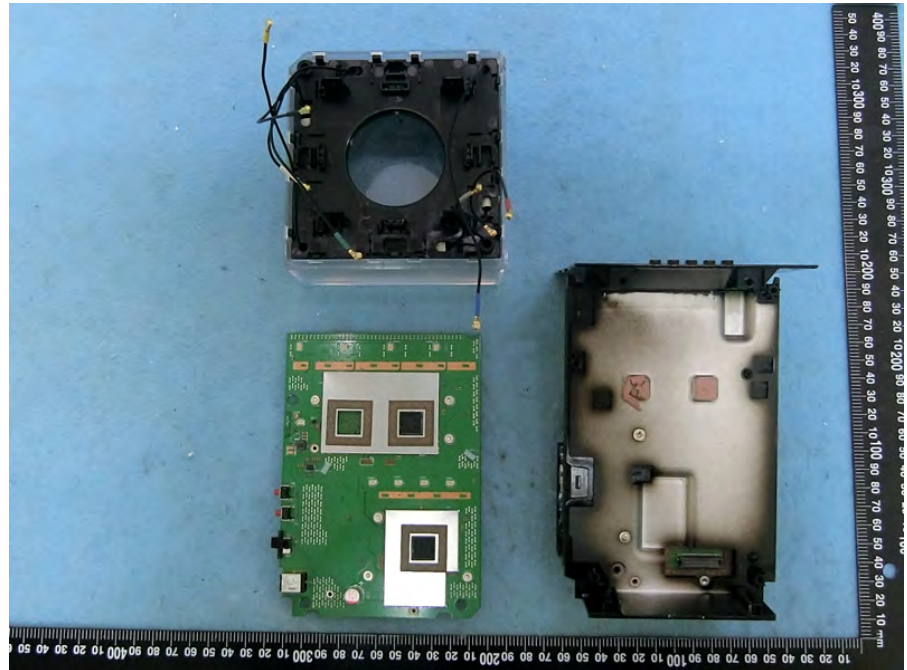


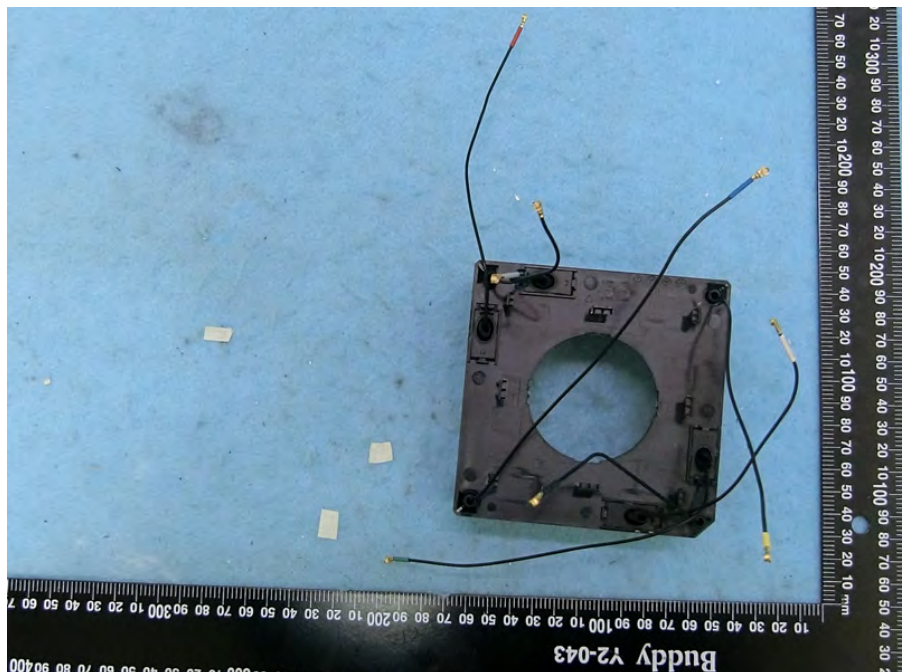
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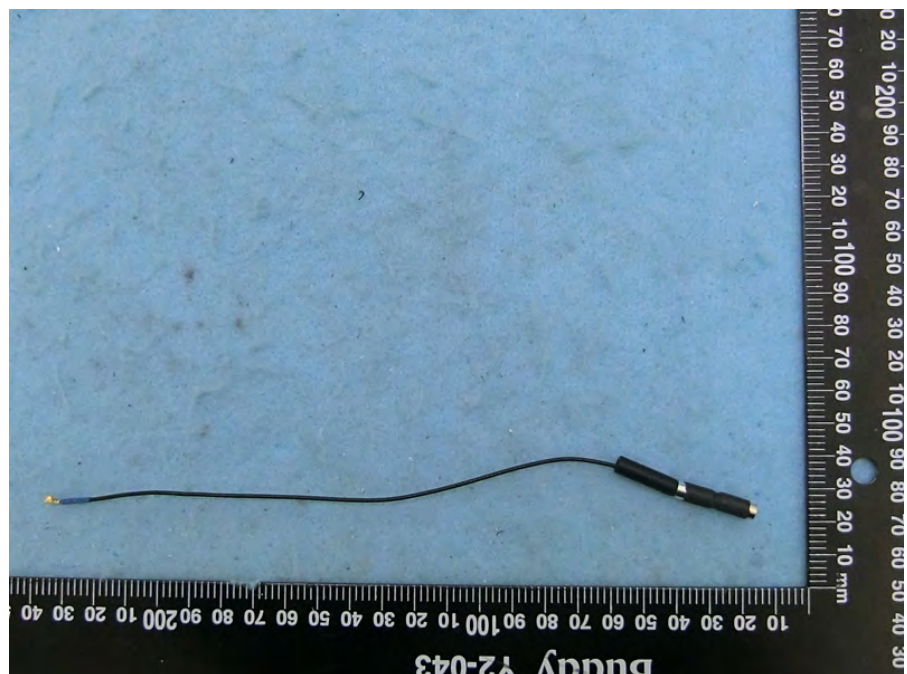
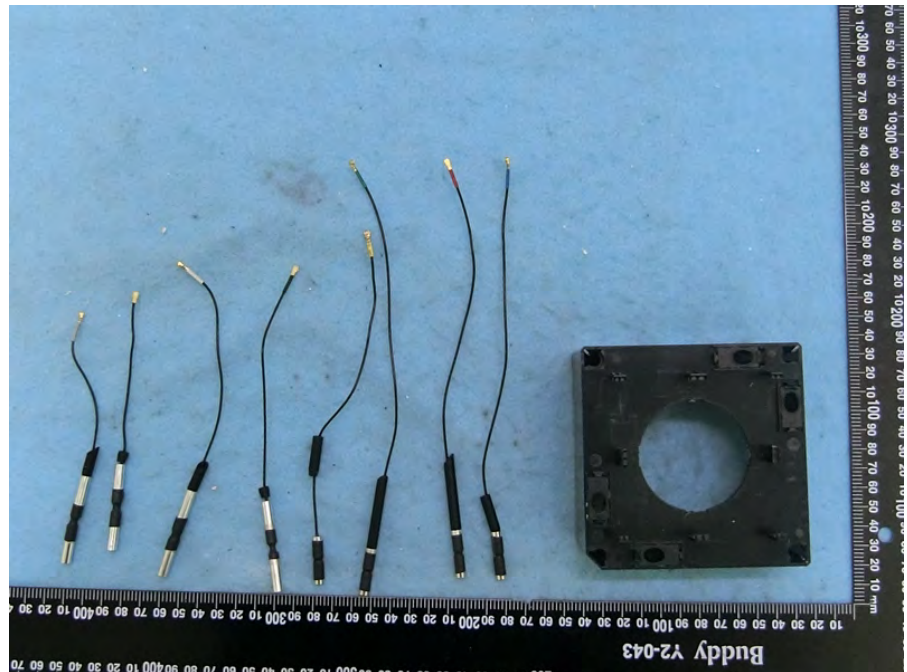


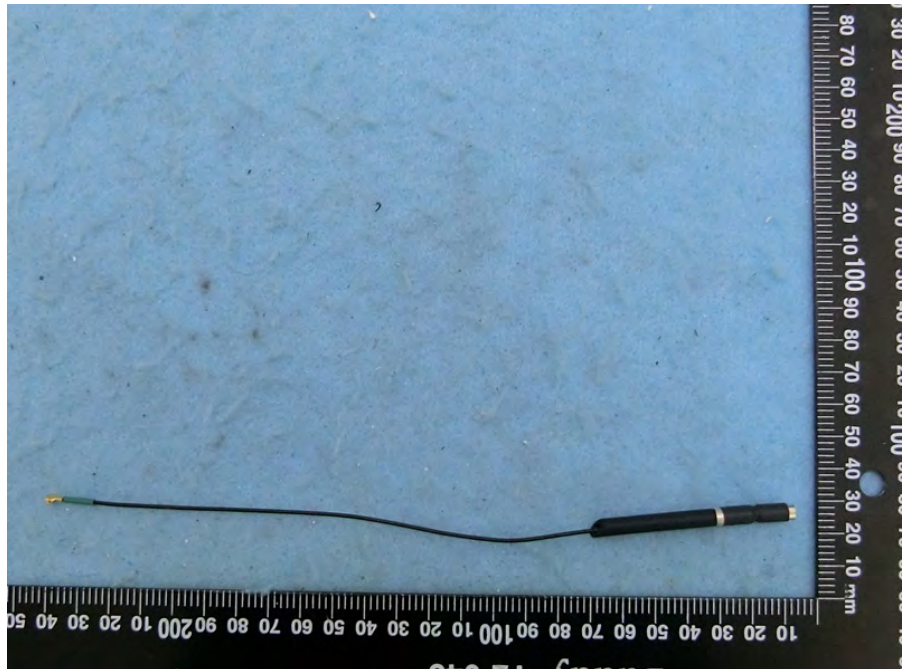
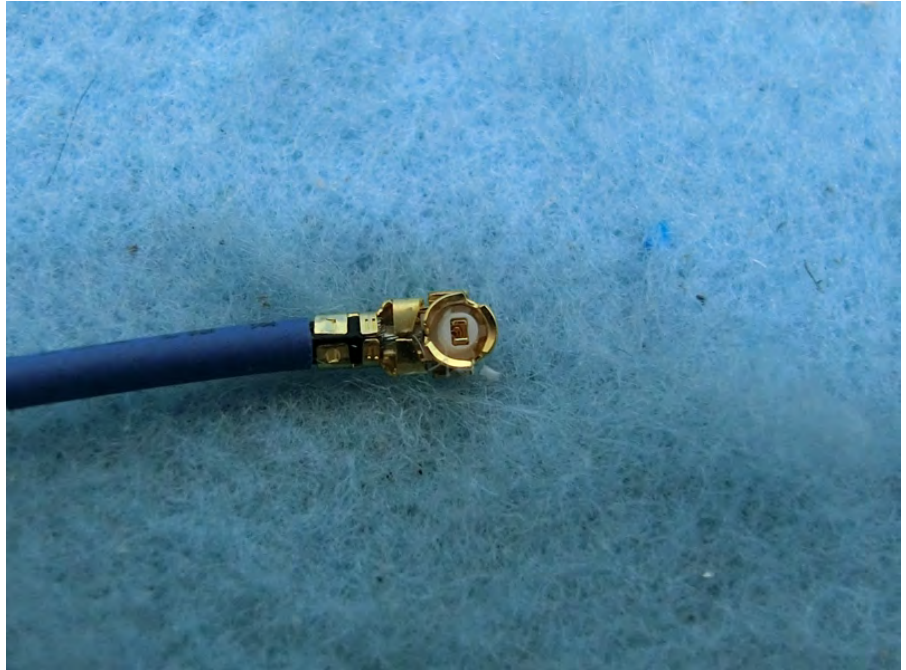


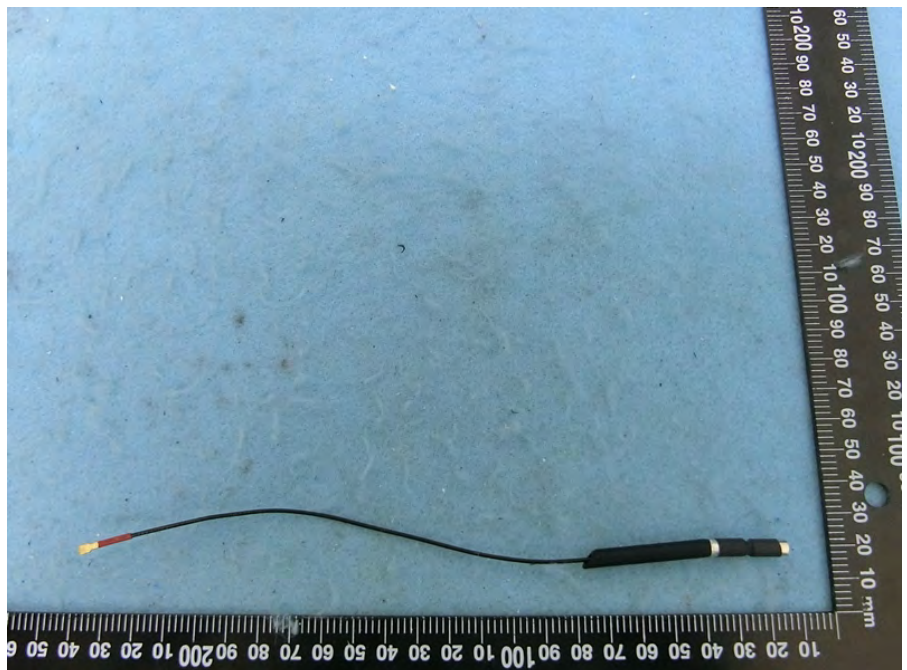
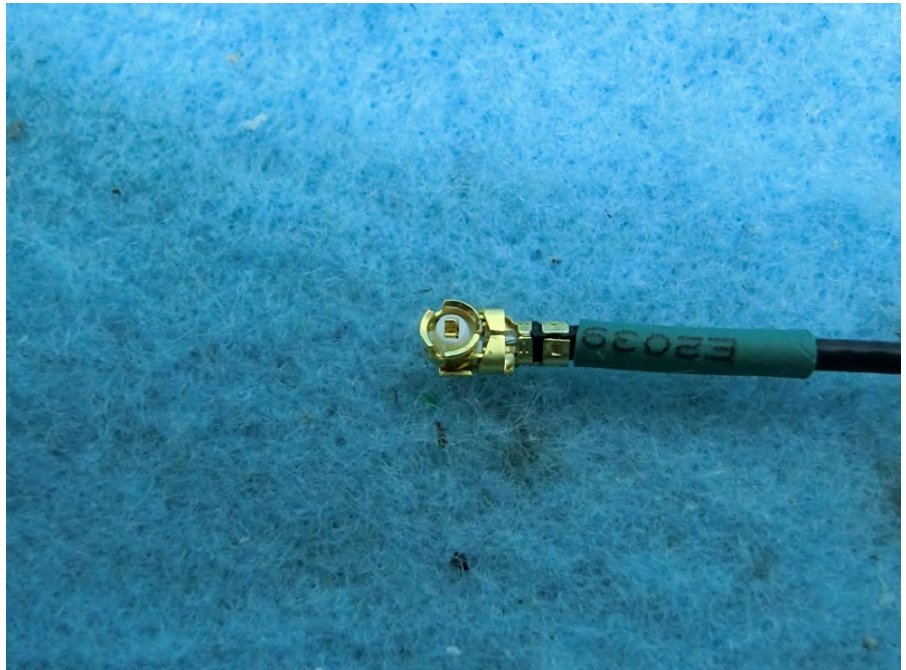


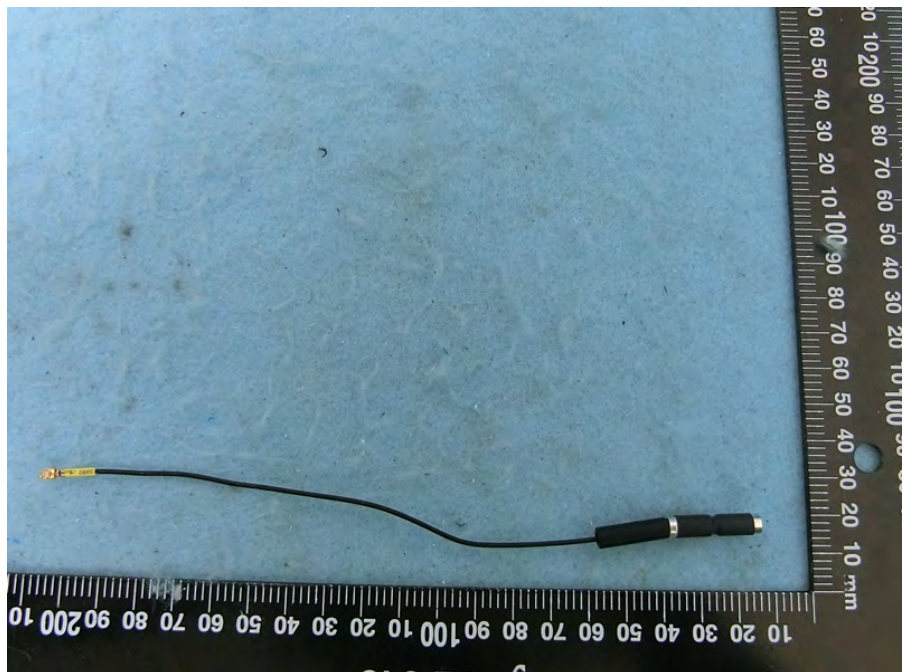


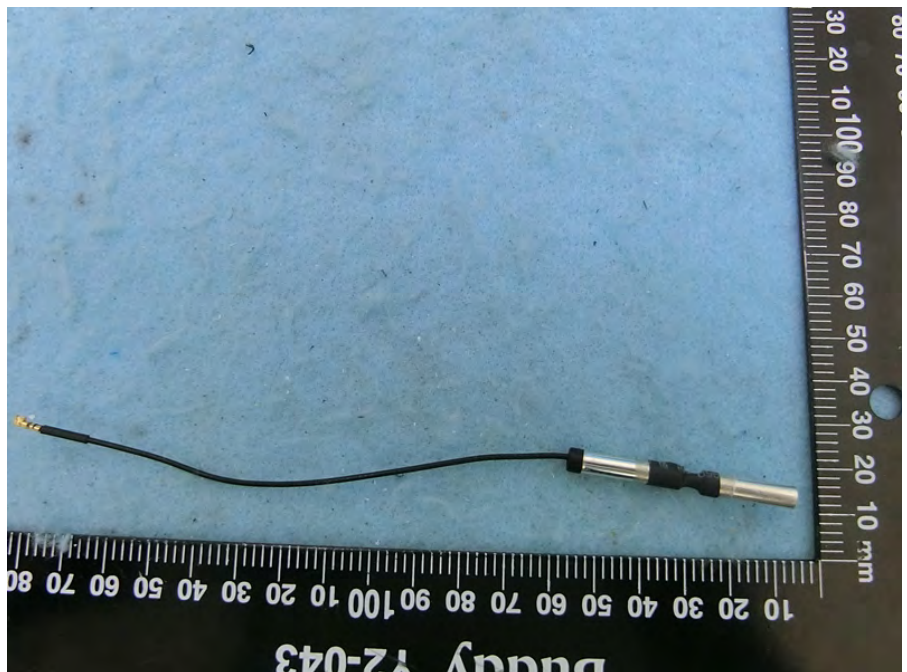


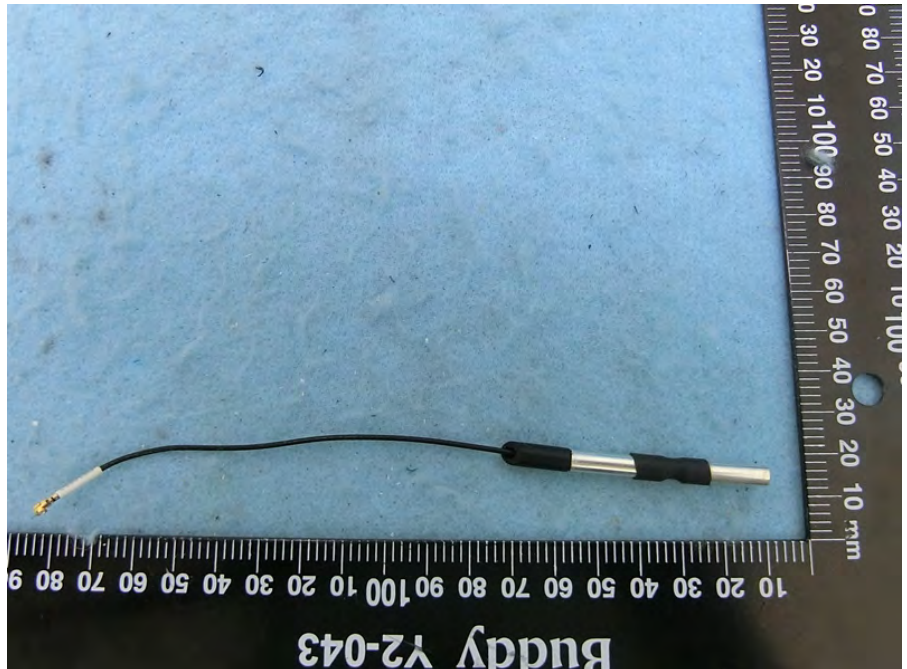


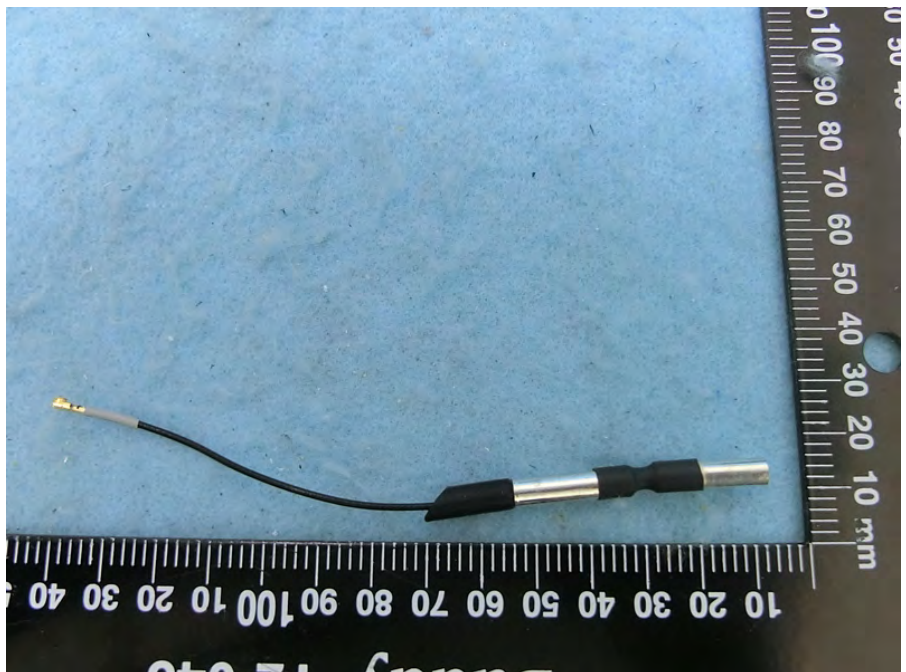


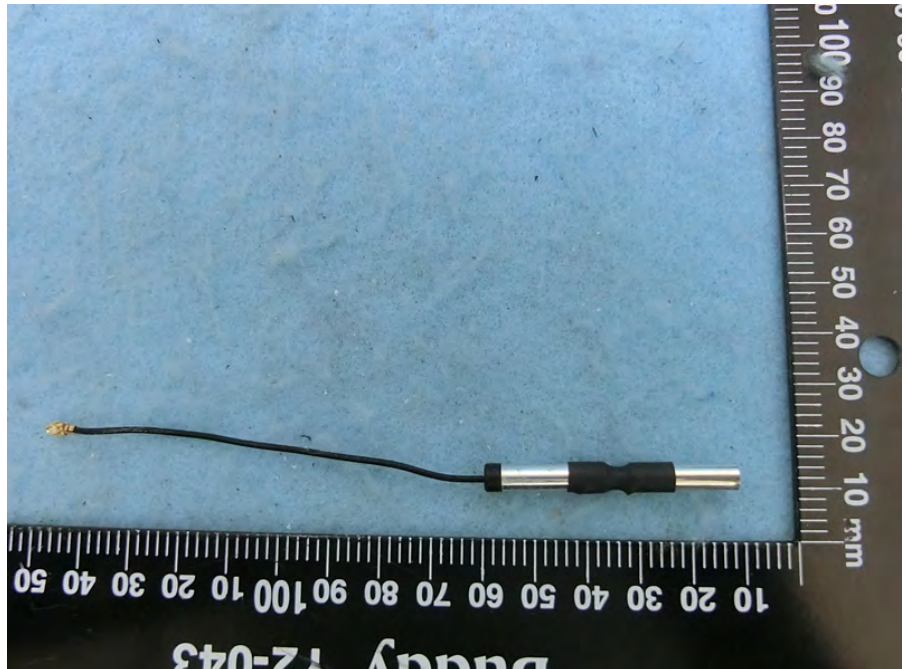
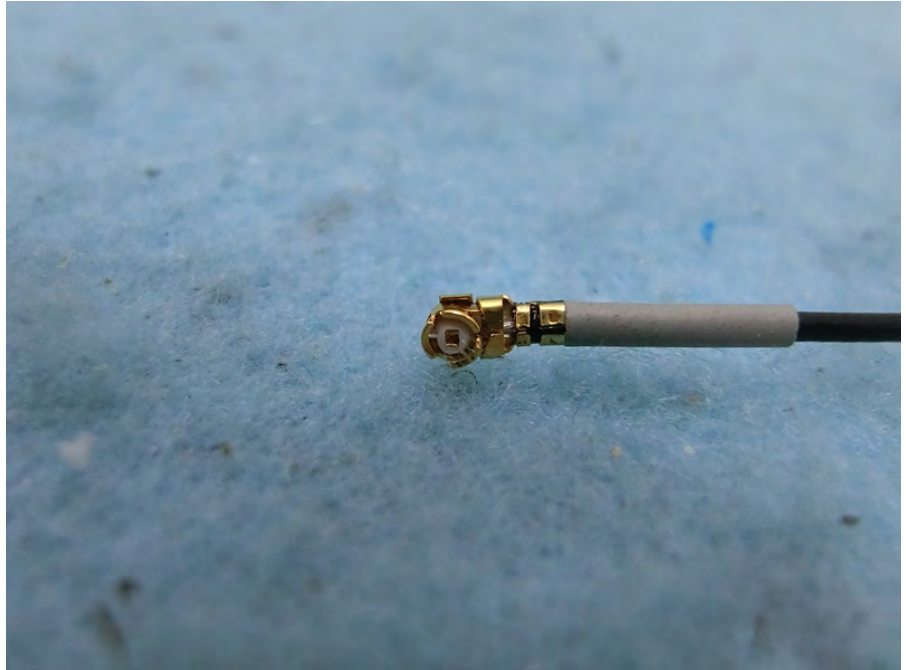


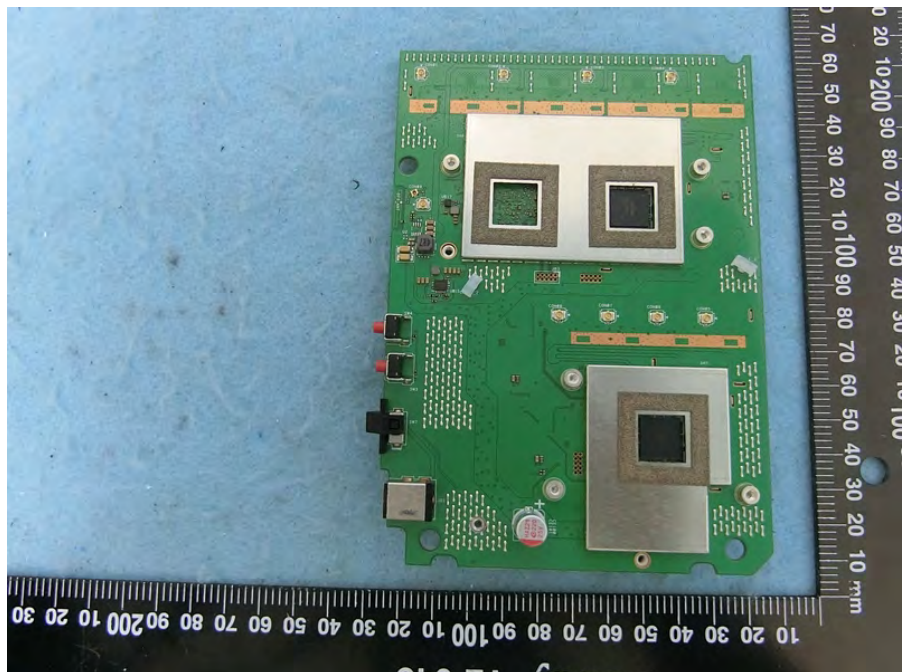


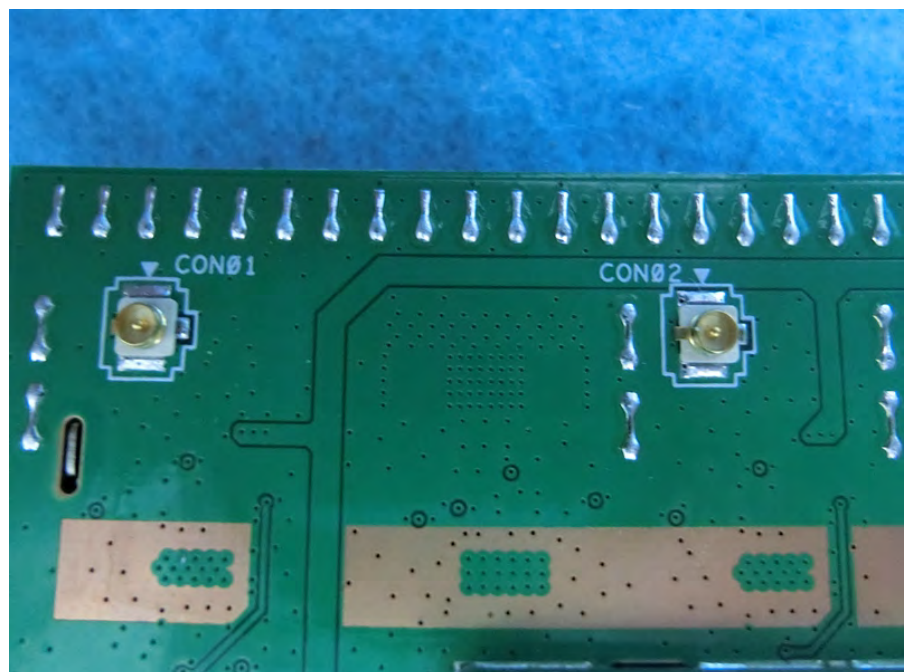
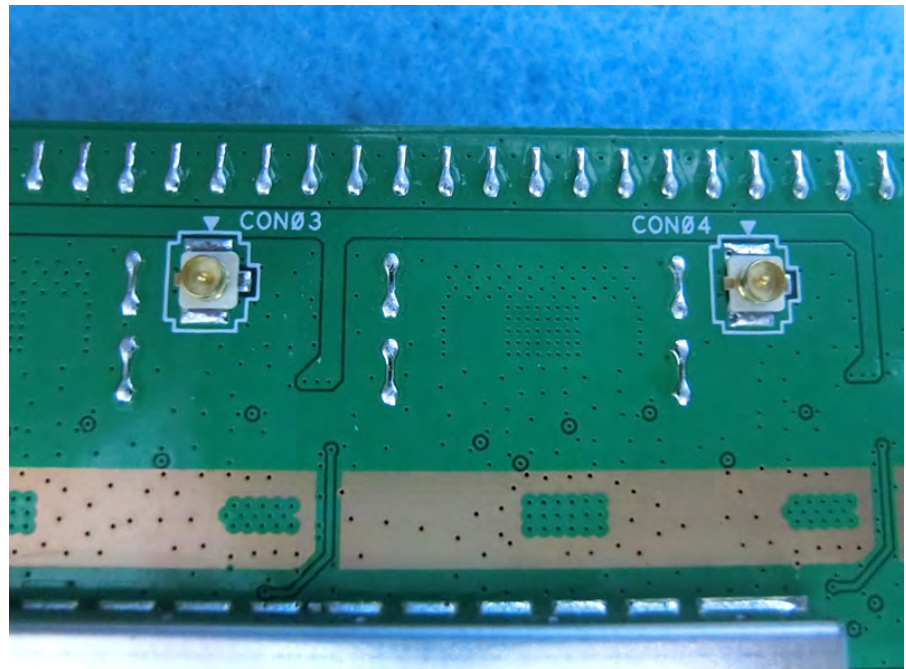


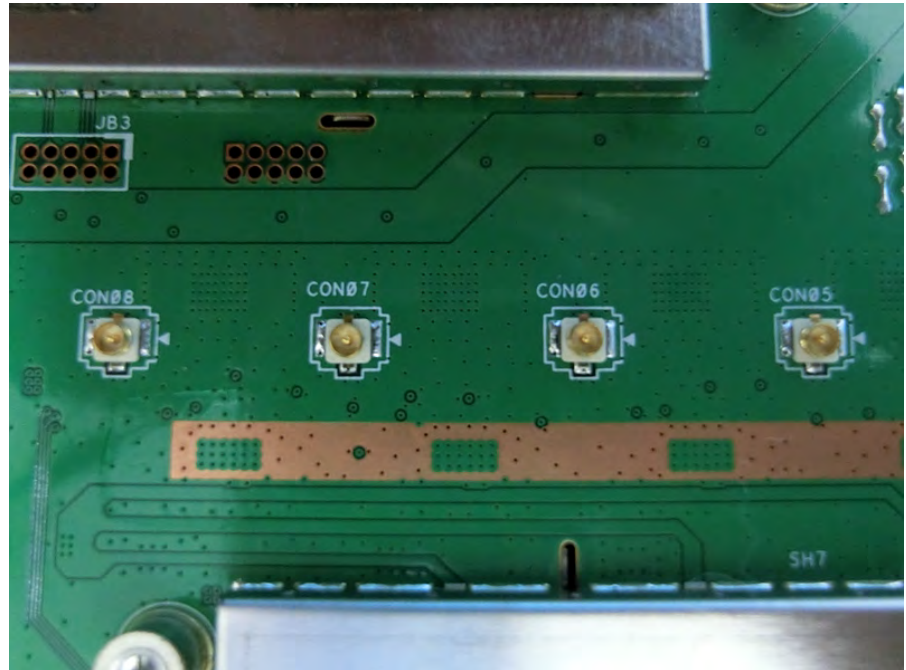


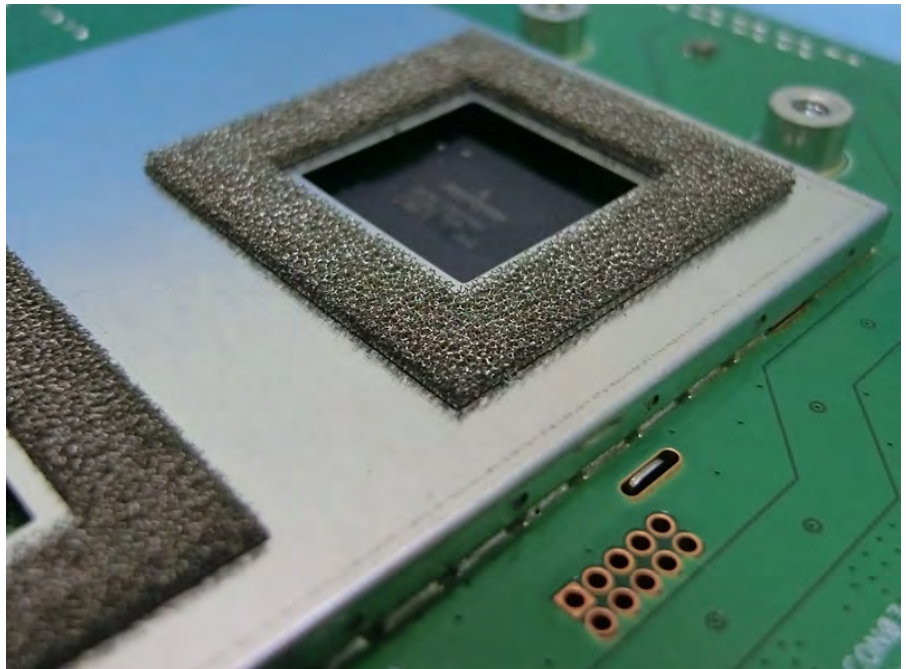
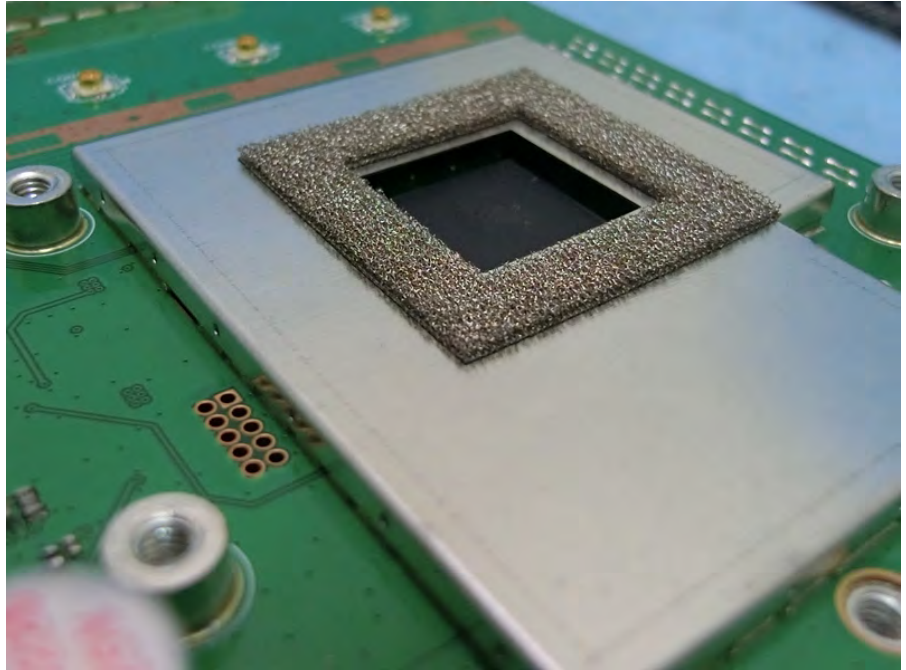


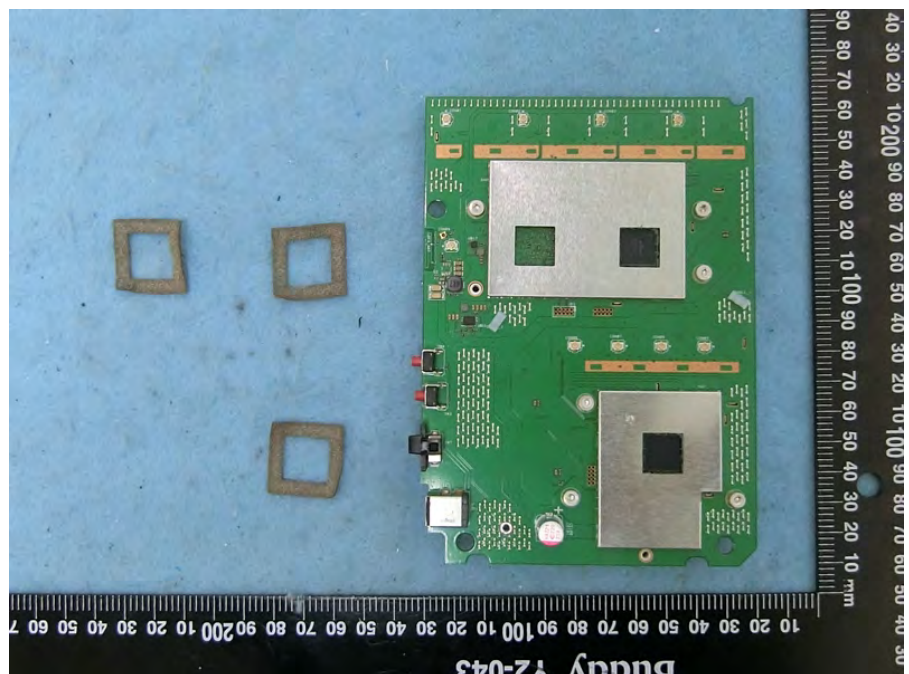
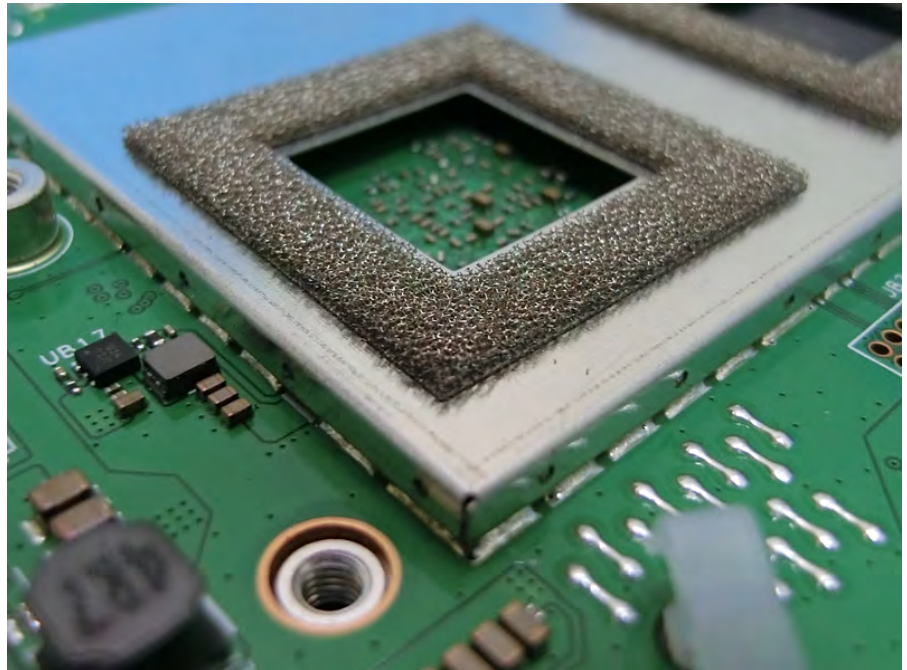


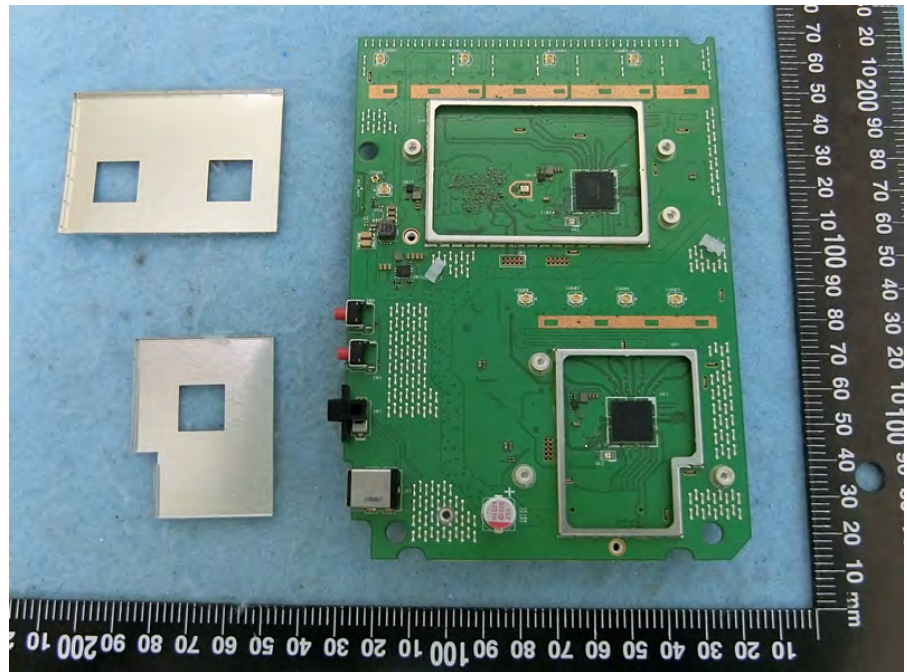




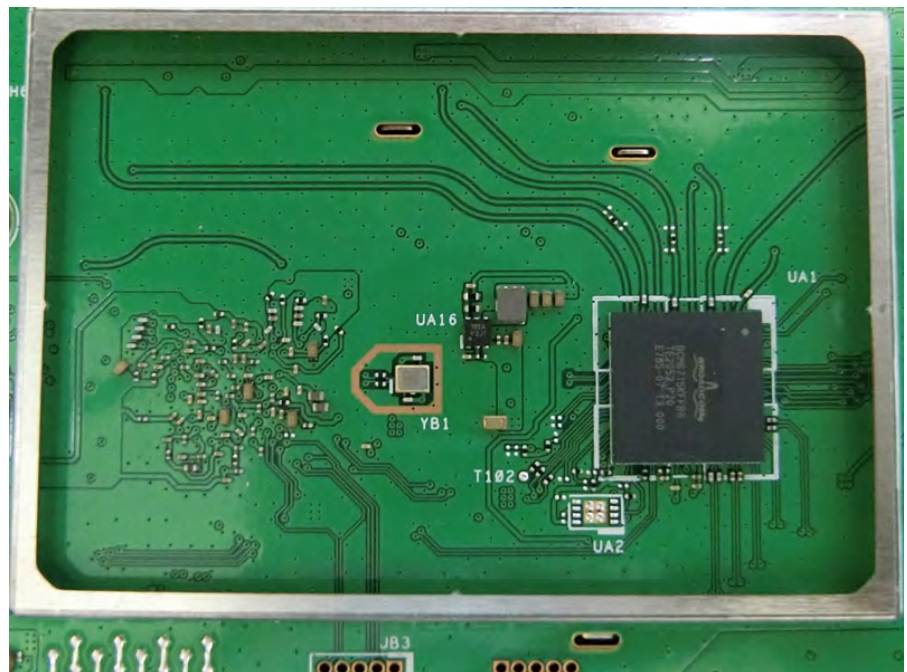


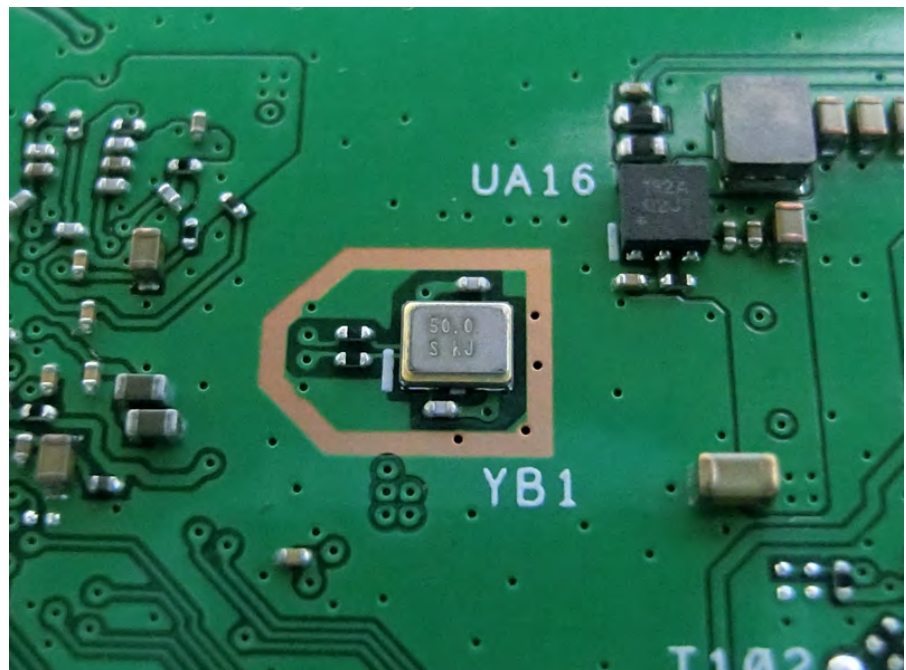


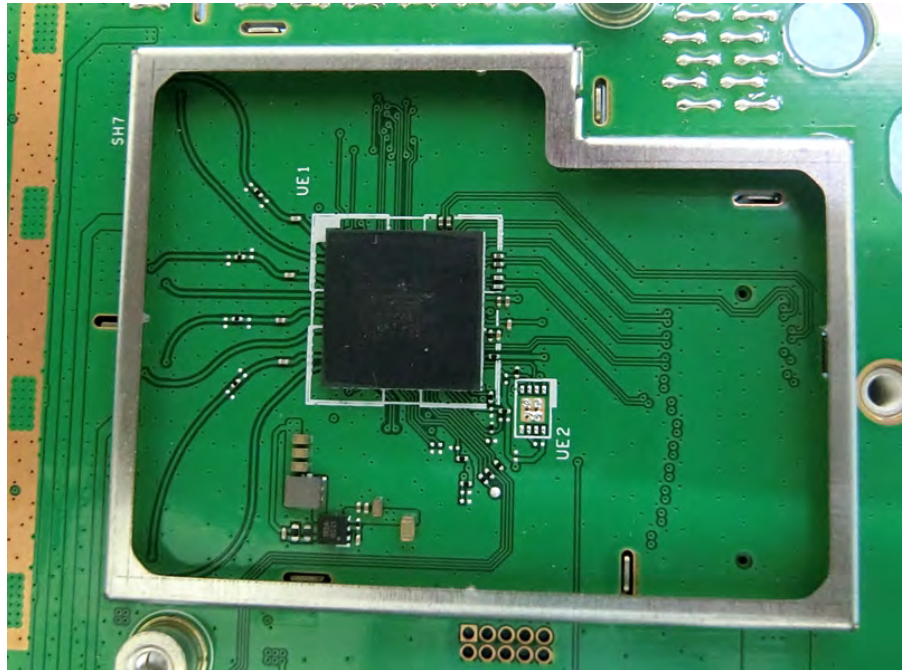


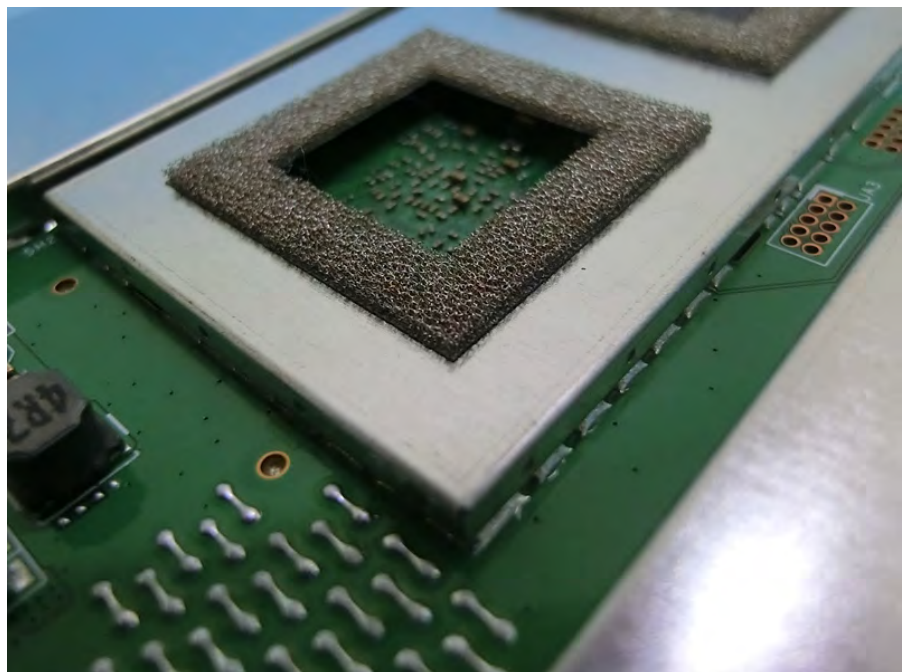
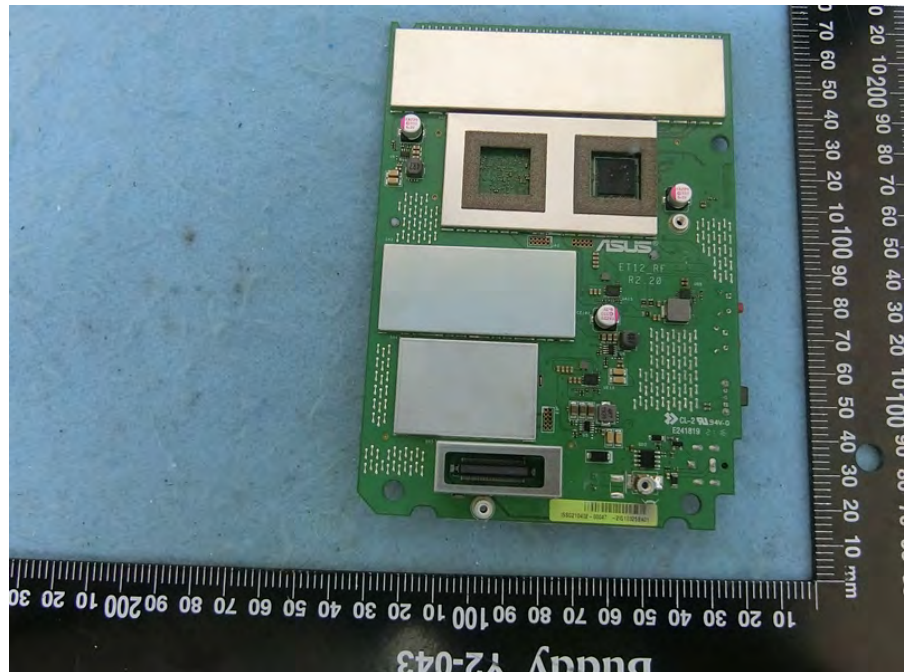


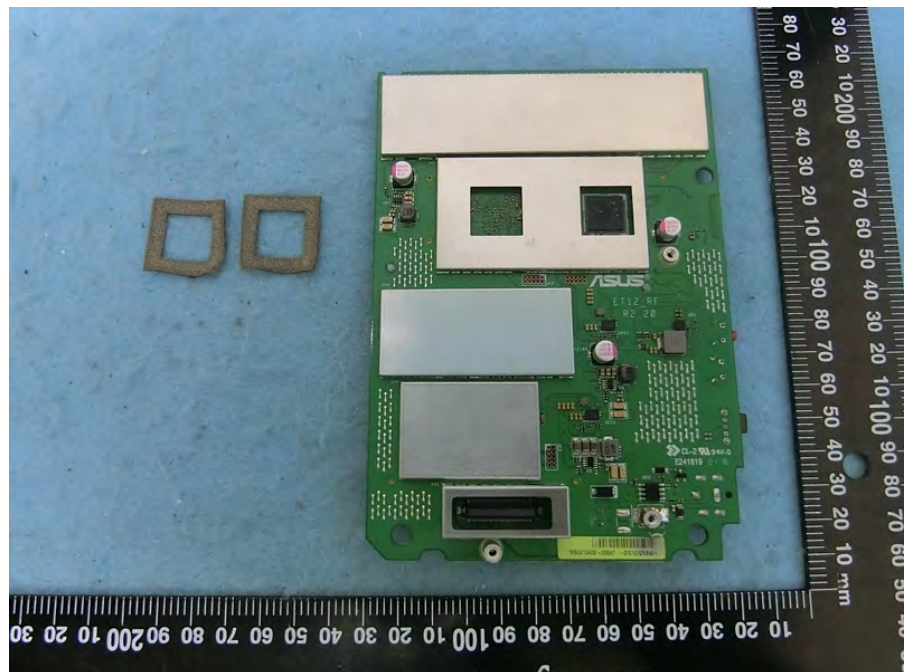
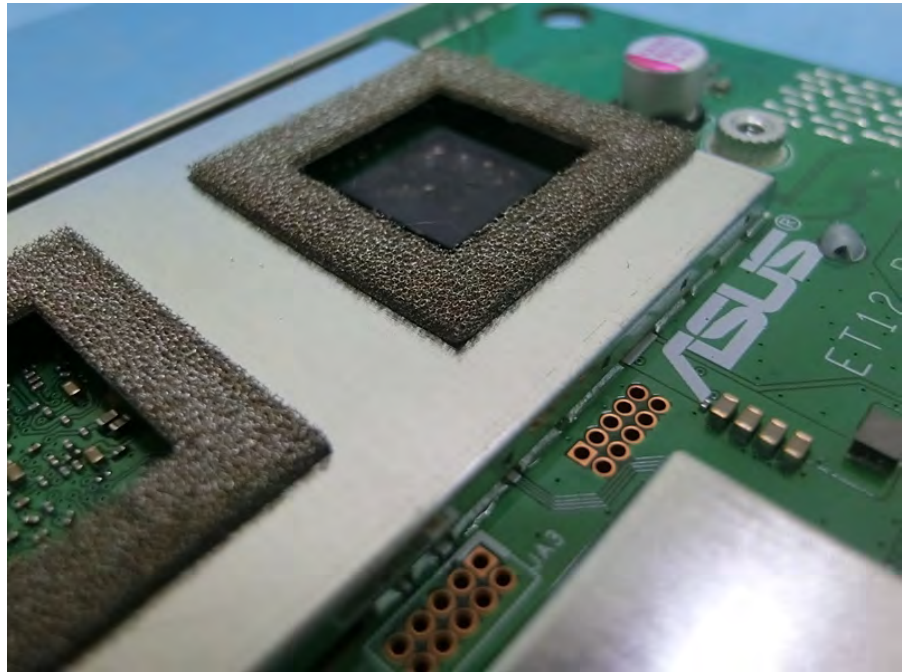
MLCC on the path of the CPU
Main Source

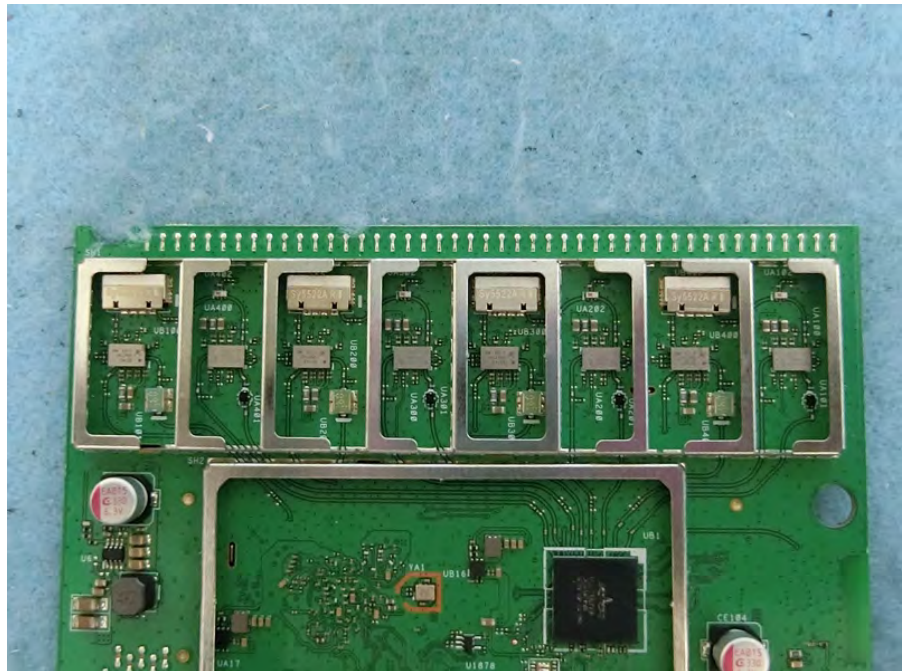
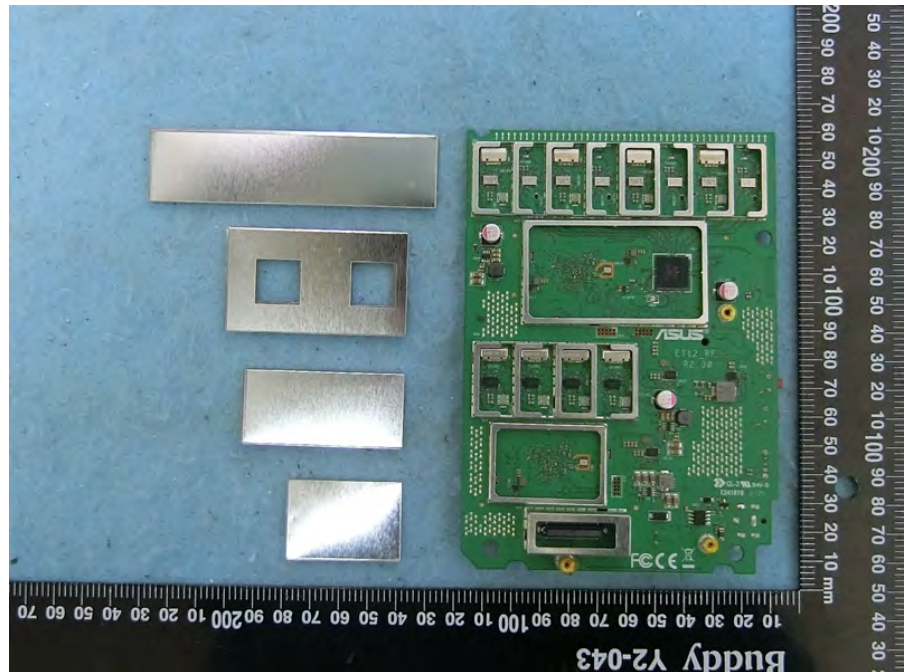


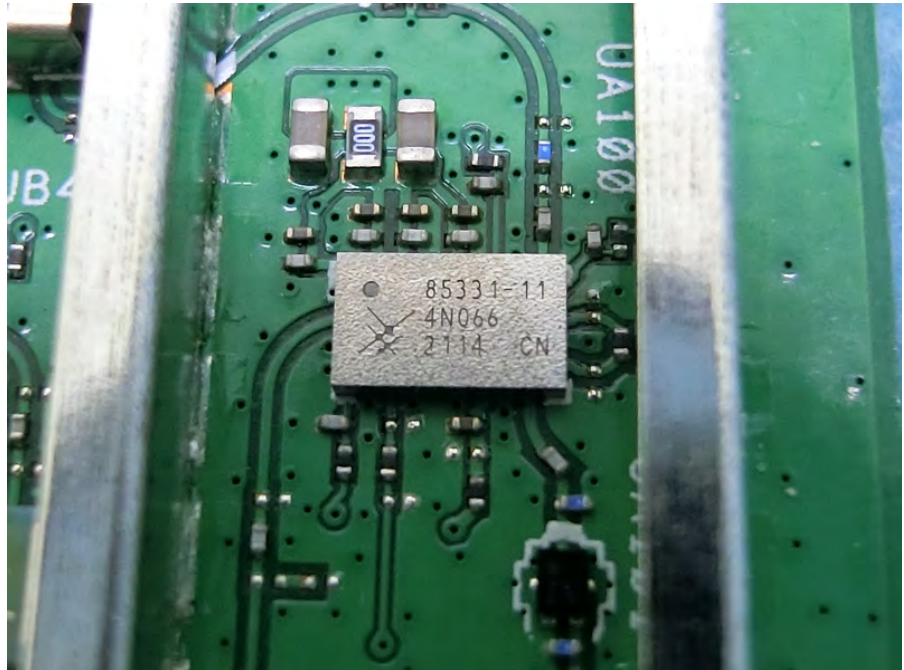




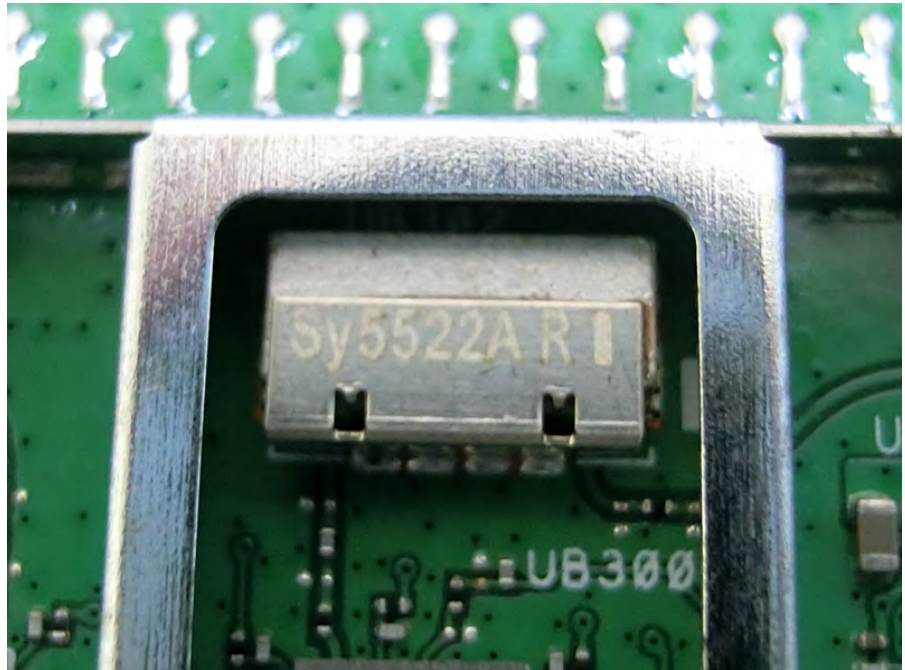


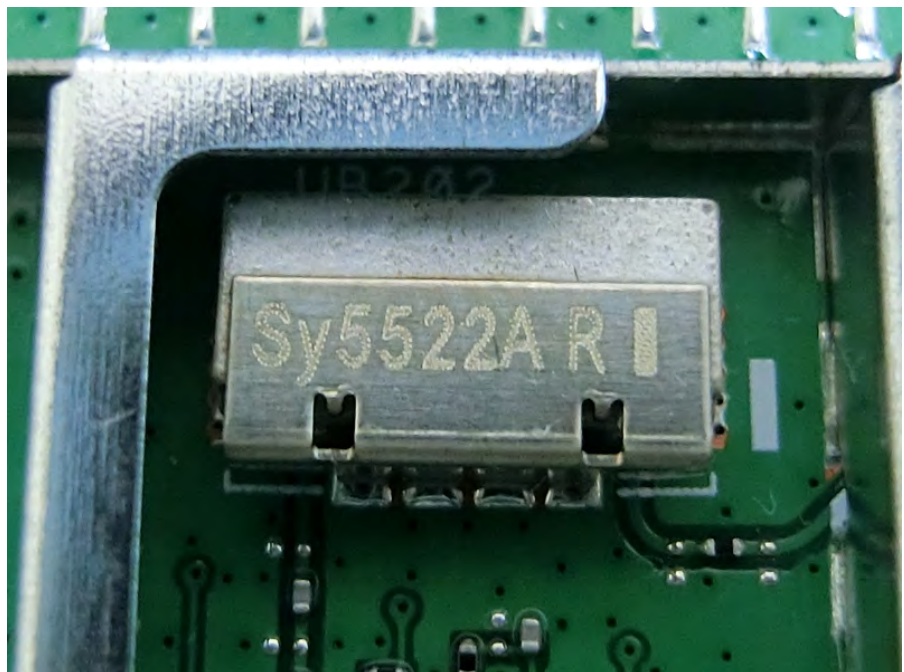
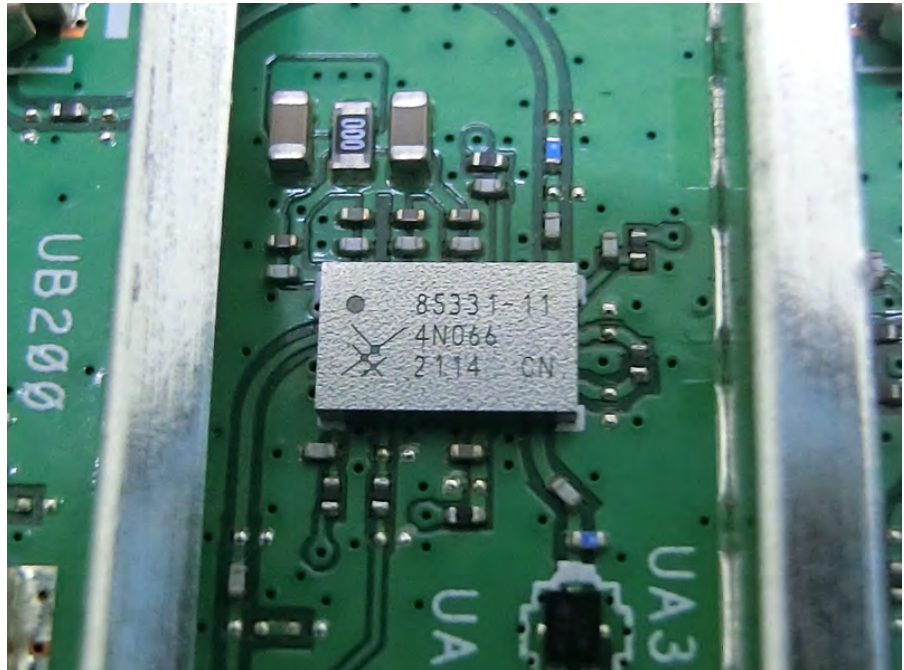


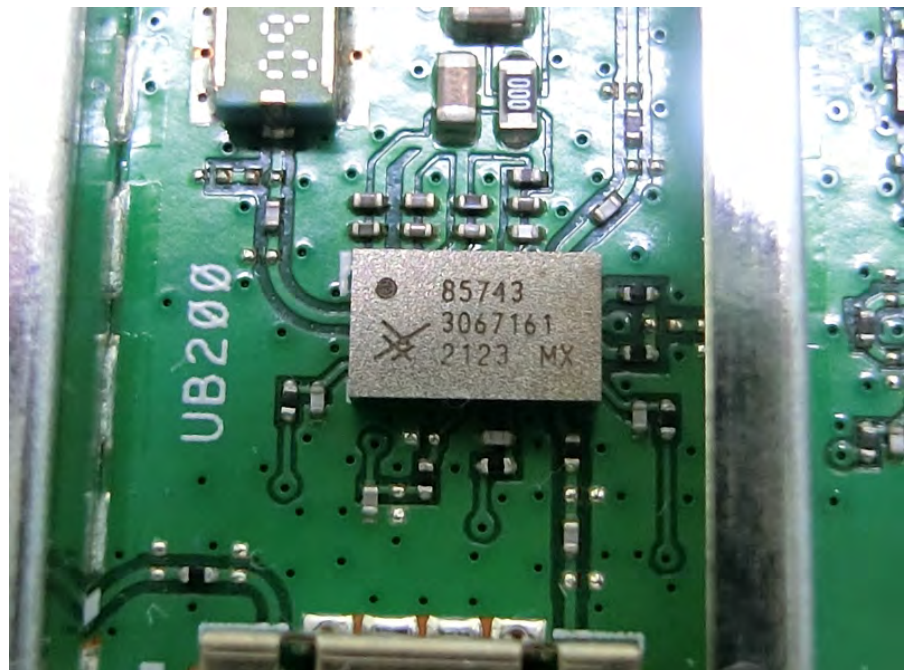
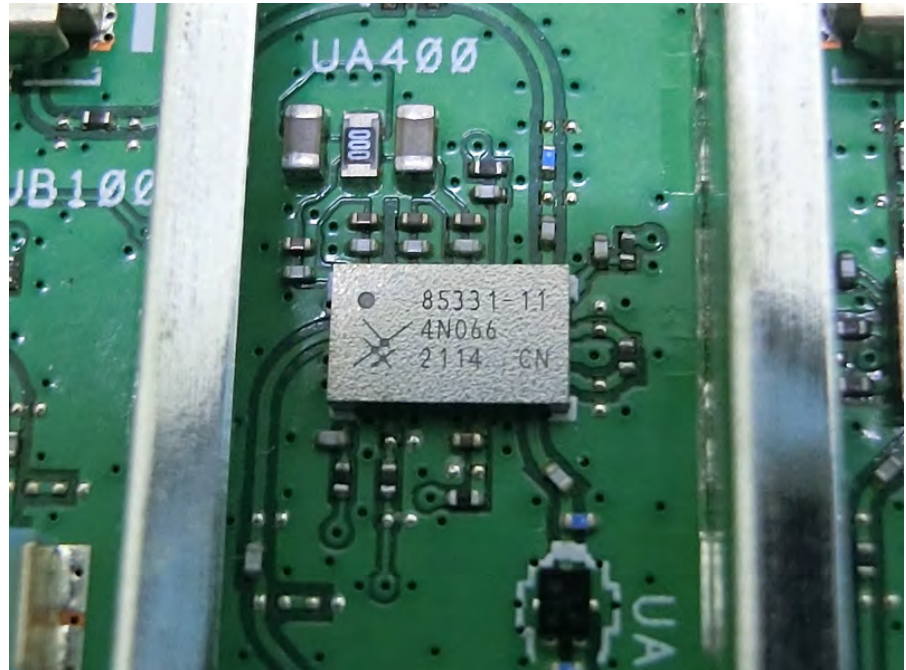


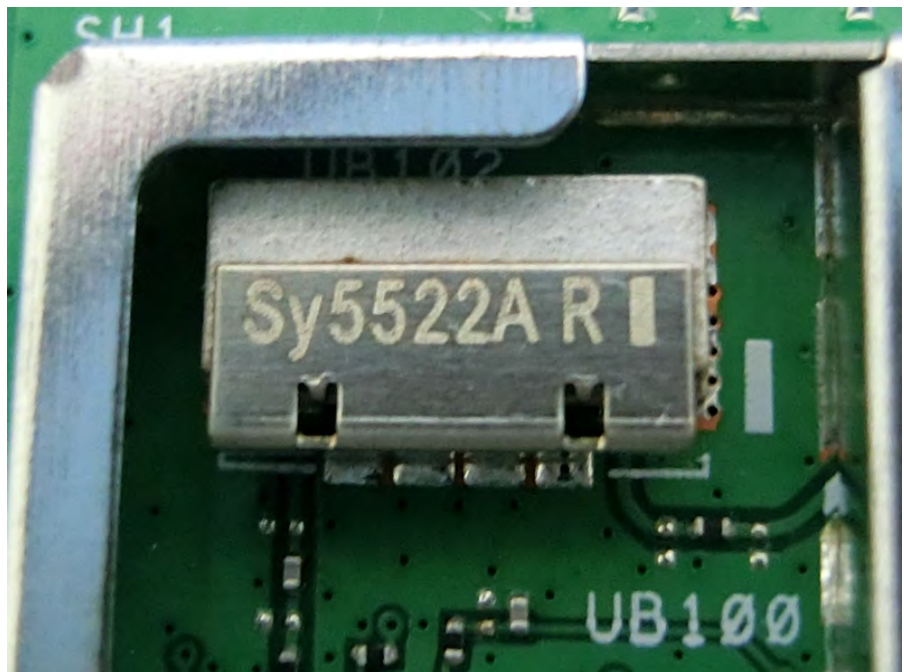
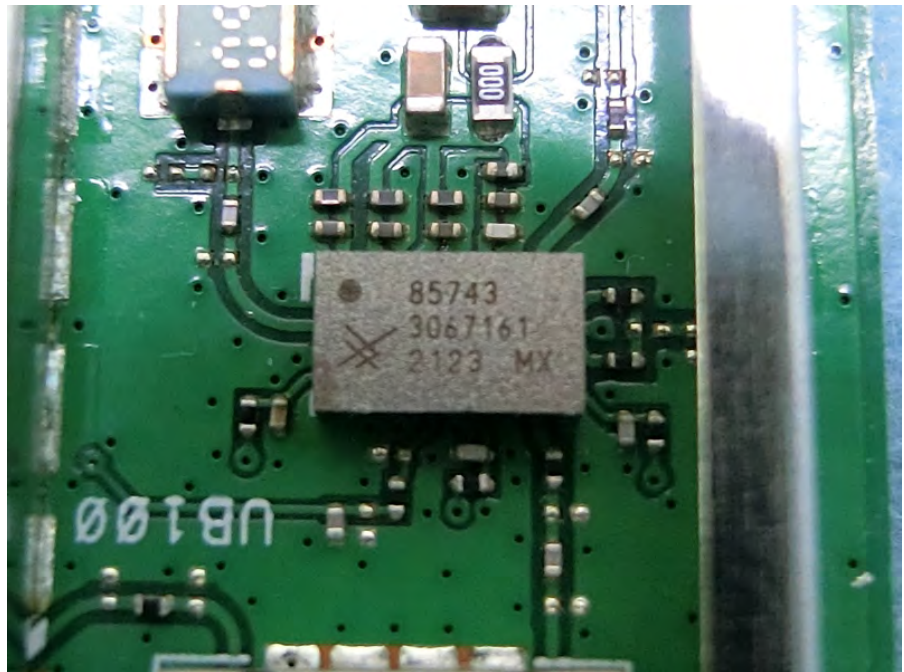




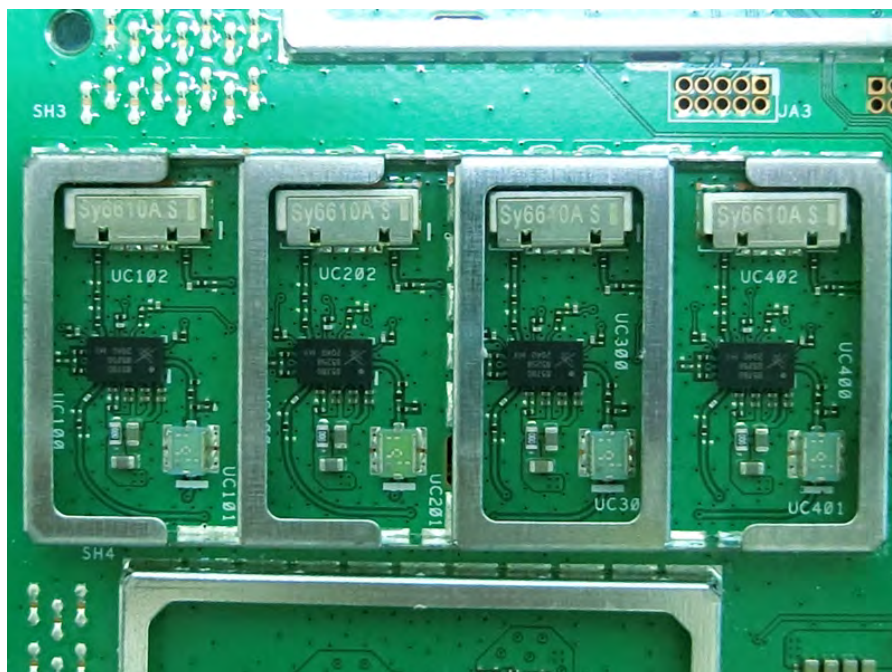


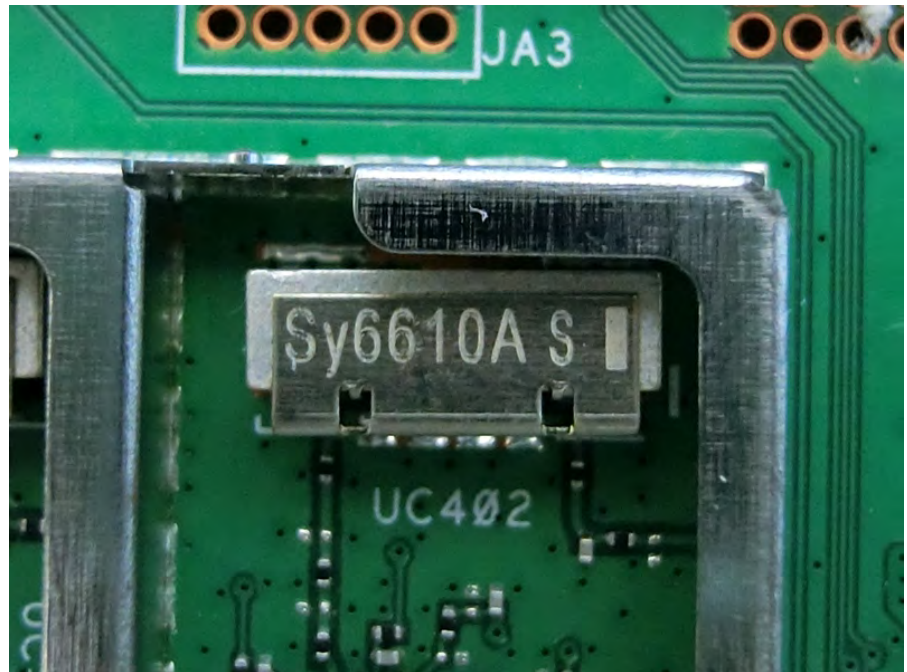


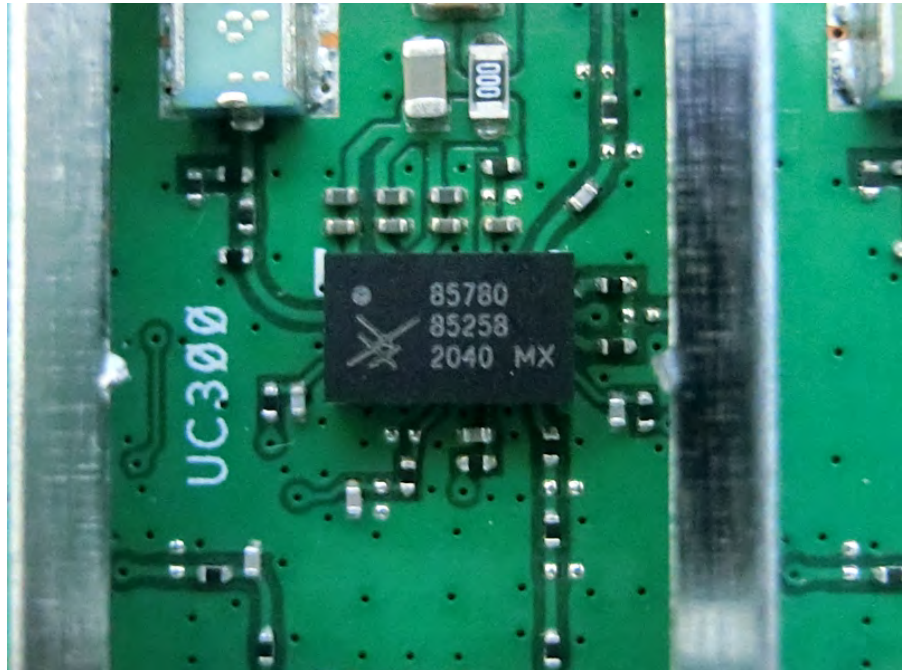


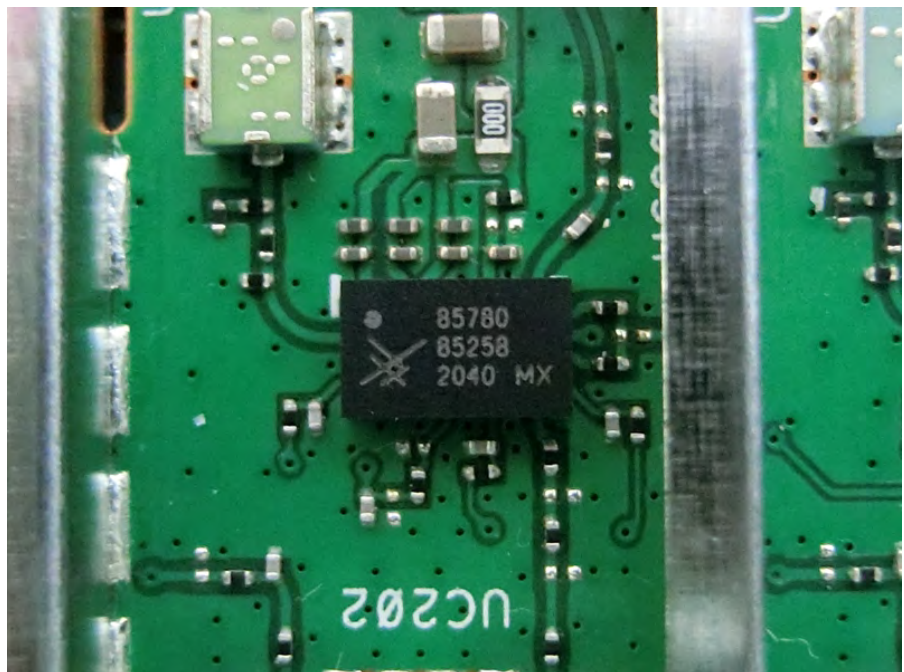
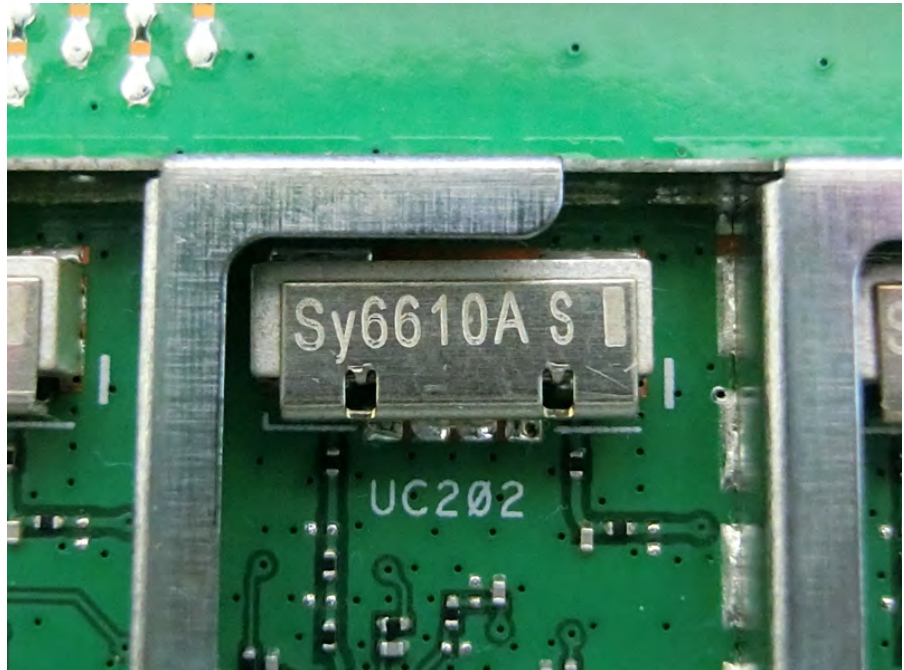


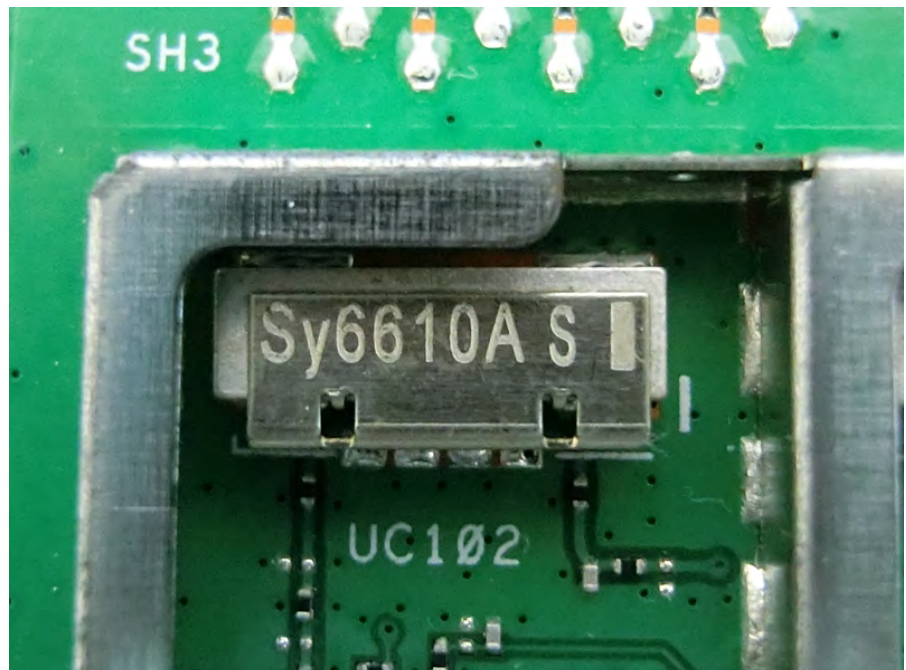
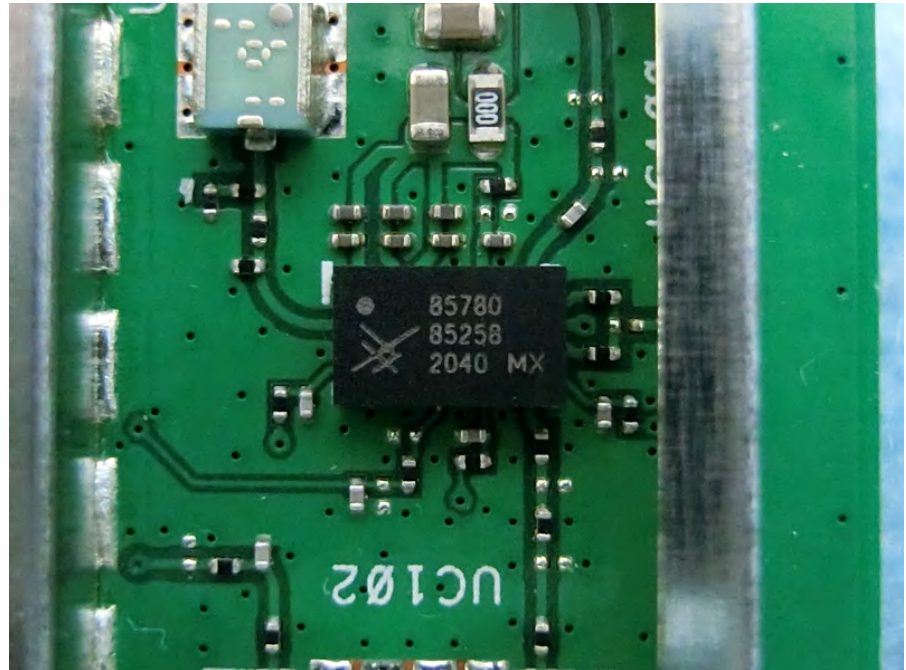


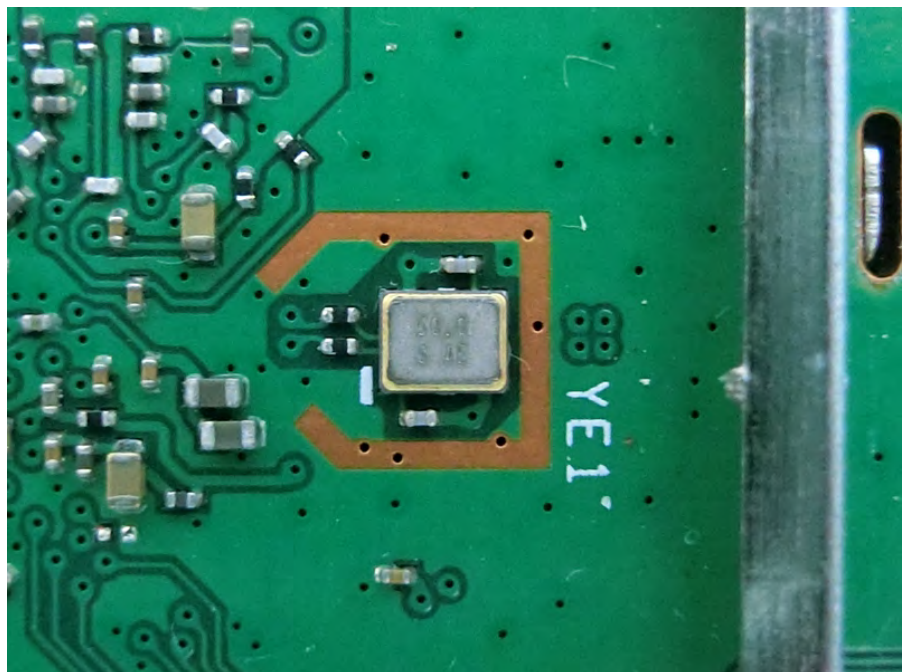
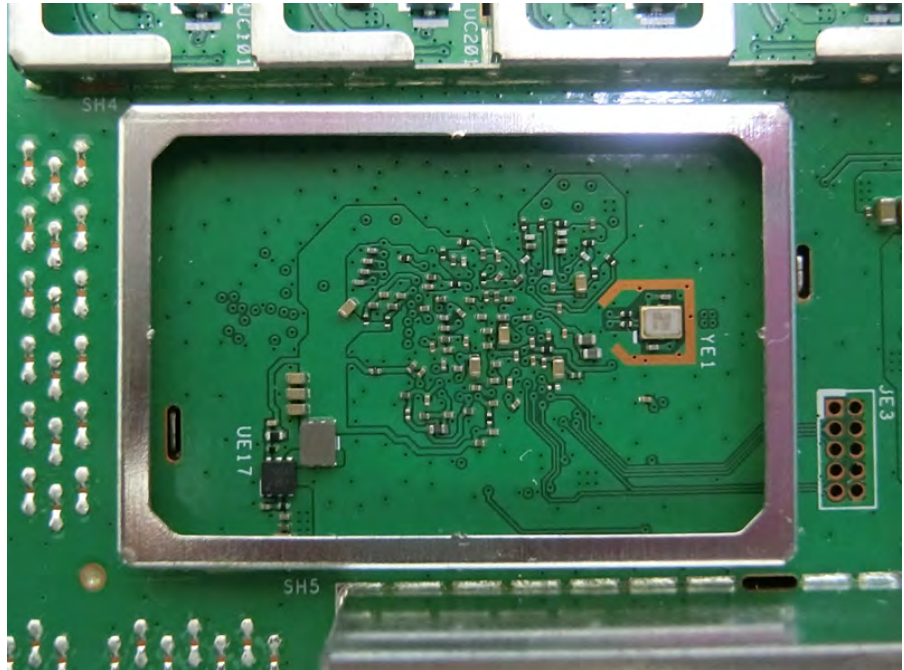


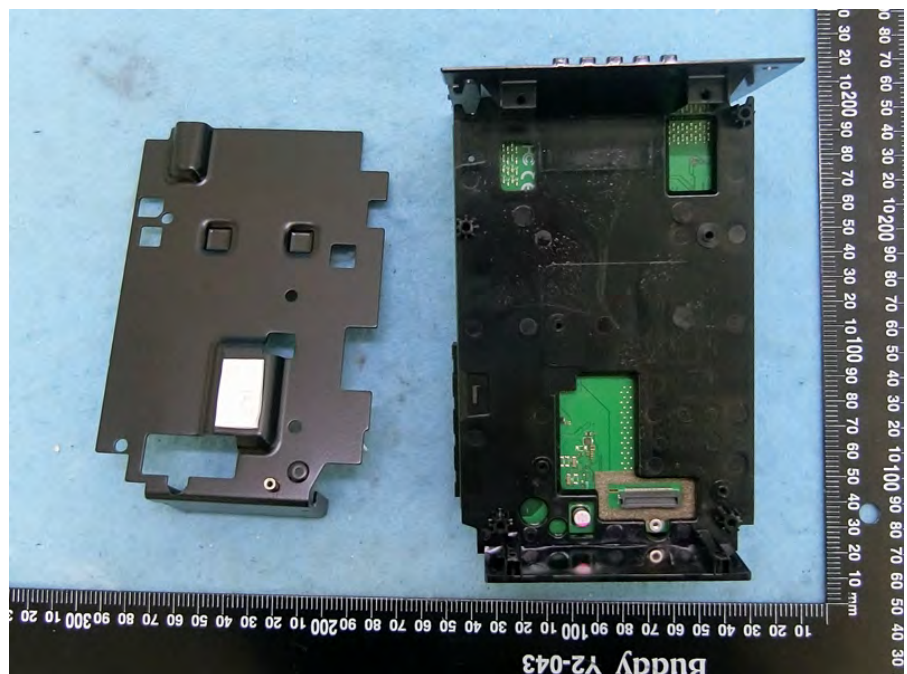


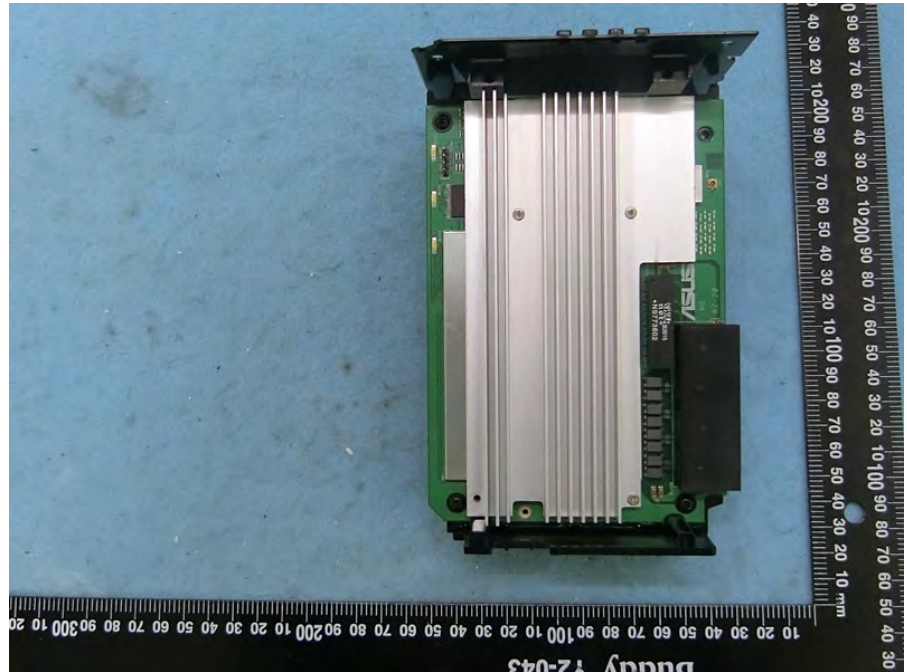


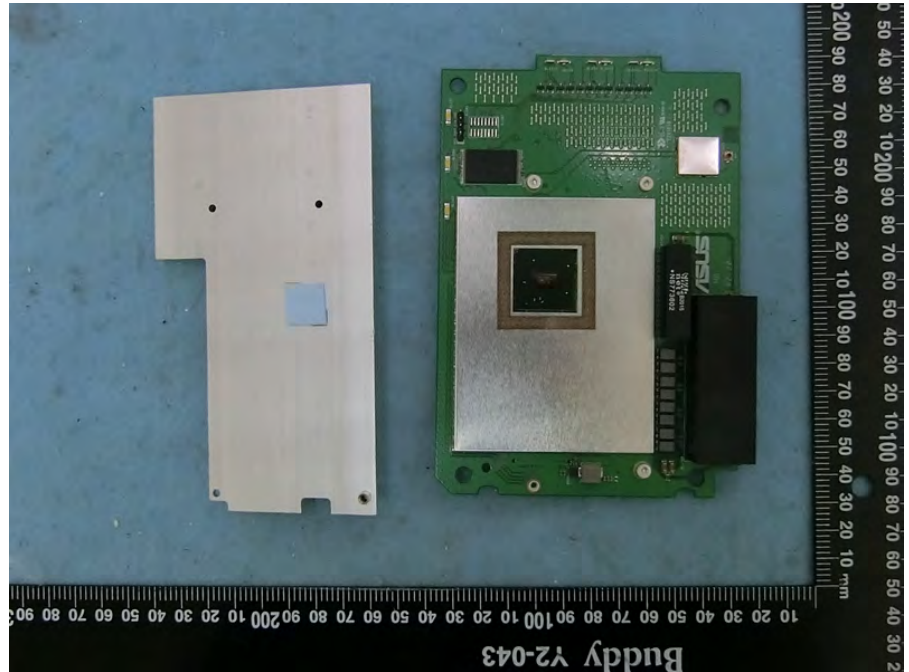


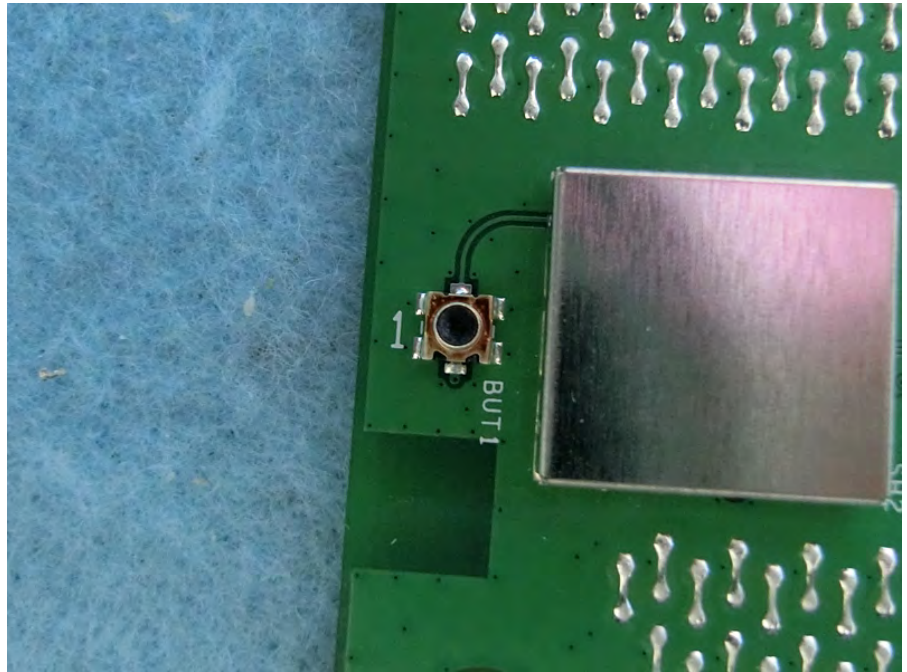


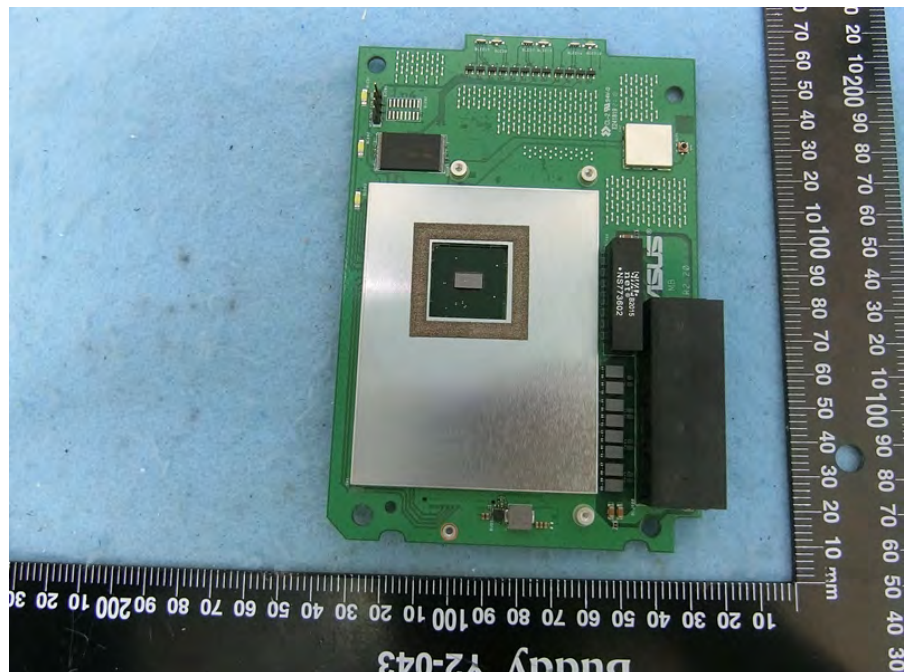
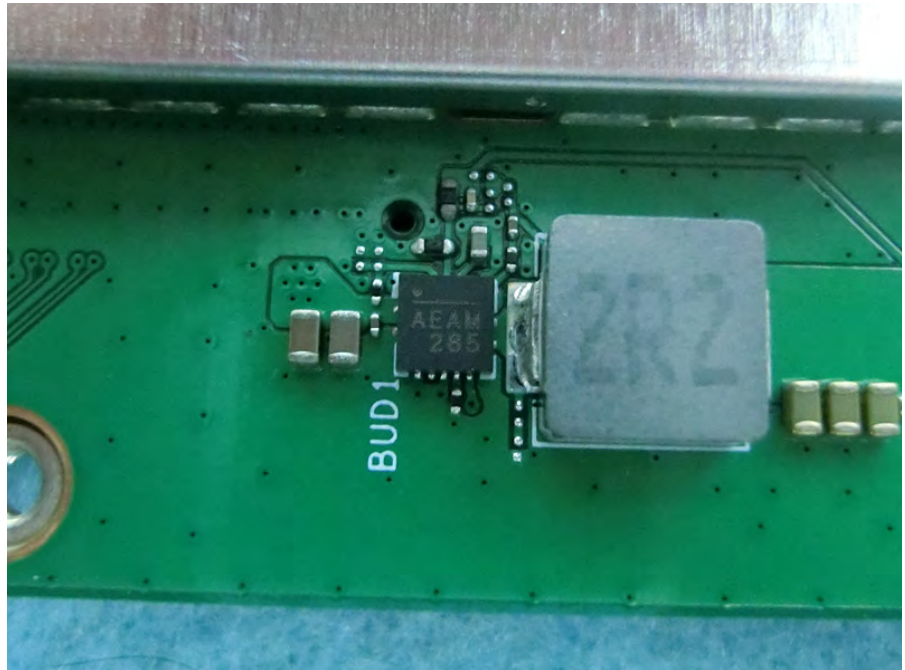


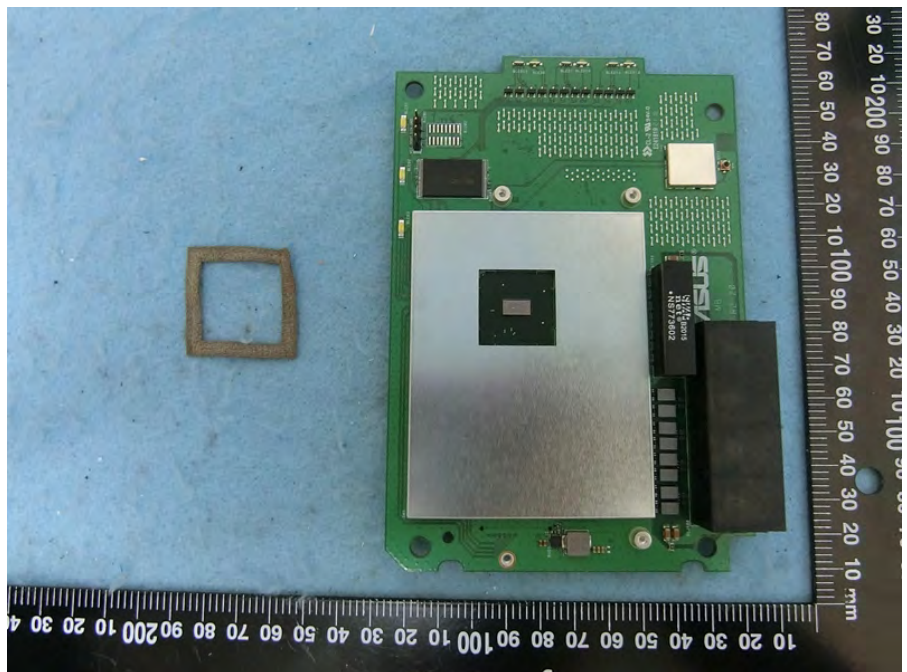
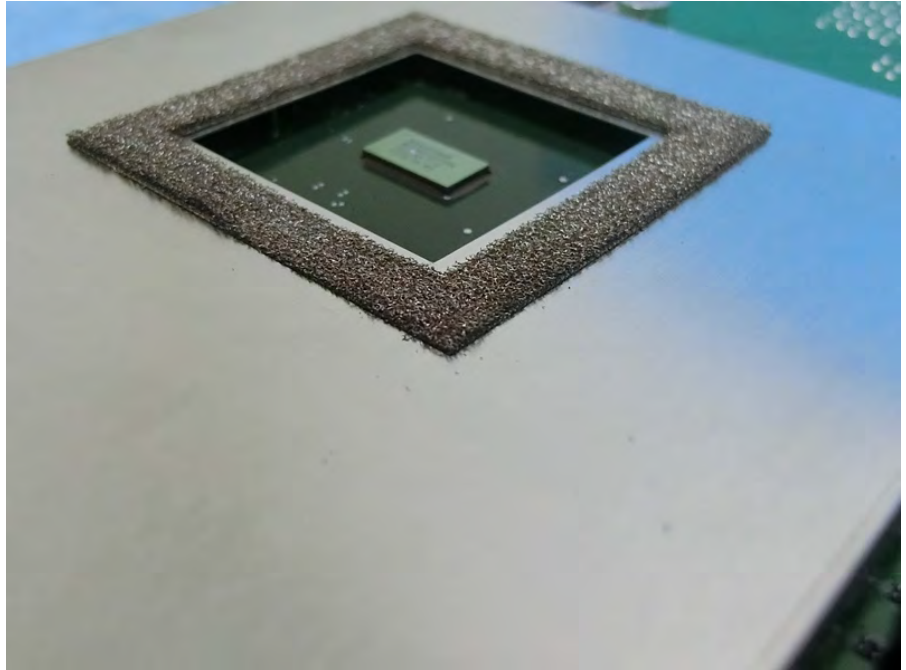


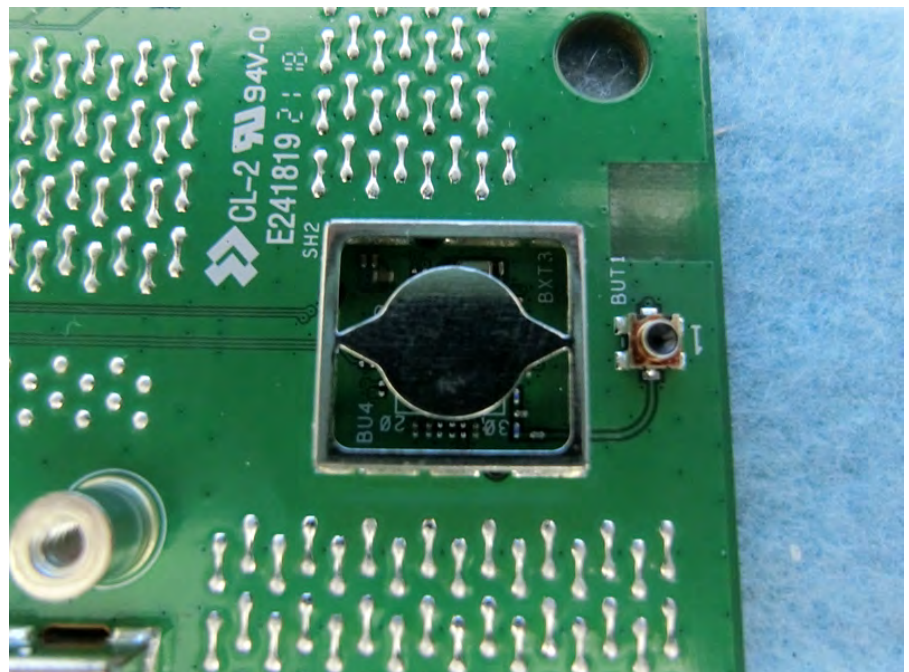
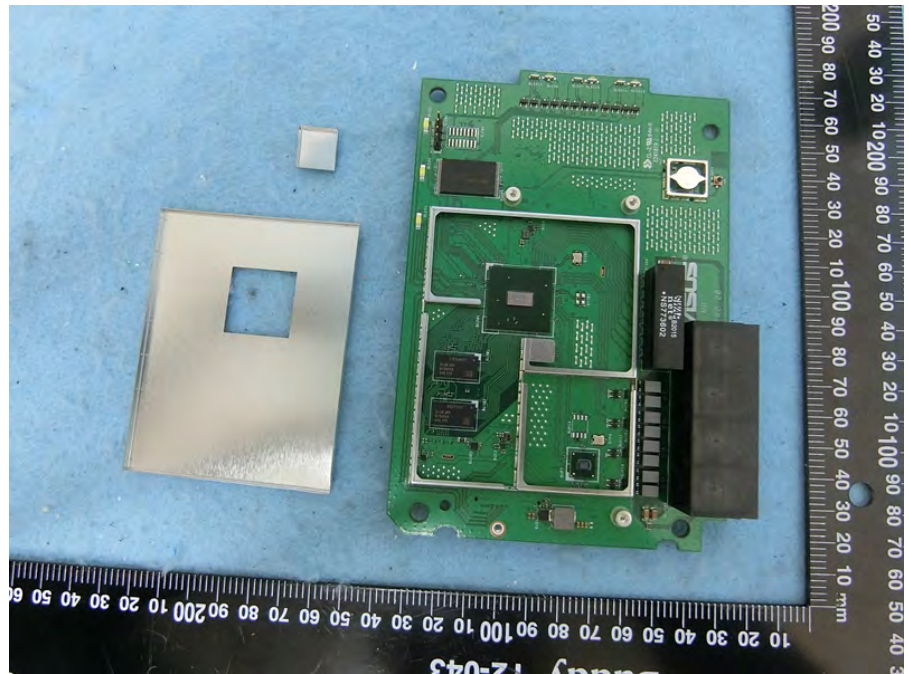


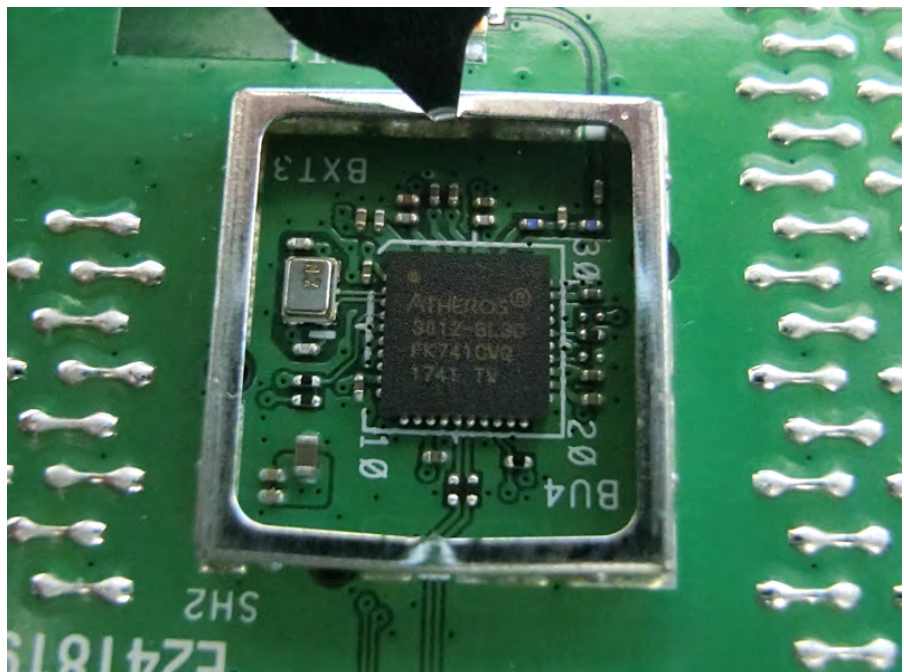
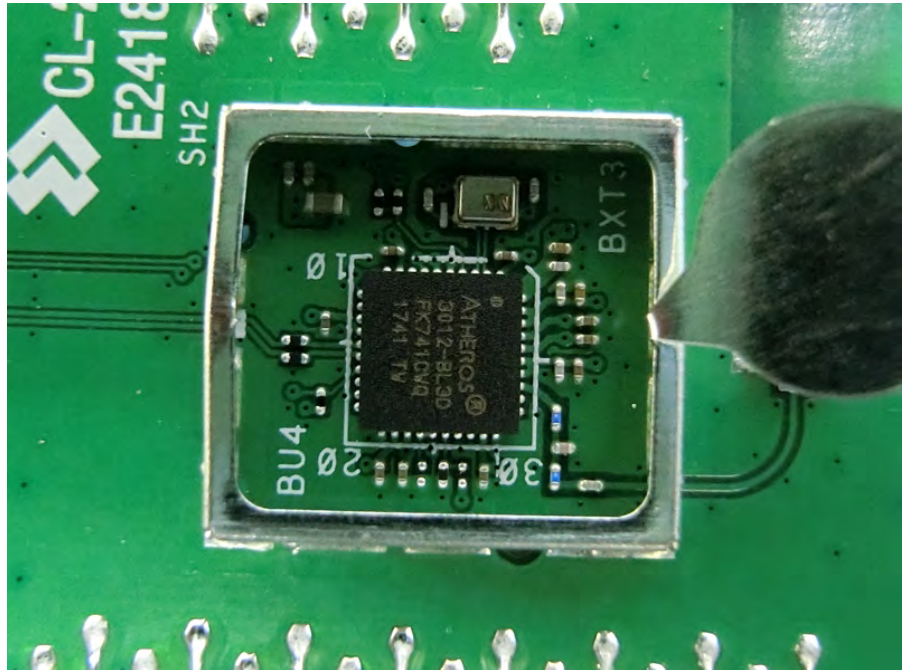














Transceiver (2.5G LAN)

Main source

