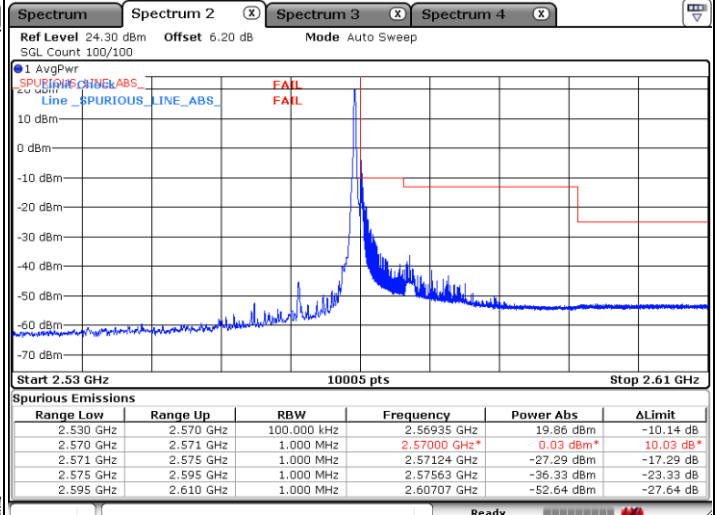
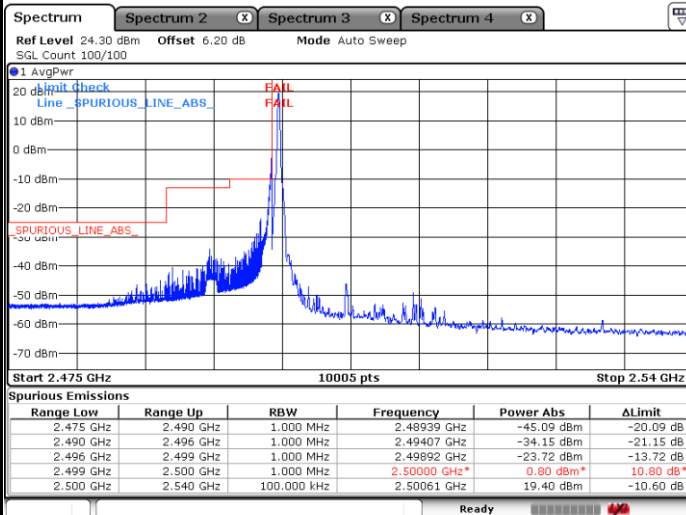




FR1 n7 / 40MHz / DFT-s-OFDM / PI/2 BPSK

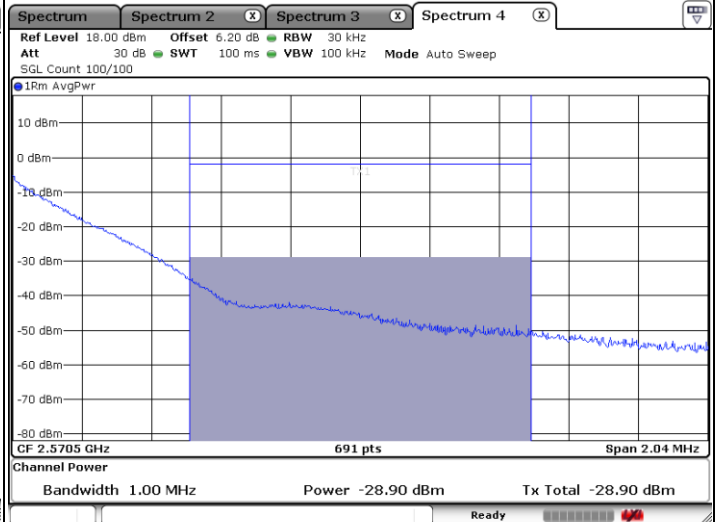
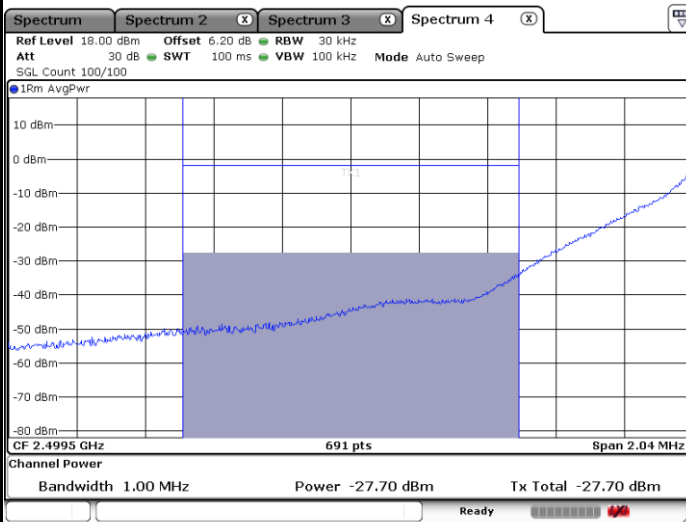
Lowest Band Edge / 1RB0

Highest Band Edge / 1RBMAX



Channel Power < -10dBm Pass

Channel Power < -10dBm Pass

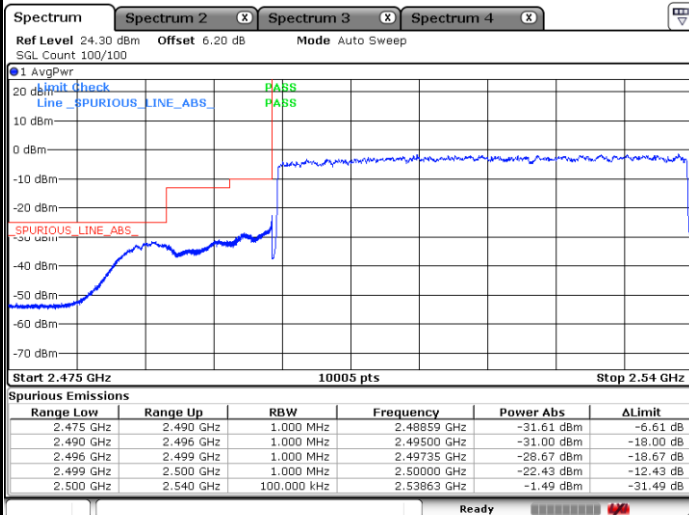




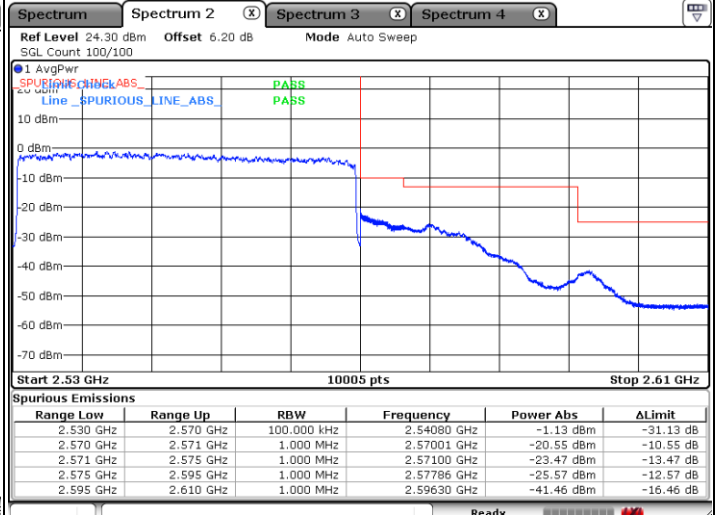
FR1 n7 /40MHz / DFT-s-OFDM / PI/2 BPSK

Lowest Band Edge / Full RB

Highest Band Edge / Full RB



Date: 28 DEC 2020 15:05:20



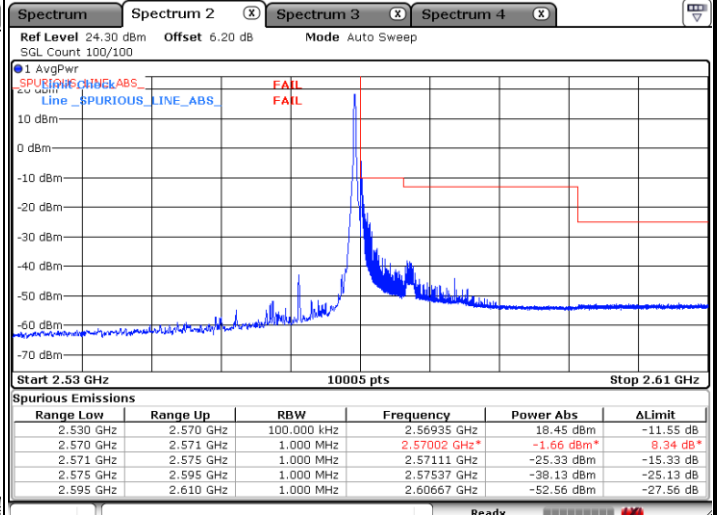
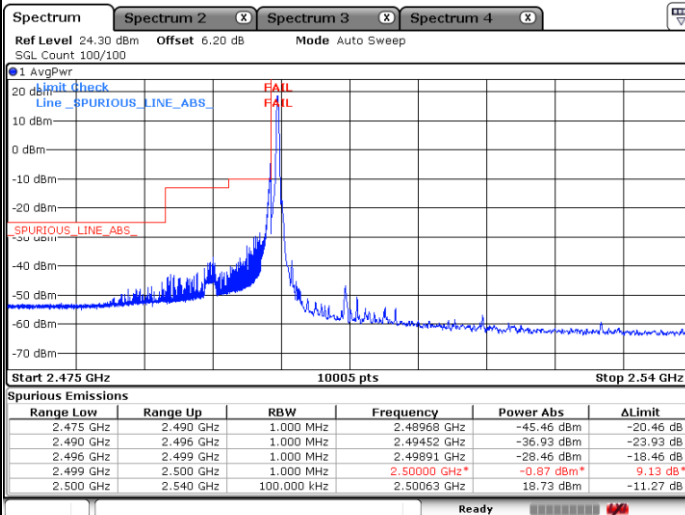
Date: 28 DEC 2020 15:32:04



FR1 n7 / 40MHz / DFT-s-OFDM / PI/2 QPSK

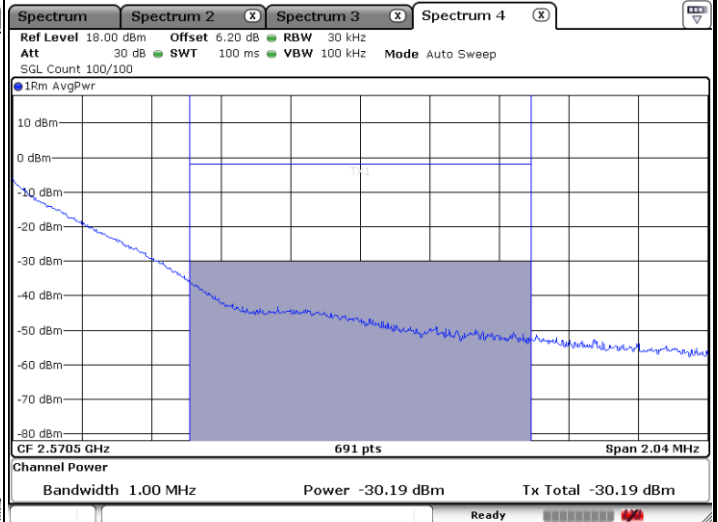
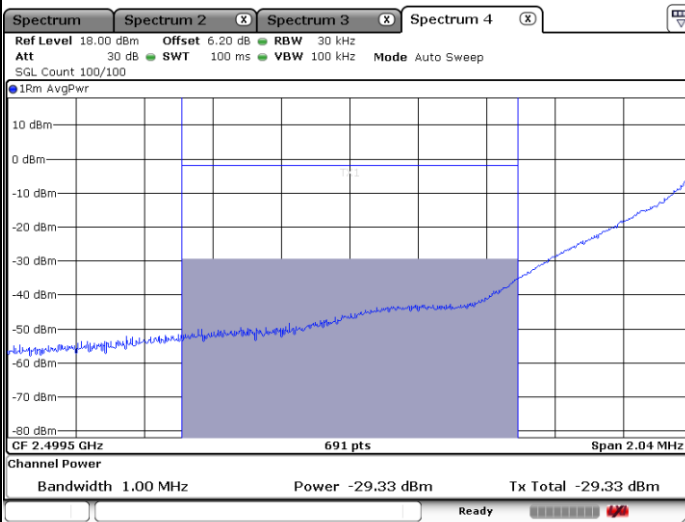
Lowest Band Edge / 1RB0

Highest Band Edge / 1RBMAX



Channel Power < -10dBm Pass

Channel Power < -10dBm Pass

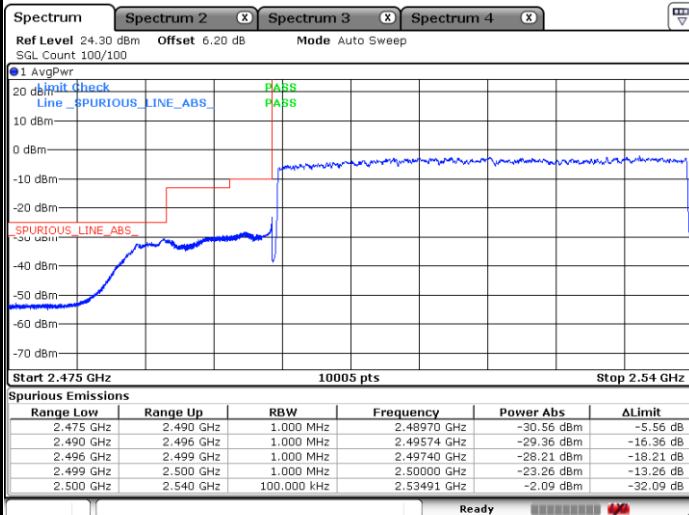




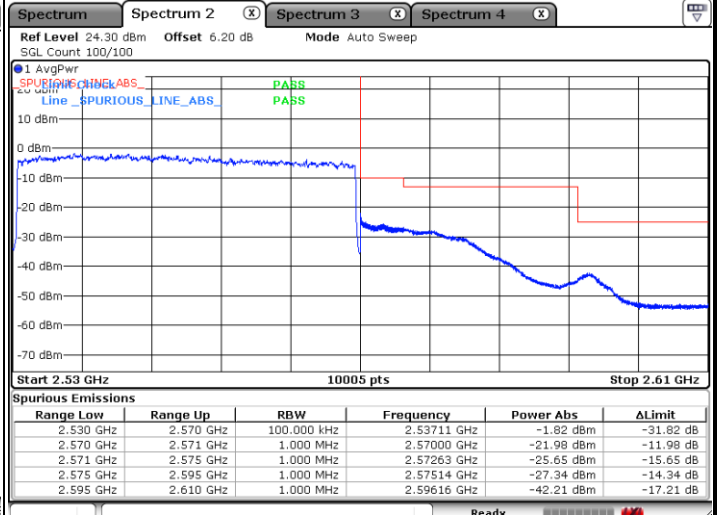
FR1 n7 / 40MHz / DFT-s-OFDM / PI/2 QPSK

Lowest Band Edge / Full RB

Highest Band Edge / Full RB



Date: 28 DEC 2020 15:06:05



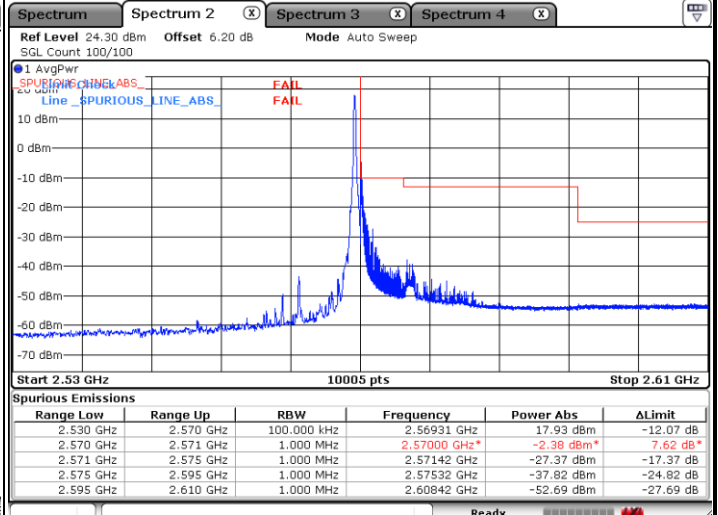
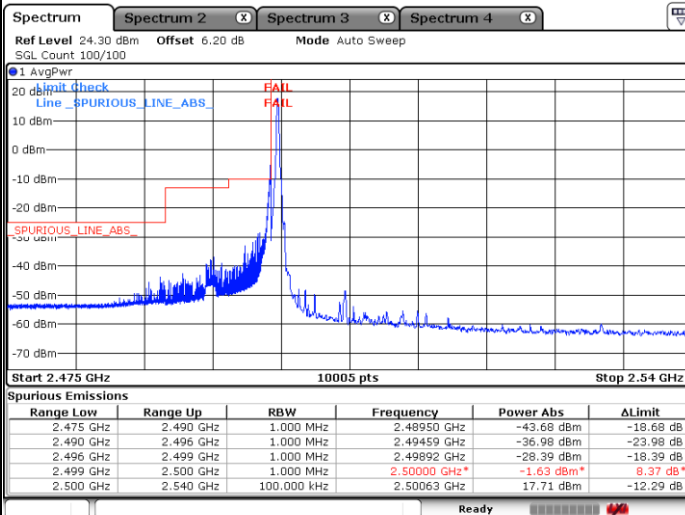
Date: 28 DEC 2020 15:20:17



FR1 n7 / 40MHz / DFT-s-OFDM / PI/2 16QAM

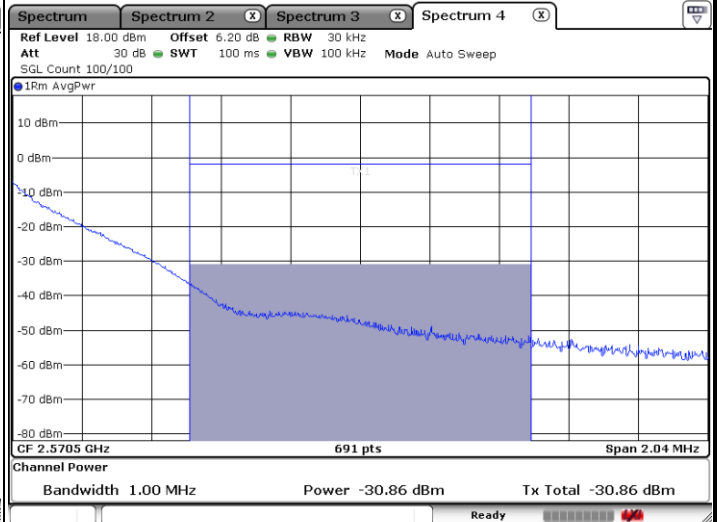
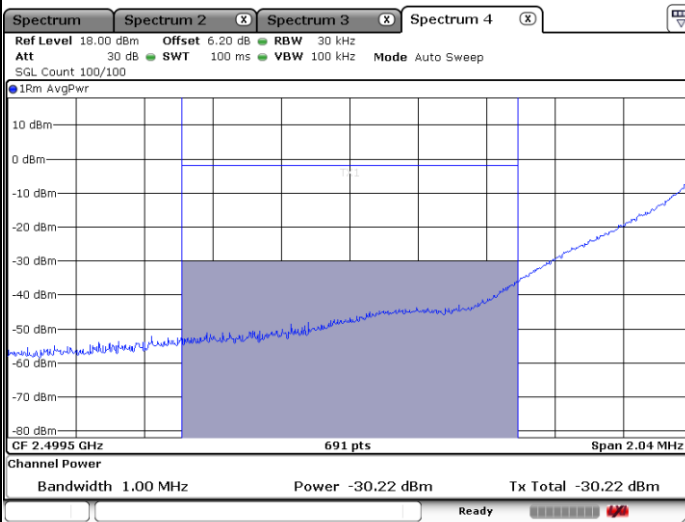
Lowest Band Edge / 1RB0

Highest Band Edge / 1RBMAX



Channel Power < -10dBm Pass

Channel Power < -10dBm Pass

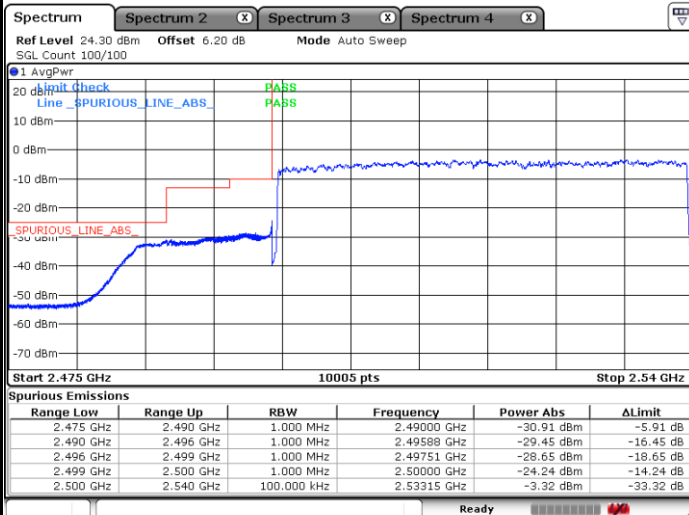




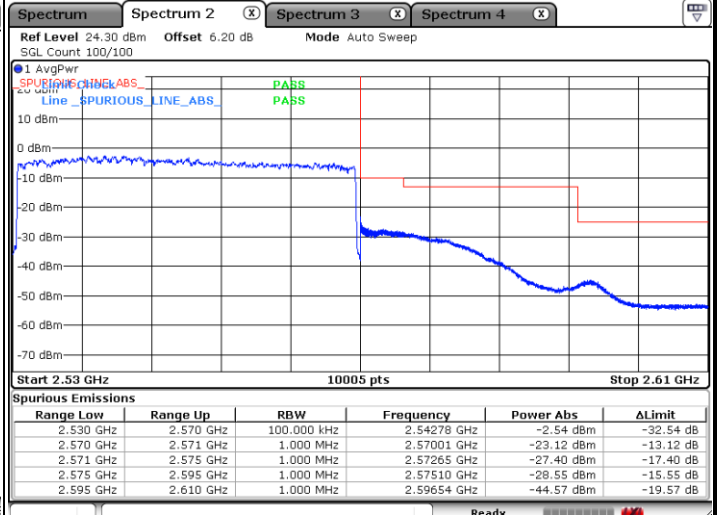
FR1 n7 /40MHz / DFT-s-OFDM / PI/2 16QAM

Lowest Band Edge / Full RB

Highest Band Edge / Full RB



Date: 28 DEC 2020 15:06:35



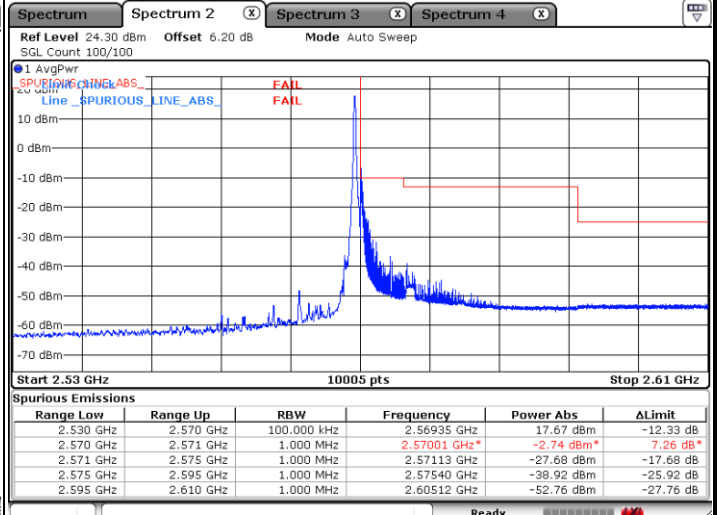
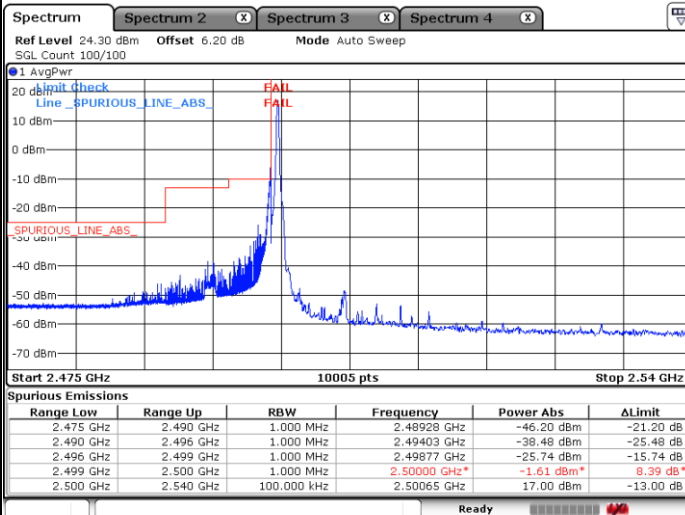
Date: 28 DEC 2020 15:20:45



FR1 n7 / 40MHz / DFT-s-OFDM / PI/2 64QAM

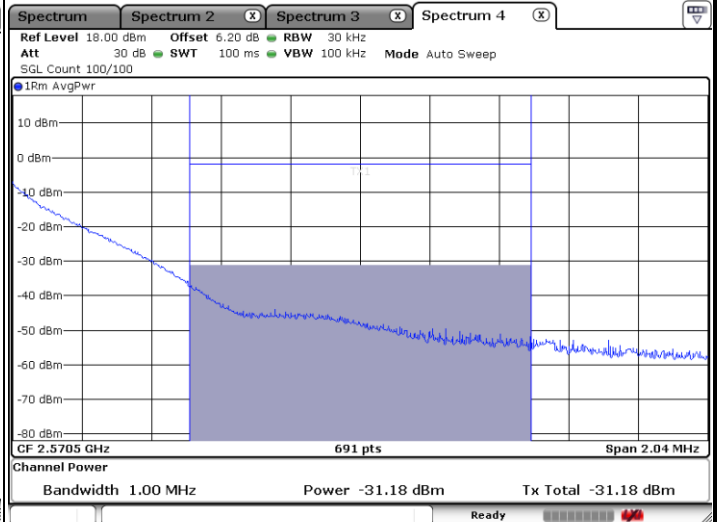
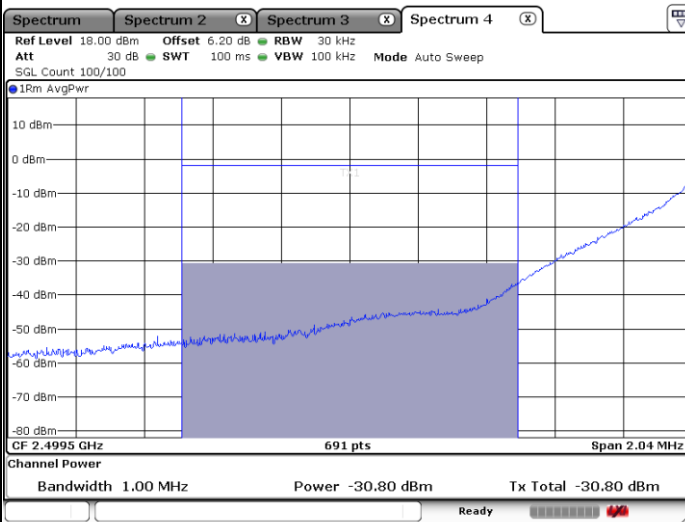
Lowest Band Edge / 1RB0

Highest Band Edge / 1RBMAX



Channel Power < -10dBm Pass

Channel Power < -10dBm Pass

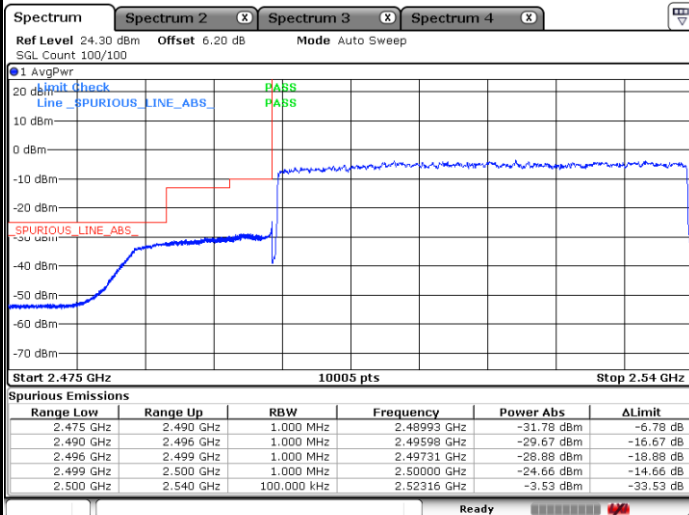




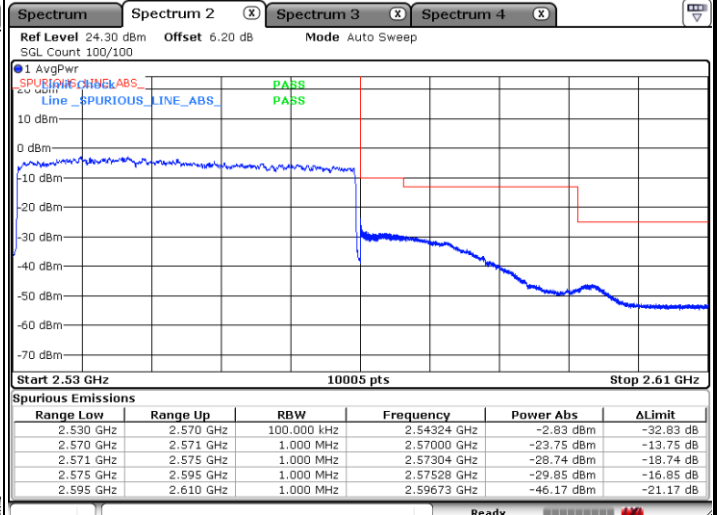
FR1 n7 / 40MHz / DFT-s-OFDM / PI/2 64QAM

Lowest Band Edge / Full RB

Highest Band Edge / Full RB



Date: 28 DEC 2020 15:07:07

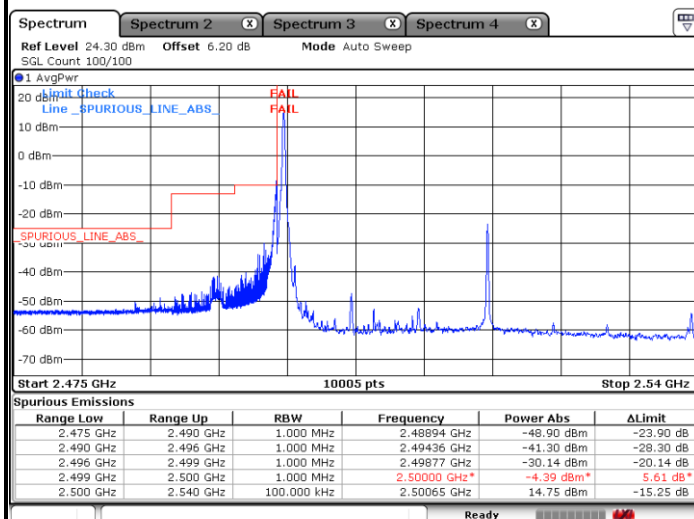


Date: 28 DEC 2020 15:21:08

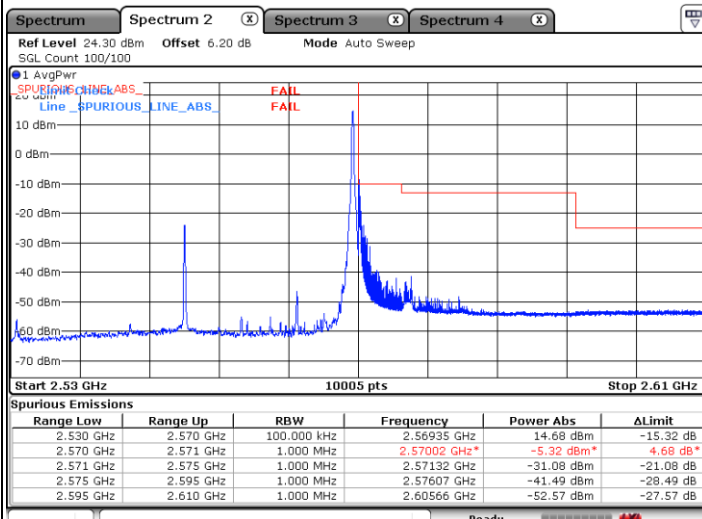


FR1 n7 / 40MHz / DFT-s-OFDM / PI/2 256QAM

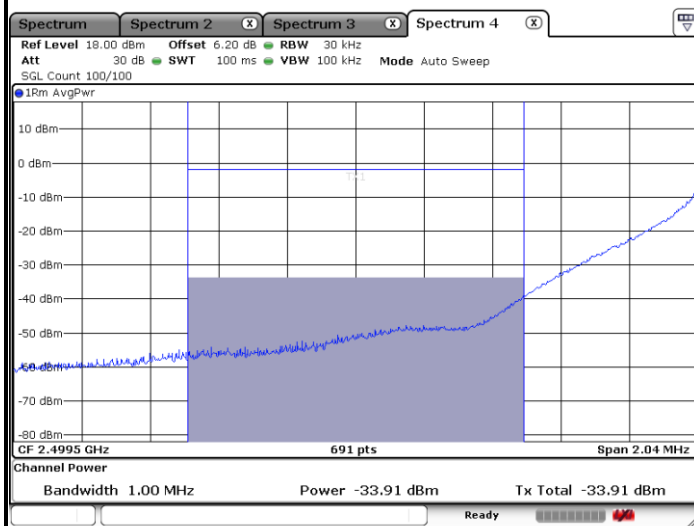
Lowest Band Edge / 1RB0



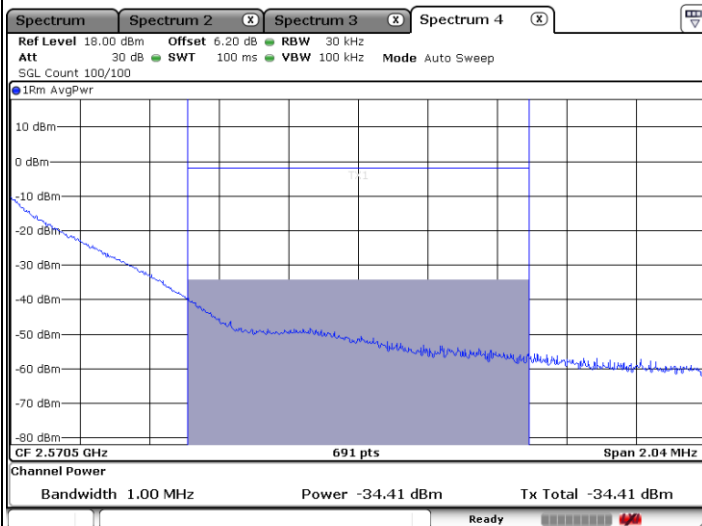
Highest Band Edge / 1RBMAX



Channel Power < -10dBm Pass



Channel Power < -10dBm Pass

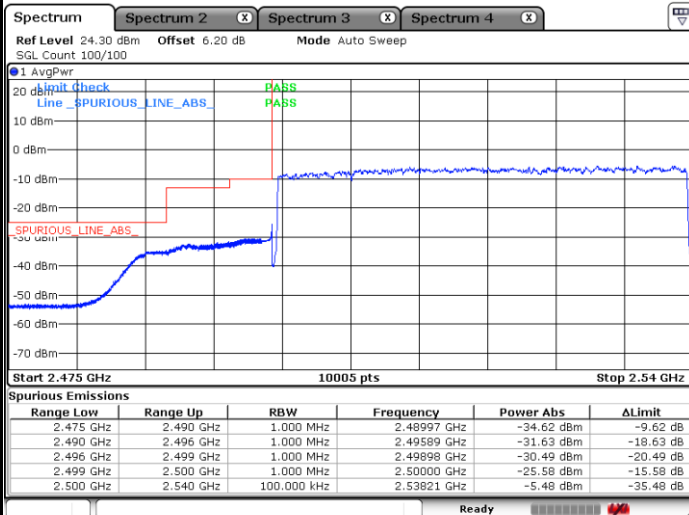




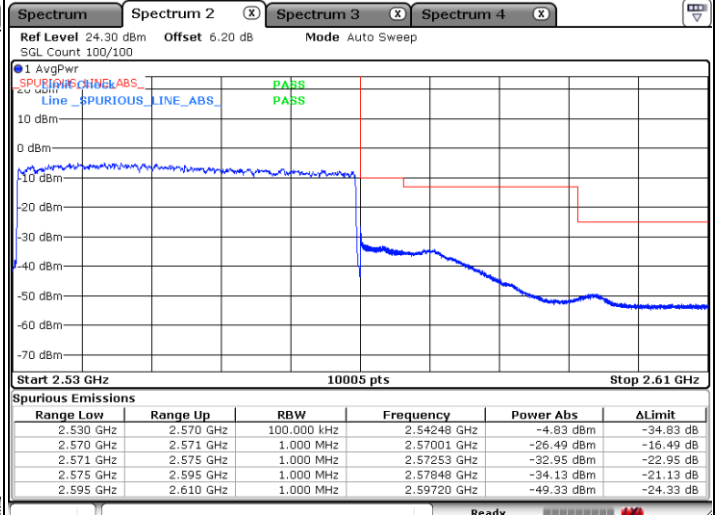
FR1 n7 / 40MHz / DFT-s-OFDM / PI/2 256QAM

Lowest Band Edge / Full RB

Highest Band Edge / Full RB



Date: 28 DEC 2020 15:16:41



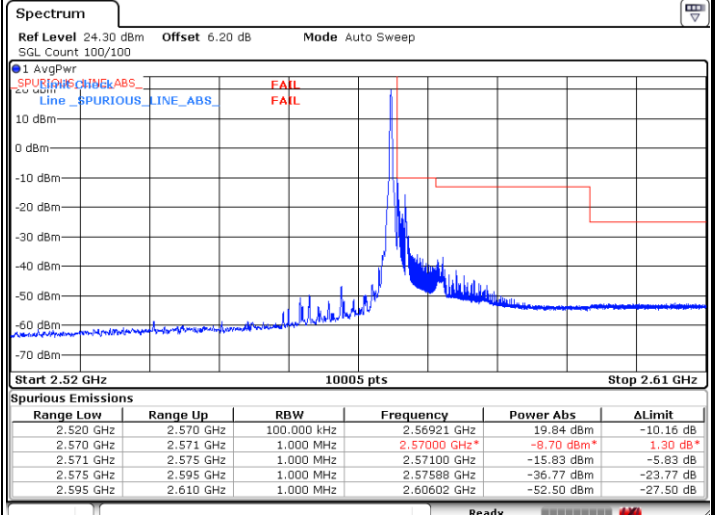
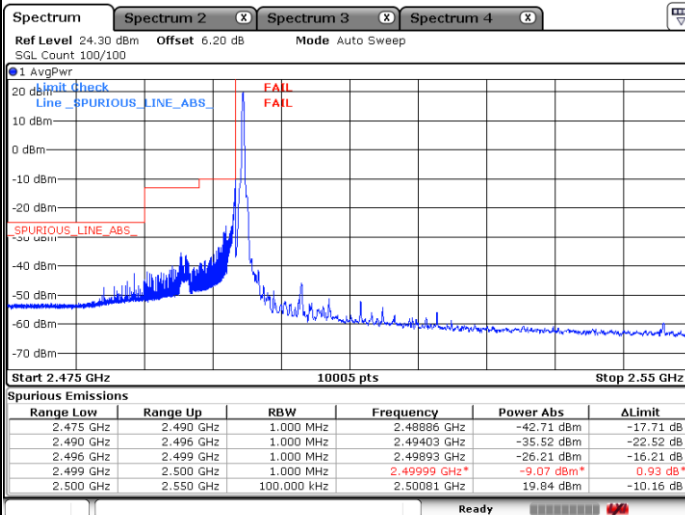
Date: 28 DEC 2020 15:21:32



FR1 n7 / 50MHz / DFT-s-OFDM / PI/2 BPSK

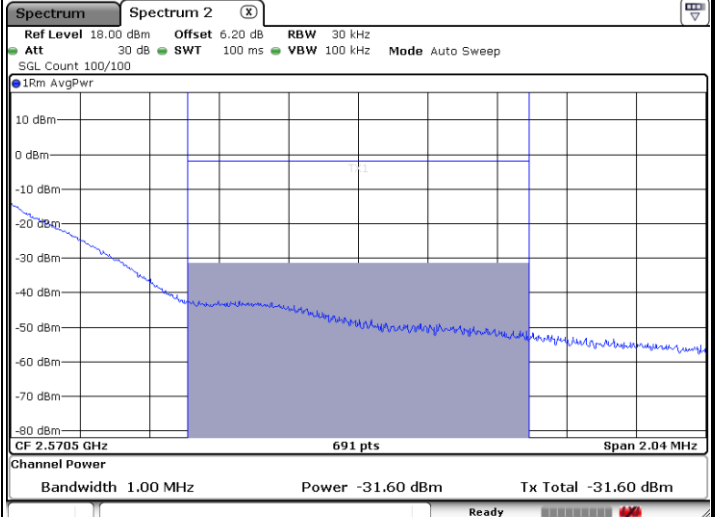
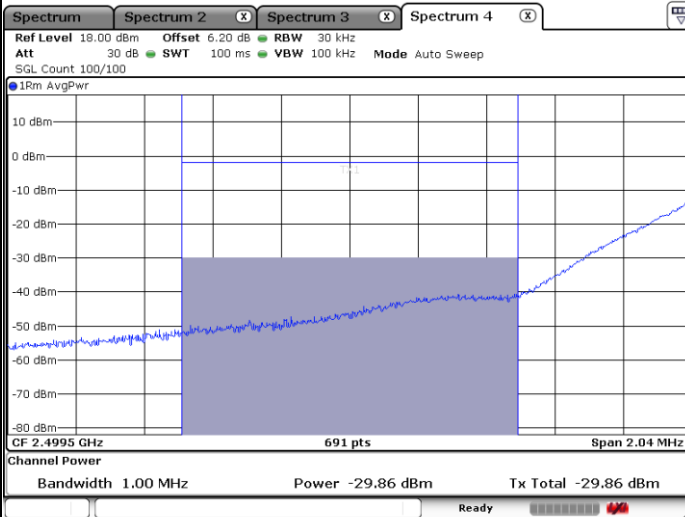
Lowest Band Edge / 1RB0

Highest Band Edge / 1RBMAX



Channel Power < -10dBm Pass

Channel Power < -10dBm Pass

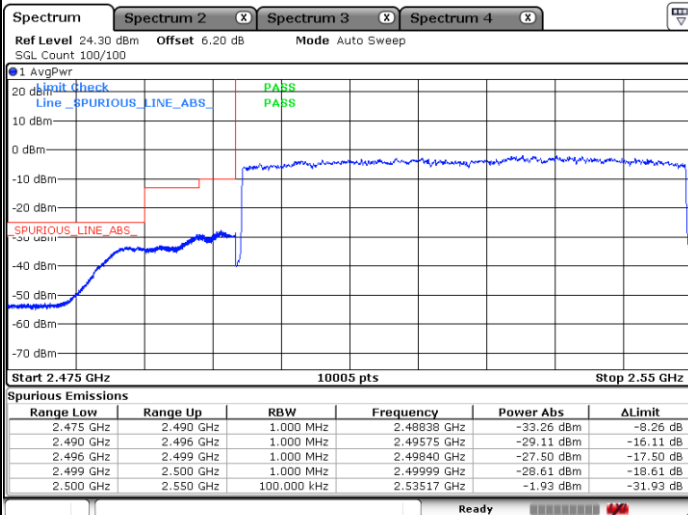




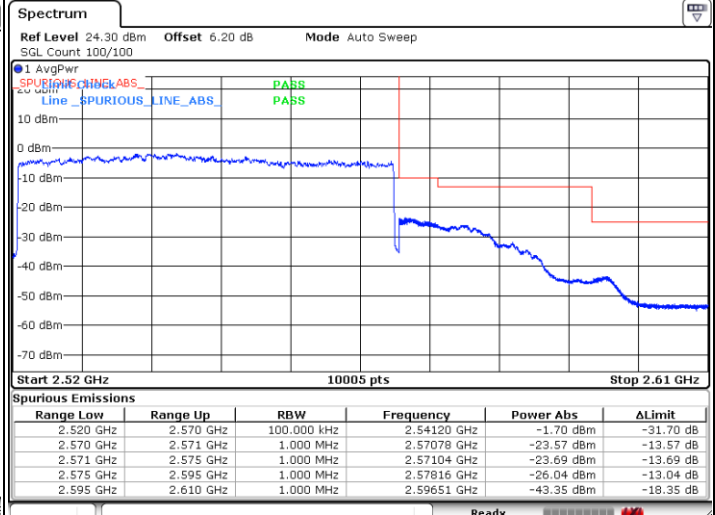
FR1 n7 /50MHz / DFT-s-OFDM / PI/2 BPSK

Lowest Band Edge / Full RB

Highest Band Edge / Full RB



Date: 28 DEC 2020 15:36:14

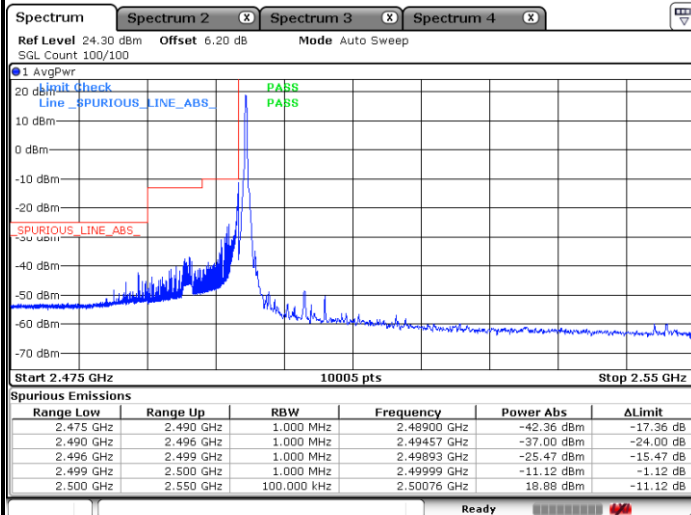


Date: 28 DEC 2020 16:51:59

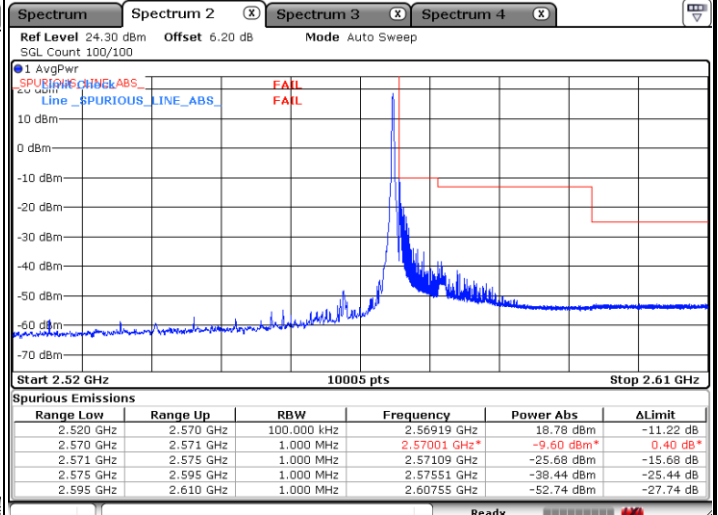


FR1 n7 / 50MHz / DFT-s-OFDM / PI/2 QPSK

Lowest Band Edge / 1RB0

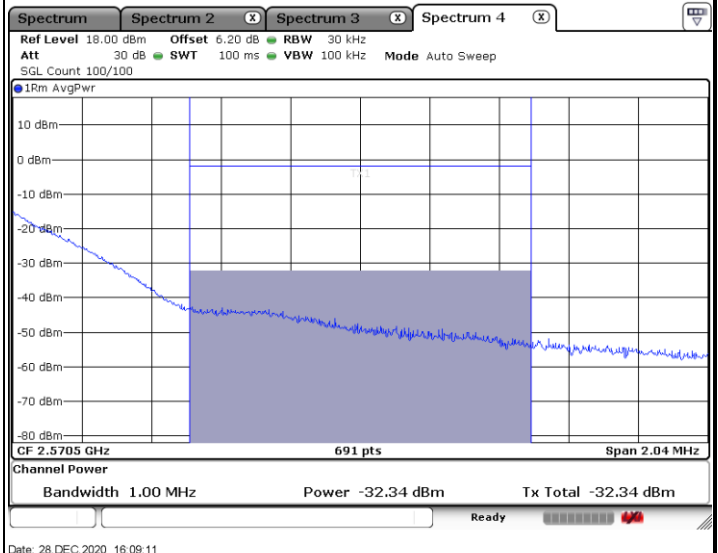


Highest Band Edge / 1RBMAX



Channel Power < -10dBm Pass

Channel Power < -10dBm Pass

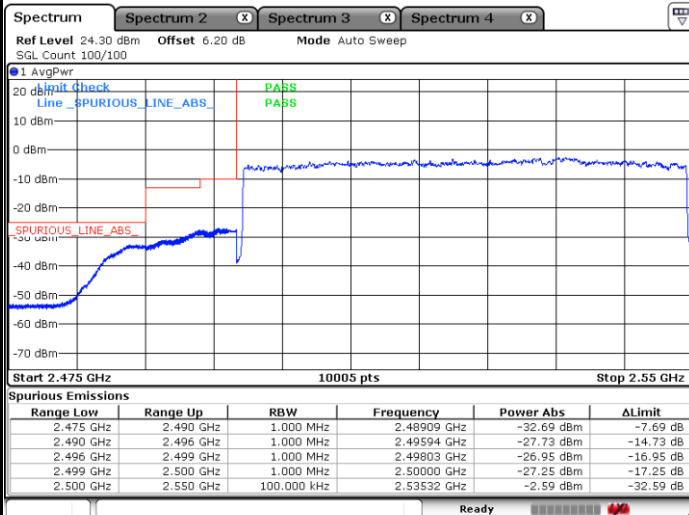




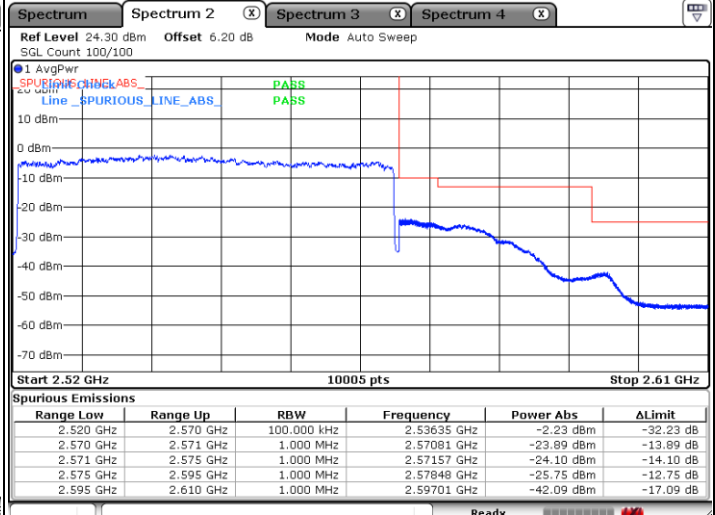
FR1 n7 / 50MHz / DFT-s-OFDM / PI/2 QPSK

Lowest Band Edge / Full RB

Highest Band Edge / Full RB



Date: 28 DEC 2020 15:37:00

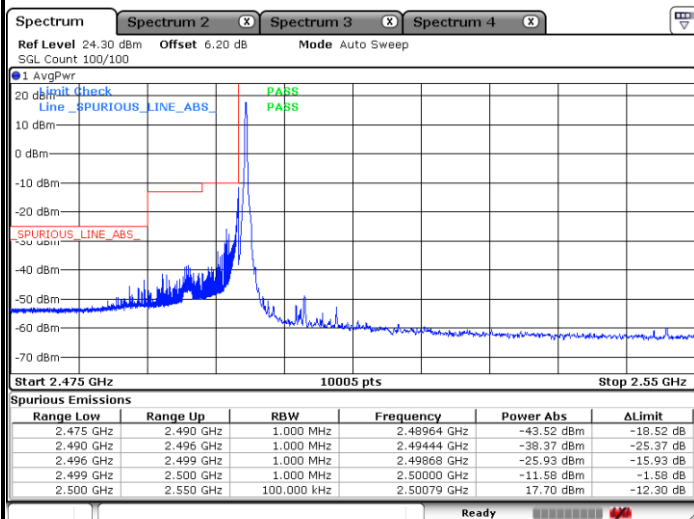


Date: 28 DEC 2020 15:59:40

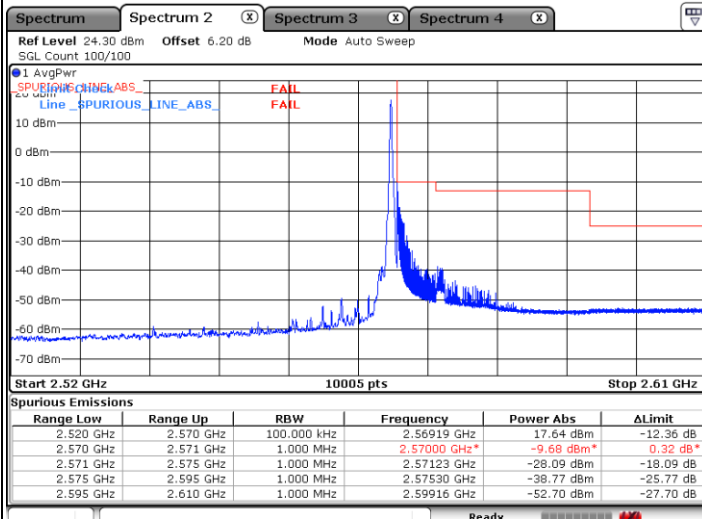


FR1 n7 / 50MHz / DFT-s-OFDM / PI/2 16QAM

Lowest Band Edge / 1RB0

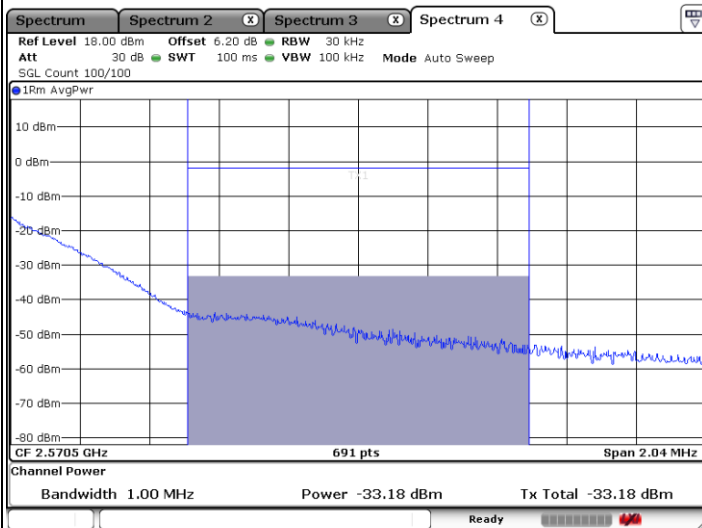


Highest Band Edge / 1RBMAX



Channel Power < -13dBm Pass

Channel Power < -10dBm Pass

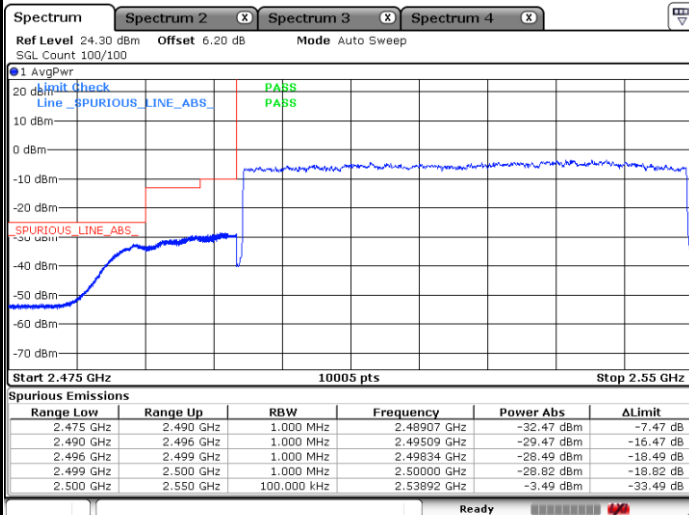




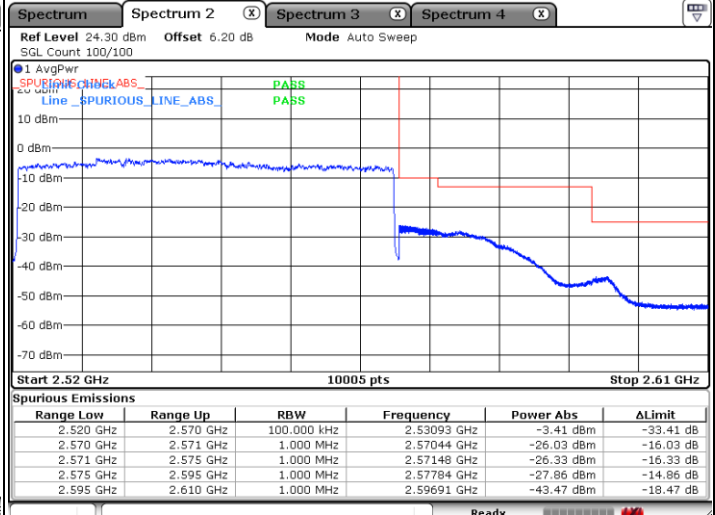
FR1 n7 /50MHz / DFT-s-OFDM / PI/2 16QAM

Lowest Band Edge / Full RB

Highest Band Edge / Full RB



Date: 28 DEC 2020 15:38:00



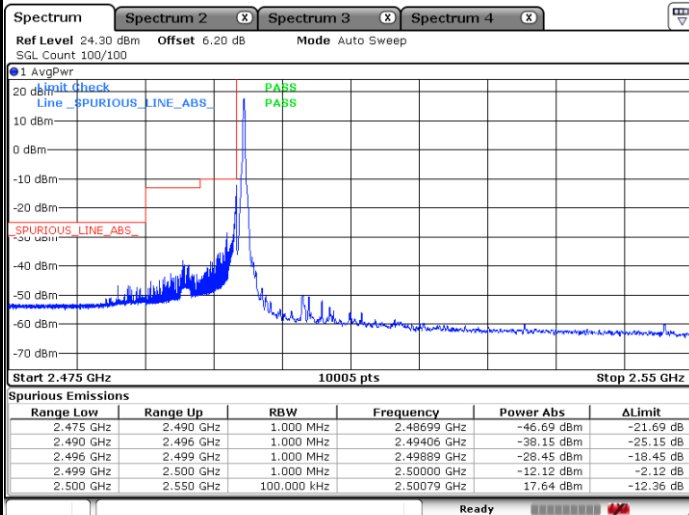
Date: 28 DEC 2020 16:00:07



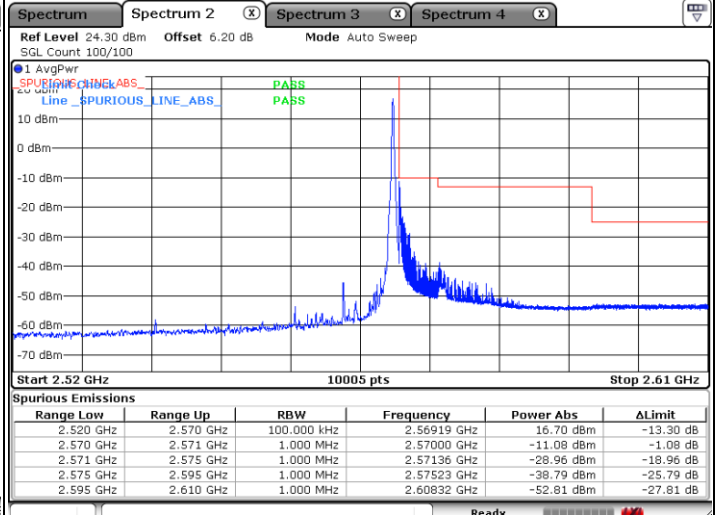
FR1 n7 / 50MHz / DFT-s-OFDM / PI/2 64QAM

Lowest Band Edge / 1RB0

Highest Band Edge / 1RBMAX



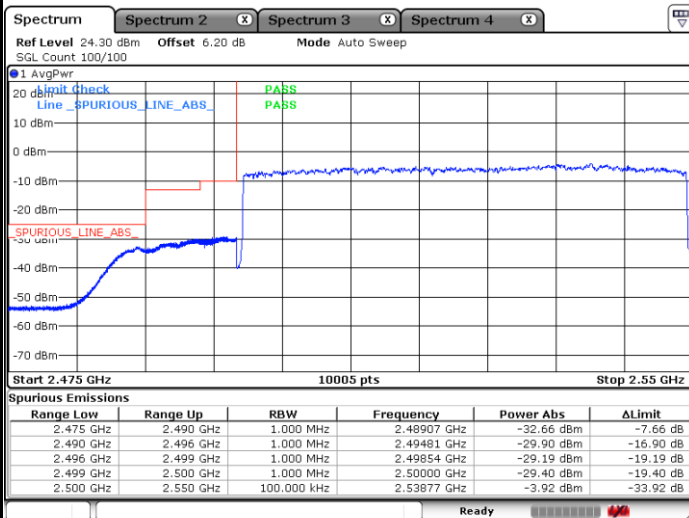
Date: 28 DEC 2020 15:39:16



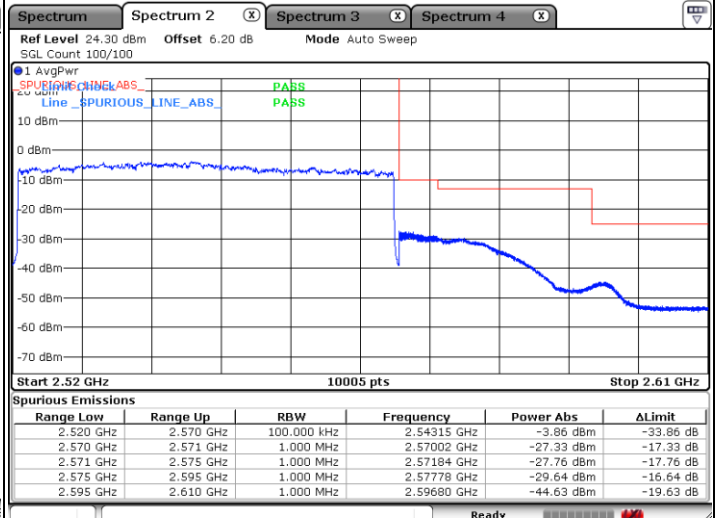
Date: 28 DEC 2020 16:02:55

Lowest Band Edge / Full RB

Highest Band Edge / Full RB



Date: 28 DEC 2020 15:38:42



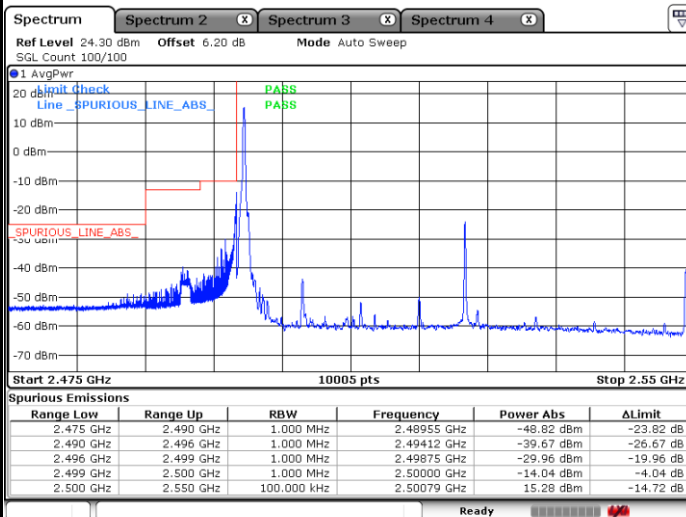
Date: 28 DEC 2020 16:00:29



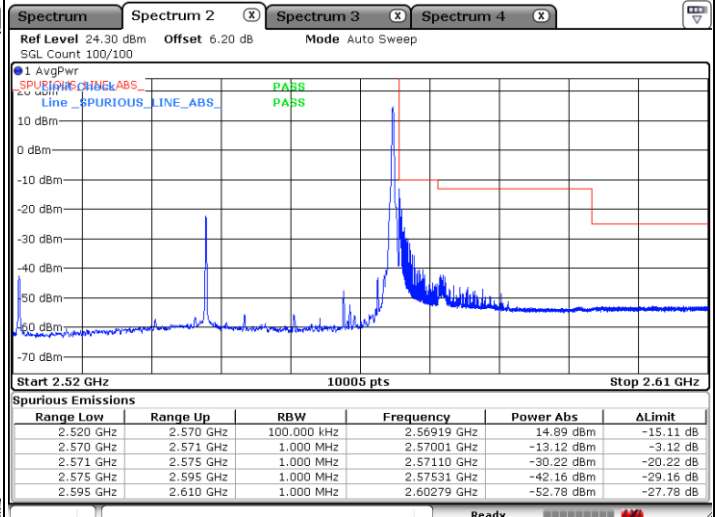
FR1 n7 / 50MHz / DFT-s-OFDM / PI/2 256QAM

Lowest Band Edge / 1RB0

Highest Band Edge / 1RBMAX



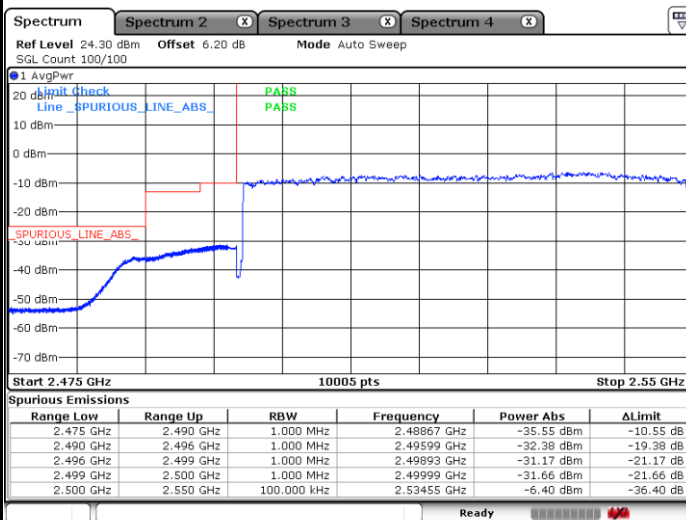
Date: 28 DEC 2020 15:53:42



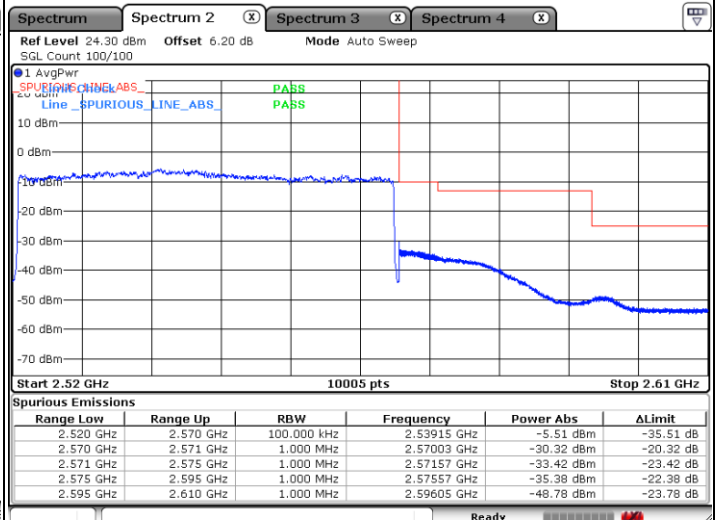
Date: 28 DEC 2020 16:01:59

Lowest Band Edge / Full RB

Highest Band Edge / Full RB



Date: 28 DEC 2020 15:53:05



Date: 28 DEC 2020 16:00:51

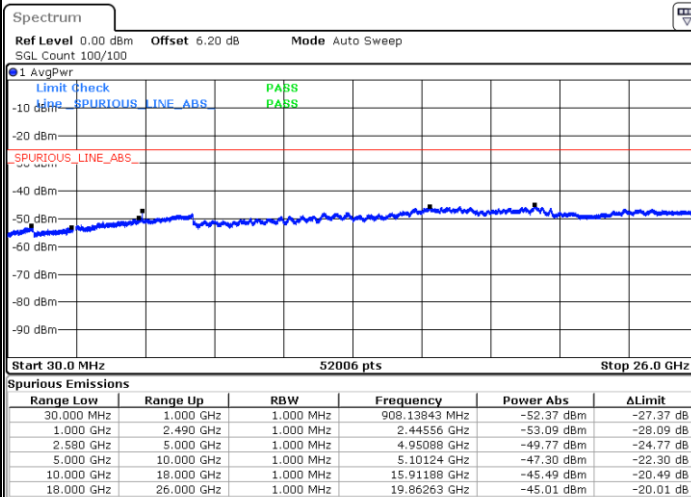


Conducted Spurious Emission

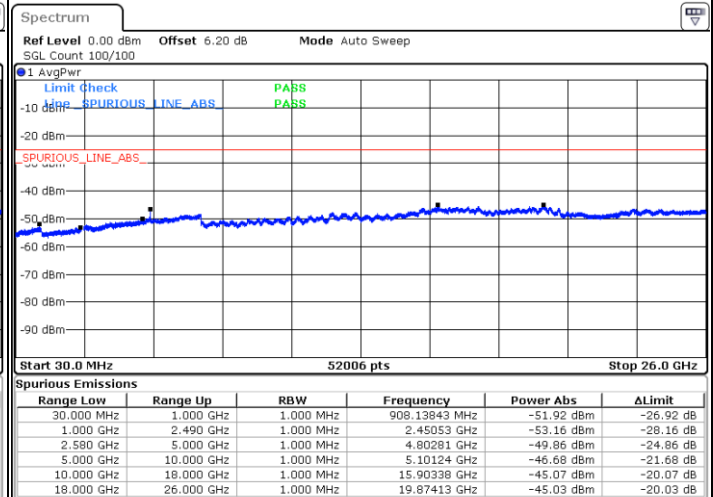
FR1 n7 / 5MHz / DFT-S OFDM / QPSK

Lowest Channel / 1RB1

Middle Channel / 1RB1

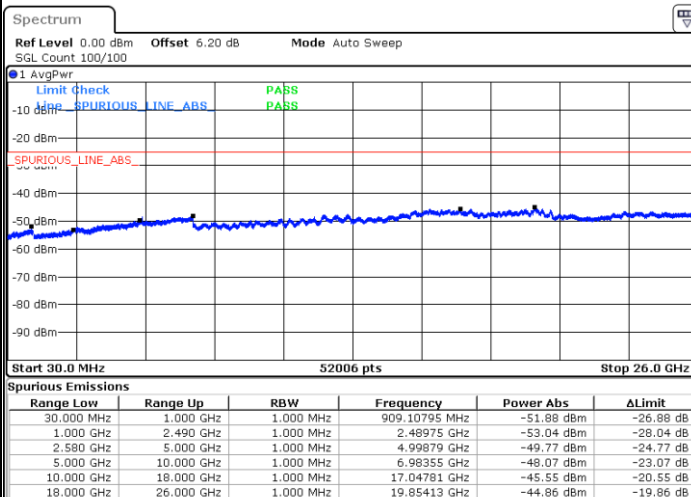


Date: 20 DEC 2020 11:43:27



Date: 20 DEC 2020 11:44:28

Highest Channel / 1RB1

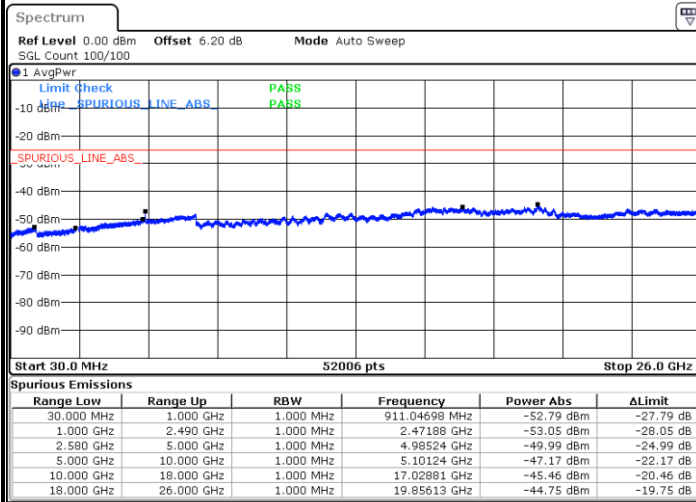


Date: 20 DEC 2020 11:45:19



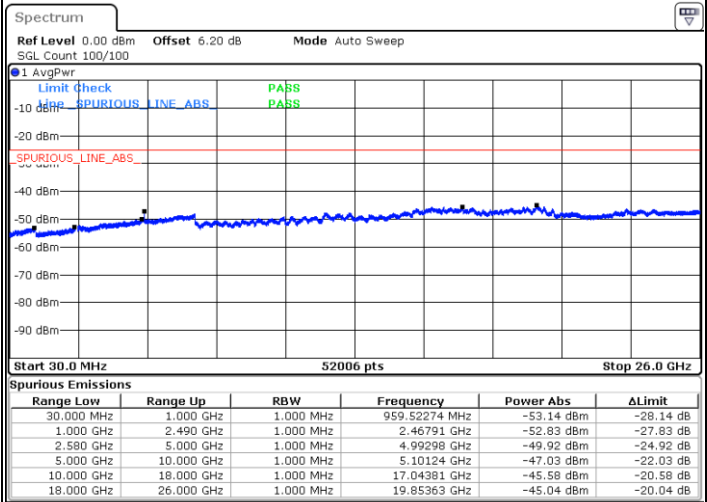
FR1 n7 / 10MHz / DFT-S OFDM / QPSK

Lowest Channel / 1RB1



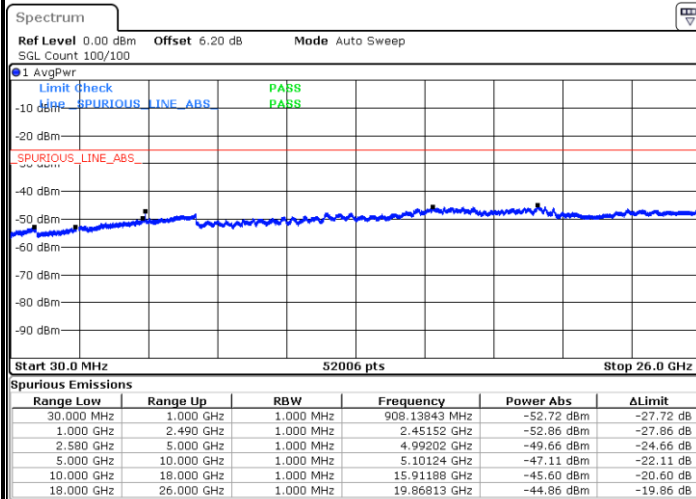
Date: 20 DEC 2020 11:46:11

Middle Channel / 1RB1



Date: 20 DEC 2020 11:47:05

Highest Channel / 1RB1

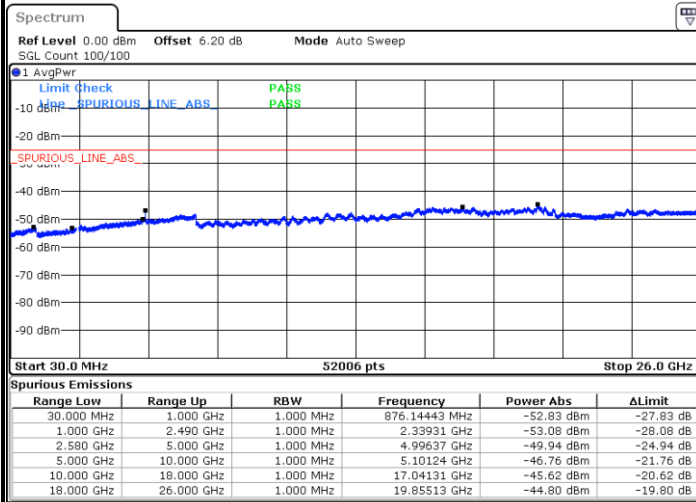


Date: 20 DEC 2020 11:47:55



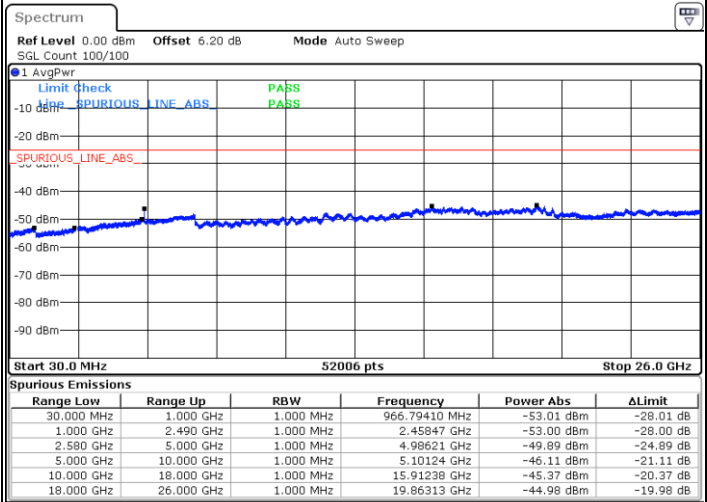
FR1 n7 / 15MHz / DFT-S OFDM / QPSK

Lowest Channel / 1RB1



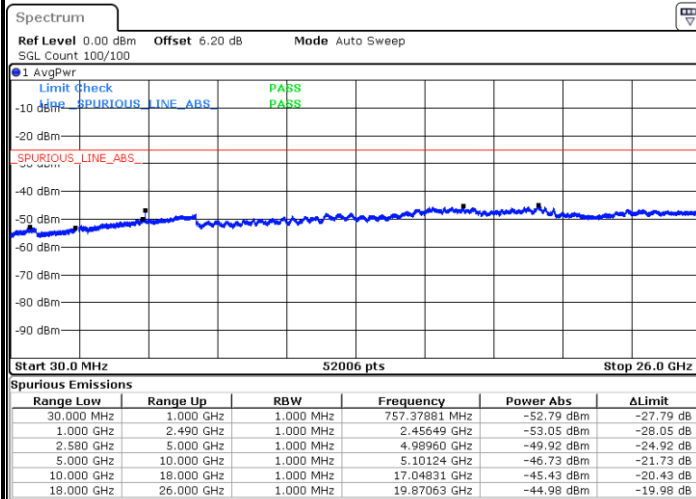
Date: 20 DEC 2020 11:48:52

Middle Channel / 1RB1



Date: 20 DEC 2020 11:50:15

Highest Channel / 1RB1

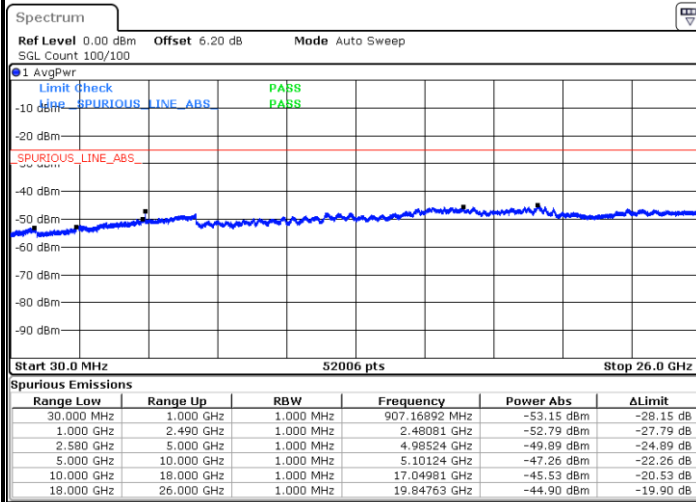


Date: 20 DEC 2020 11:51:31



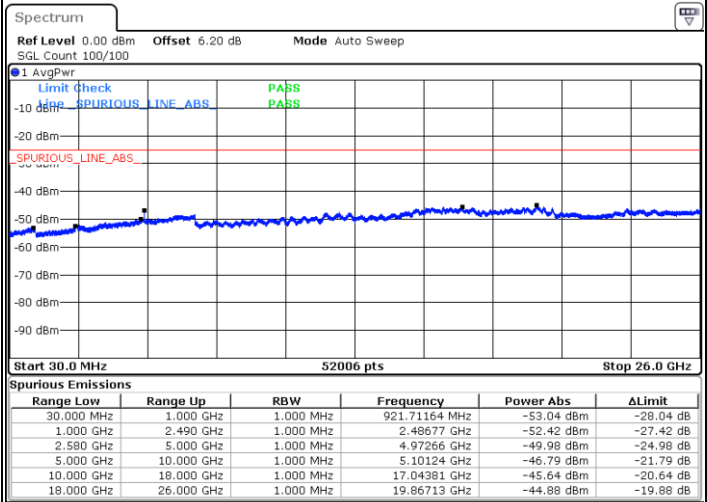
FR1 n7 / 20MHz / DFT-S OFDM / QPSK

Lowest Channel / 1RB1



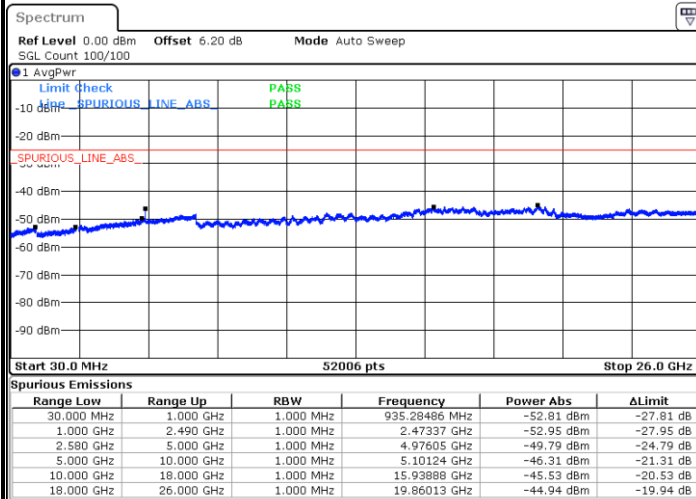
Date: 20 DEC 2020 11:52:25

Middle Channel / 1RB1



Date: 20 DEC 2020 11:53:18

Highest Channel / 1RB1

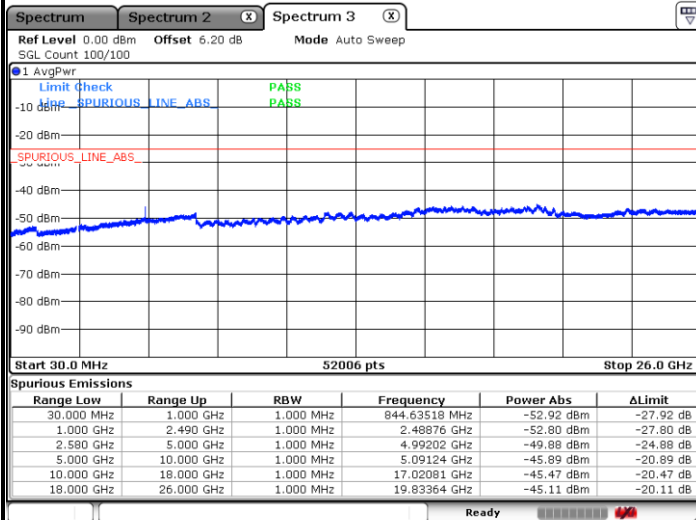


Date: 20 DEC 2020 11:54:25



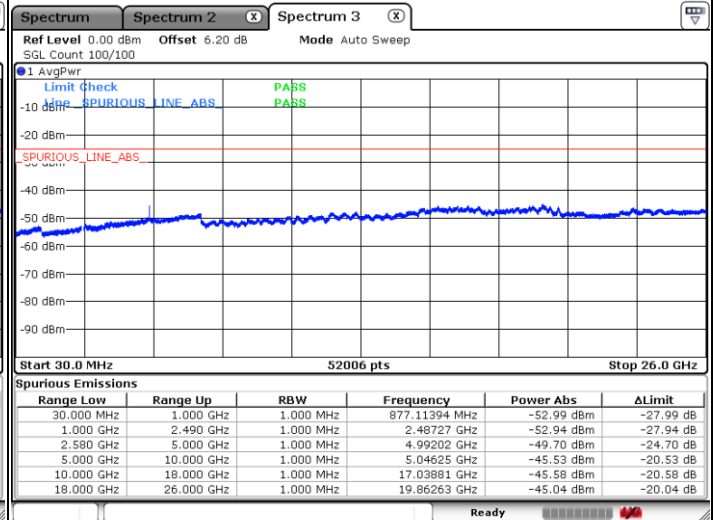
FR1 n7 / 25MHz / DFT-S OFDM / QPSK

Lowest Channel / 1RB1



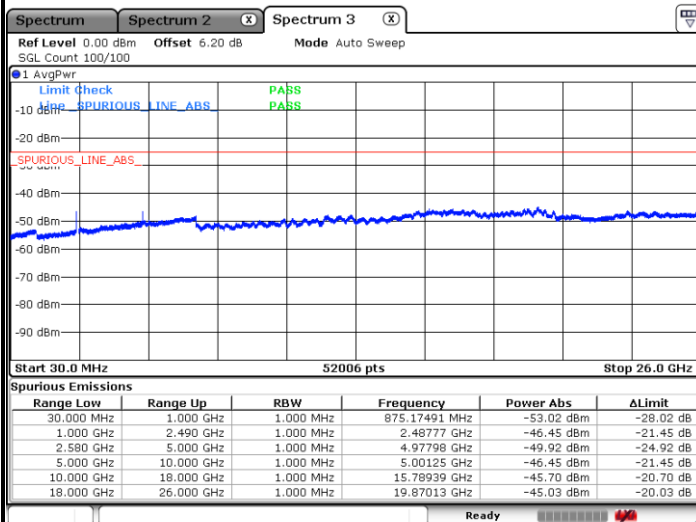
Date: 28 DEC 2020 13:40:22

Middle Channel / 1RB1



Date: 28 DEC 2020 13:42:27

Highest Channel / 1RB1

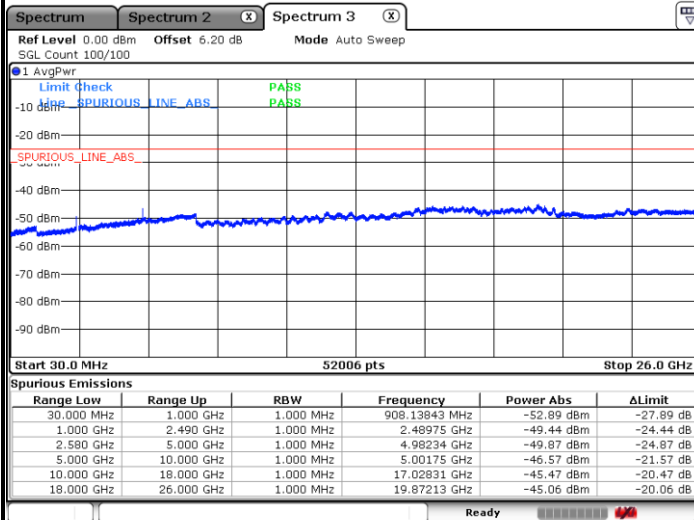


Date: 28 DEC 2020 13:44:42



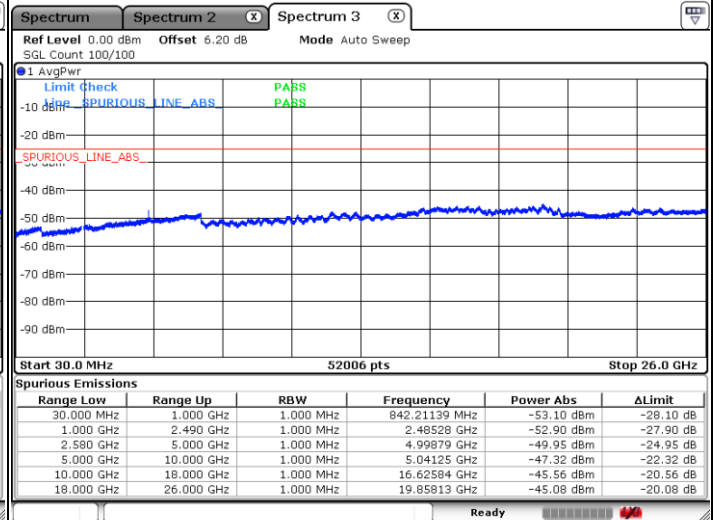
FR1 n7 / 30MHz / DFT-S OFDM / QPSK

Lowest Channel / 1RB1



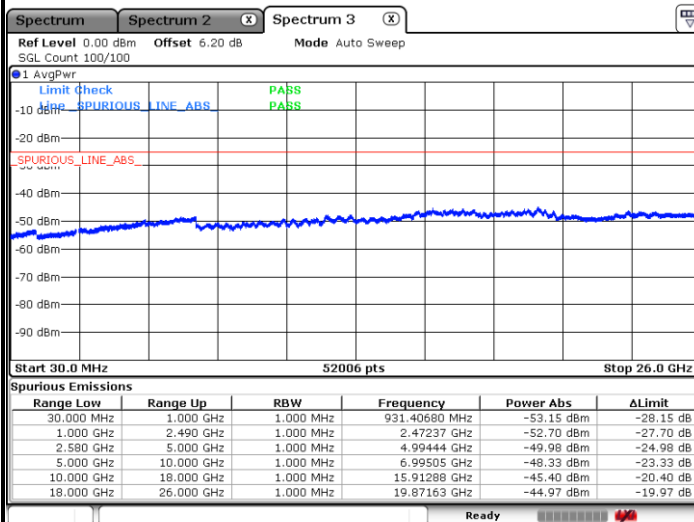
Date: 28 DEC 2020 14:09:13

Middle Channel / 1RB1



Date: 28 DEC 2020 14:27:34

Highest Channel / 1RB1

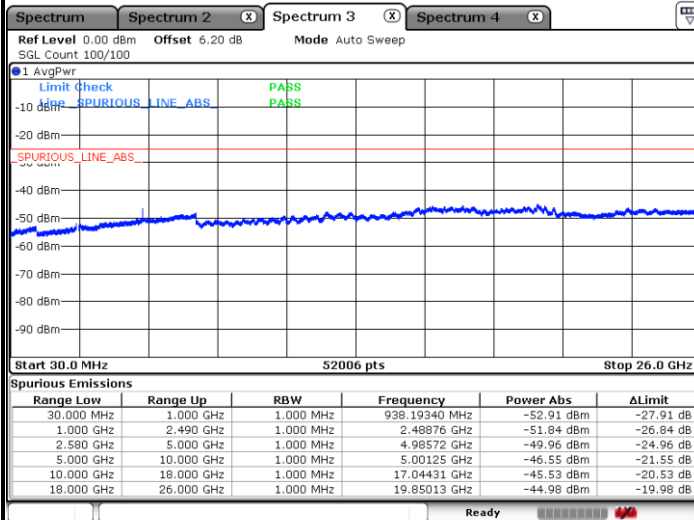


Date: 28 DEC 2020 14:28:45



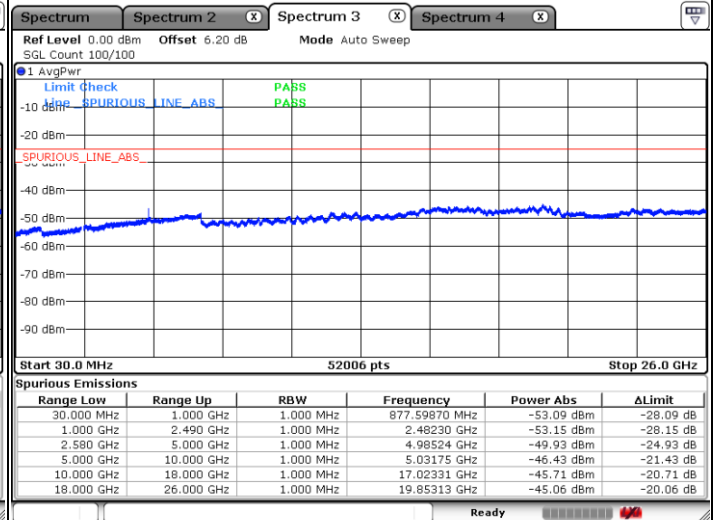
FR1 n7 / 40MHz / DFT-S OFDM / QPSK

Lowest Channel / 1RB1



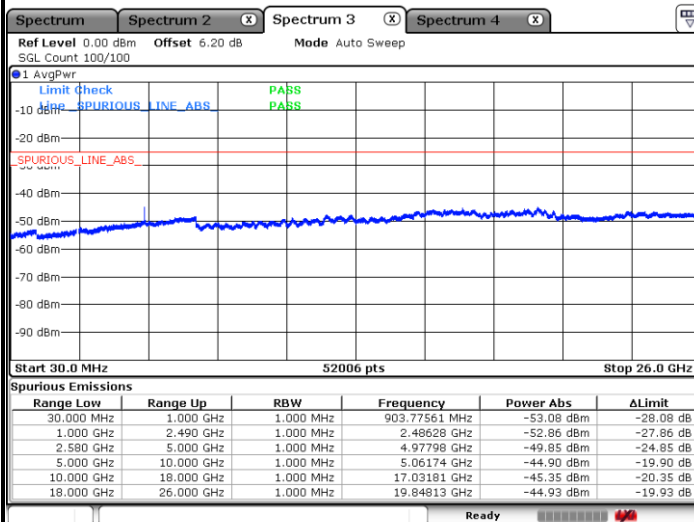
Date: 28 DEC 2020 15:17:53

Middle Channel / 1RB1



Date: 28 DEC 2020 15:01:08

Highest Channel / 1RB1

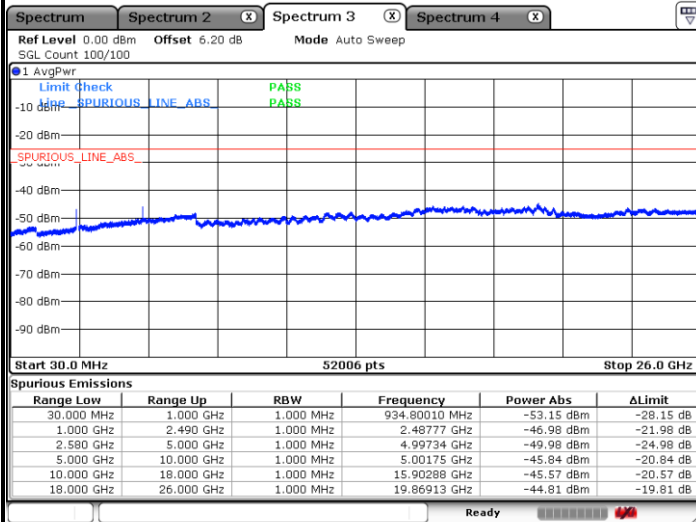


Date: 28 DEC 2020 15:19:39



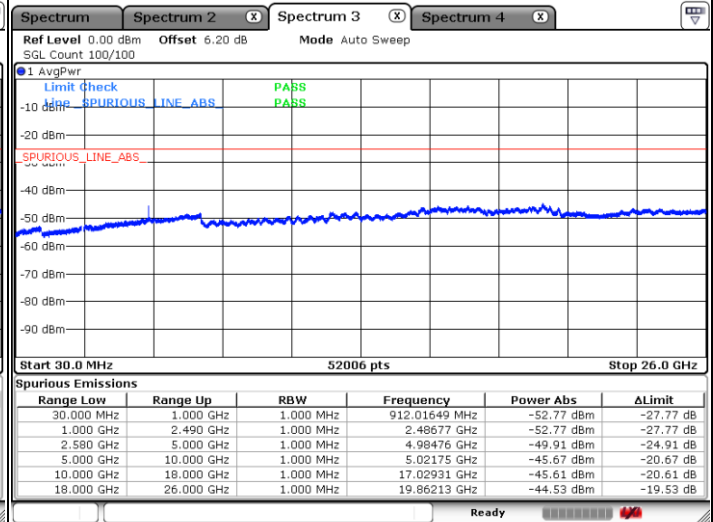
FR1 n7 / 50MHz / DFT-S OFDM / QPSK

Lowest Channel / 1RB1



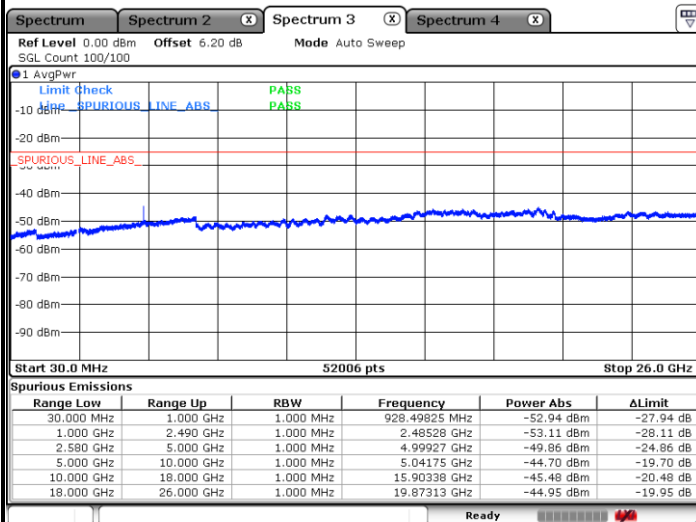
Date: 28 DEC 2020 15:56:00

Middle Channel / 1RB1



Date: 28 DEC 2020 16:14:15

Highest Channel / 1RB1



Date: 28 DEC 2020 16:11:57

Frequency Stability

Test Conditions		FR1 n7 (QPSK) / Middle Channel	Limit
Temperature (°C)	Voltage (Volt)	BW 50MHz	Note 2.
		Deviation (ppm)	Result
50	Normal Voltage	0.0005	PASS
40	Normal Voltage	0.0006	
30	Normal Voltage	0.0008	
20(Ref.)	Normal Voltage	0.0000	
10	Normal Voltage	0.0012	
0	Normal Voltage	0.0015	
-10	Normal Voltage	0.0006	
-20	Normal Voltage	0.0008	
-30	Normal Voltage	0.0028	
20	Maximum Voltage	0.0006	
20	Normal Voltage	0.0000	
20	Battery End Point	0.0022	

Note:

1. Normal Voltage =7.74 V. ; Battery End Point (BEP) =7.3 V. ; Maximum Voltage =8.7 V.
2. Note: The frequency fundamental emissions stay within the authorized frequency block.



5G NR n12 SA

Peak-to-Average Ratio

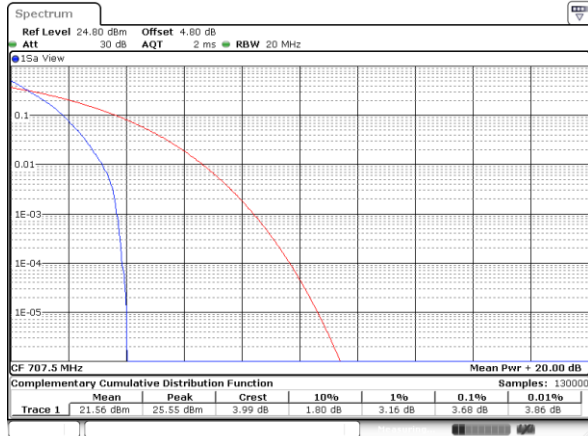
Mode	FR1 n12 / 15MHz / DFT-S OFDM				
Mod.	PI/2 BPSK	QPSK	16QAM	64QAM	Limit: 13dB
RB Size	Full RB	Full RB	Full RB	Full RB	Result
Middle CH	3.68	5.42	6.52	6.84	PASS
Mod.	256QAM				Limit: 13dB
RB Size	Full RB				Result
Middle CH	7.10				PASS



FR1 n12 / 15MHz / DFT-S OFDM

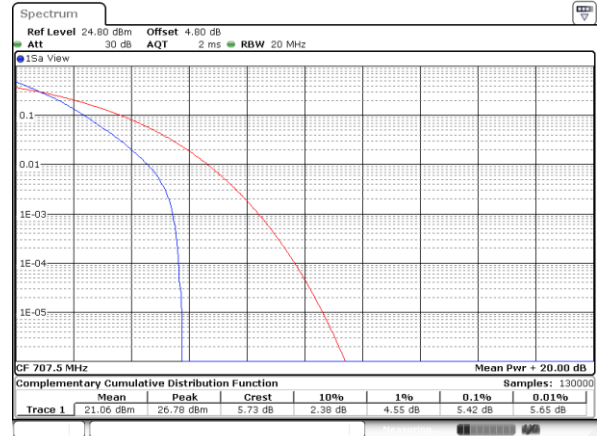
Middle Channel / Full RB

PI/2 BPSK

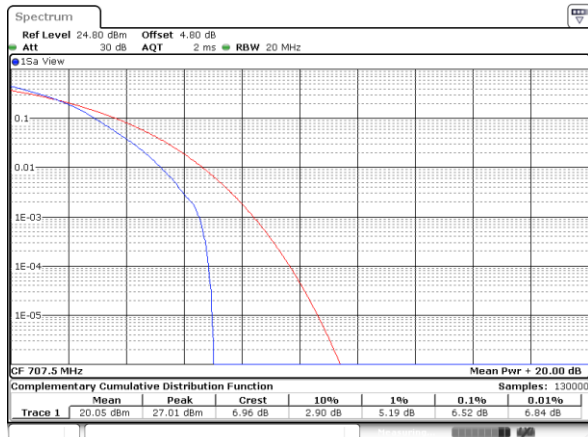


Middle Channel / Full RB

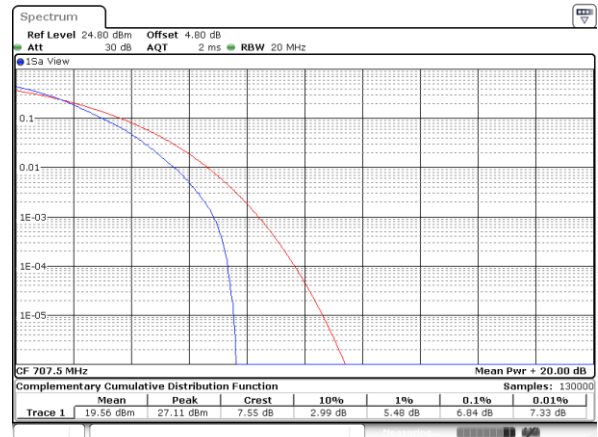
QPSK



16QAM



64QAM

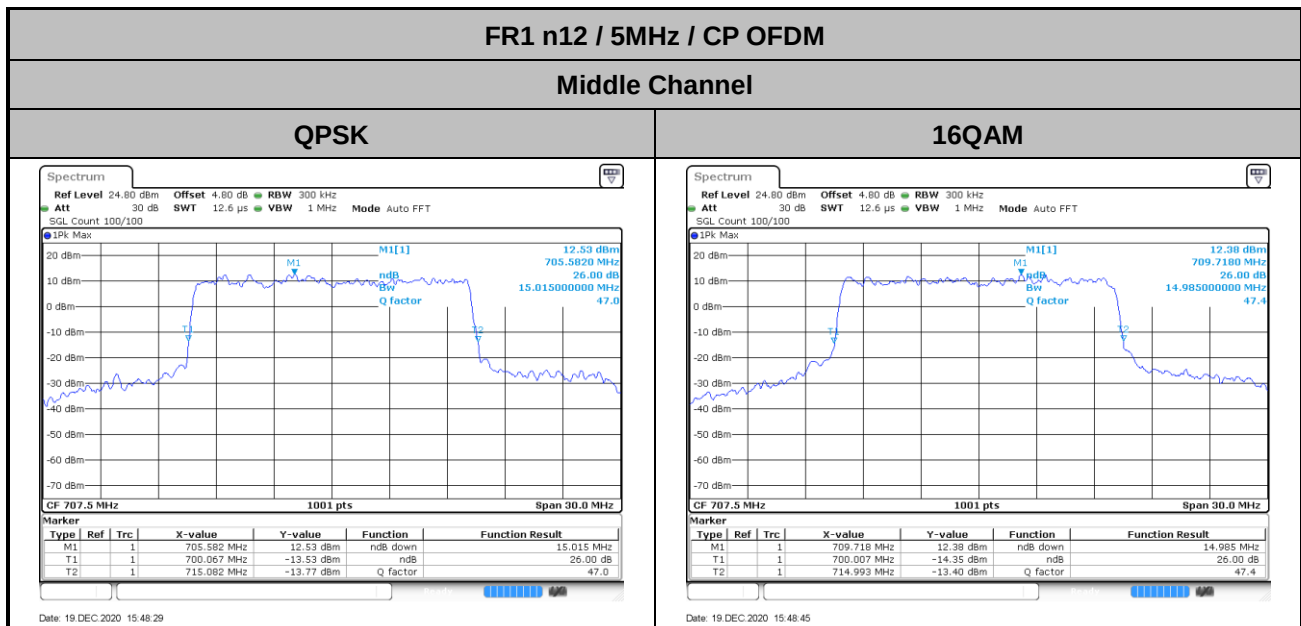


256QAM



**26dB Bandwidth**

Mode	FR1 n12 : 26dB BW(MHz) / CP OFDM	
BW	15MHz	
Mod.	QPSK	16QAM
Middle CH	15.015	14.985





Occupied Bandwidth

Mode	FR1 n12 : 99%OBW(MHz) / CP OFDM	
BW	15MHz	
Mod.	QPSK	16QAM
Middle CH	14.056	14.086

