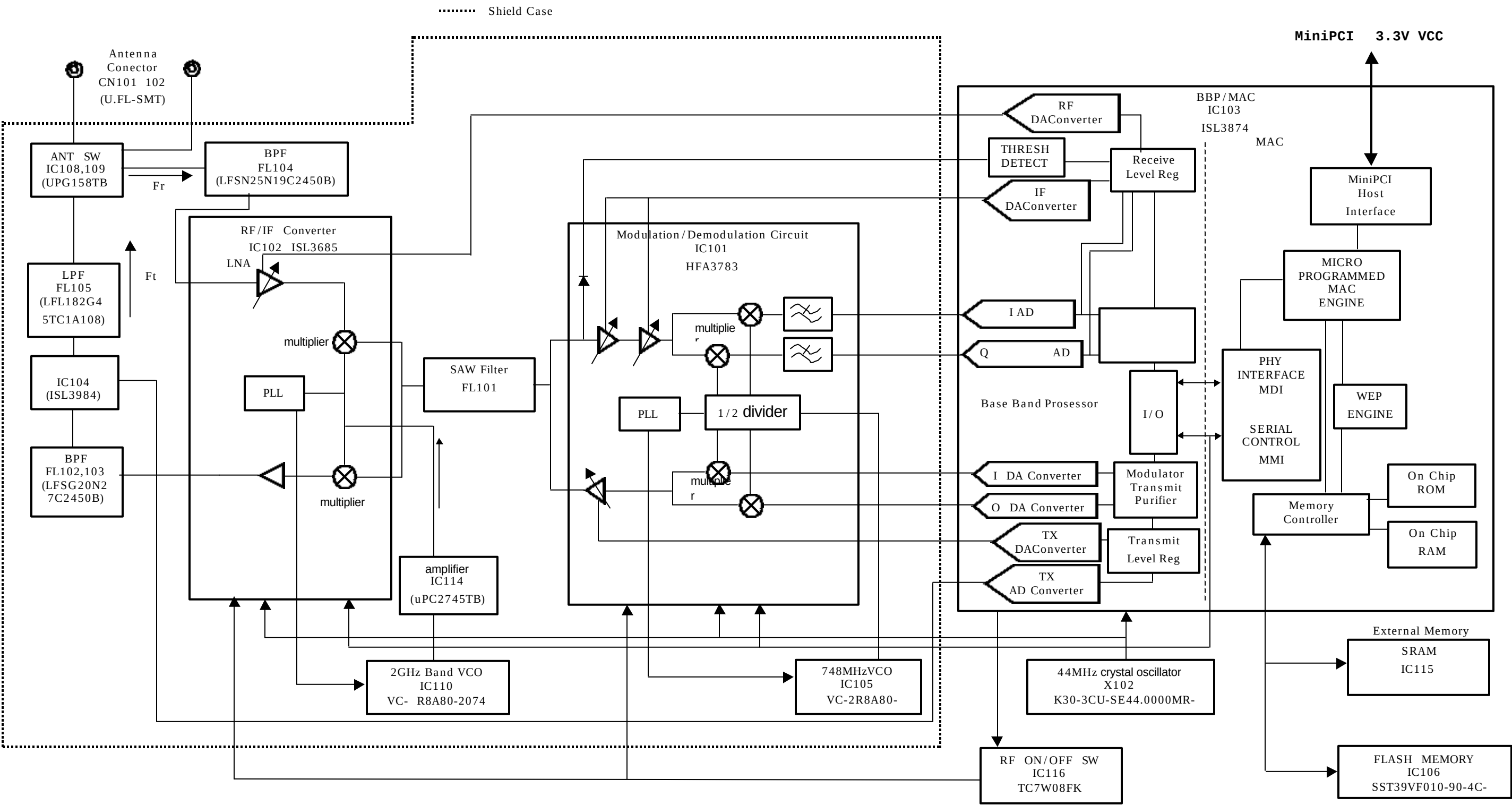


Wireless LAN Module Block Diagram



The diagram illustrates the system architecture for the Pentium III 700/300ULV (FCBGA) processor. The central component is the **Chipset [Banister]** (492pin BGA), which connects to various system components via multiple buses:

- Memory:** Connected to **Memory PC100 DIMM** (144pin) via the **Memory BUS (100MHz)**.
- Video:** Connected to **VGA (Mobility-M1)** (328pin BGA) via the **Memory BUS**. The VGA output is connected to **LCD** and **CRT** displays.
- Storage:** Connected to **HDD** (28pin) via the **ATA33 (33MHz)** bus.
- Audio:** Connected to **Codec STAC9723** (48pin TQFP) via **AC Link 1**. The codec is connected to **AMP** (28pin), which outputs to **Headphone Out**, **Microphone In**, and a **Speaker**.
- Expansion:**
 - PCIC (OZ711E1) CSP** is connected via the **PCI BUS (33MHz)** to a **PC Card**.
 - Mini PCI slot** (128pin) is connected via the **PCI BUS** to **WirelessLAN 802.11b**, which is connected to an **Antenna**.
- Power Management:** Connected to **Pow MPU** (100pin TQFP) via **AC Link 2** and **SMBUS**. The Pow MPU controls the **Status LCD**, **Security Button**, and **Battery**.
- System Buses:**
 - X BUS (8MHz order)** connects to **FDC37N769** (100pin TQFP), which manages **Dr/Rv**, **Serial**, **Parallel**, and **FDD** interfaces, as well as **IR**.
 - P BUS** connects to **KBC** (28pin TQFP), **BIOS ROM** (48pin), and **RING** (100pin TQFP).
- Input/Output:**
 - USB0** connects to **USB Port1**.
 - USB1** connects to **USB HUB TUSB2036** (32pin TQFP), which manages **USB Port2** and **Bluetooth**.
 - Keyboard** and **Touch Panel** are connected to the **Selector** block.
 - Quick point** is connected to the **Selector** block.
 - PS/2** interface is connected to the **Selector** block.

Other components shown include the **Gyserville ASIC** connected to the **Chipset**, and the **Antenna** connected to the **WirelessLAN** module.

[illegible]