

COMPAL CONFIDENTIAL

MODEL NAME : EDX20

PCB No : LS 2481

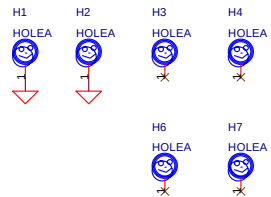
PCB Revision : 0.2

SMT build : PVT

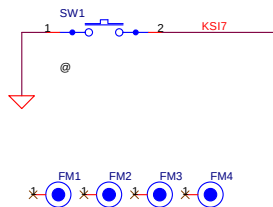
EDX20 Sub_Board Schematics Document

BTN Board

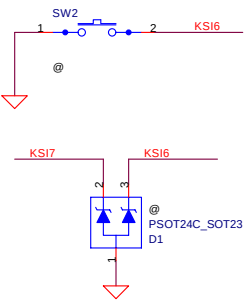
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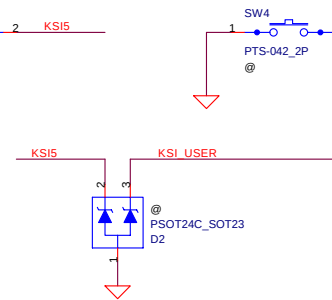
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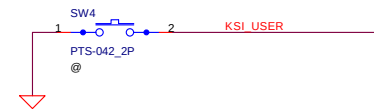
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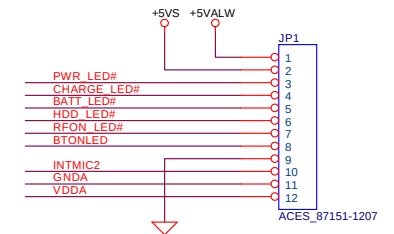
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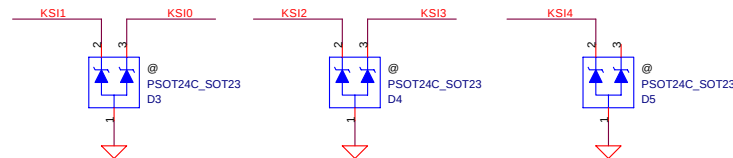
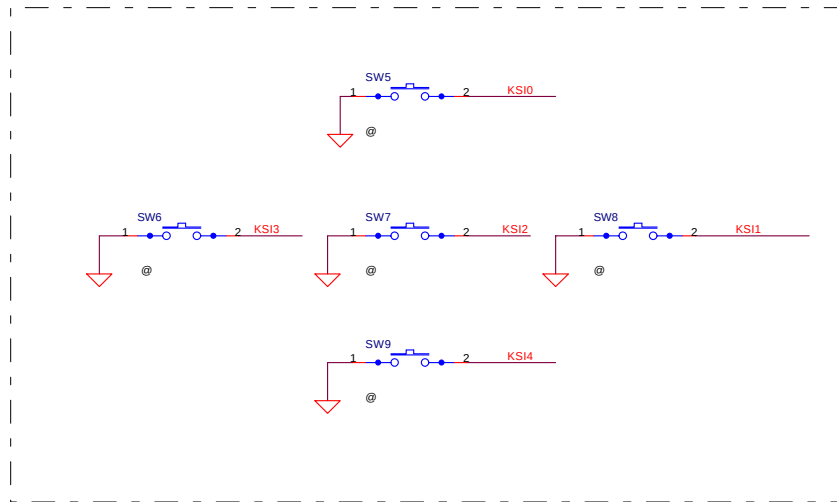
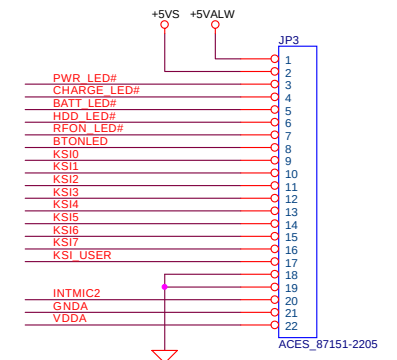
USER BTN4



To LED Board Connector



From M/B Side Connector



Compal Electronics, Inc.

BTN BOARD

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MODEL NAME : EDX20

PCB No : LS 2482

PCB Revision : 0.3

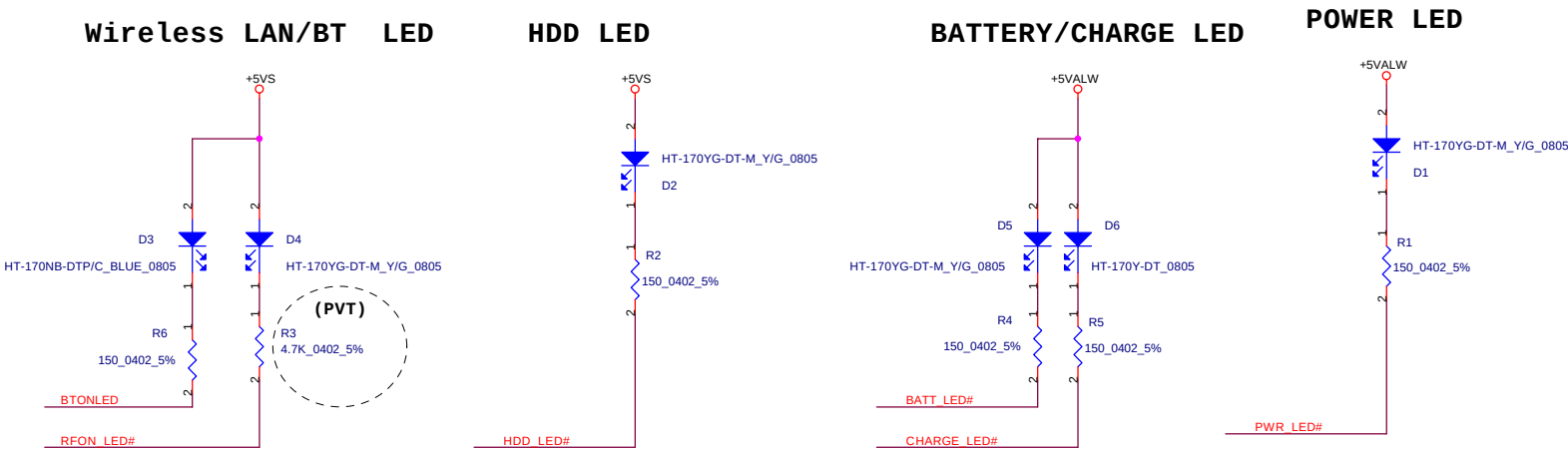
SMT build : PVT

EDX20 Sub_Board Schematics Document

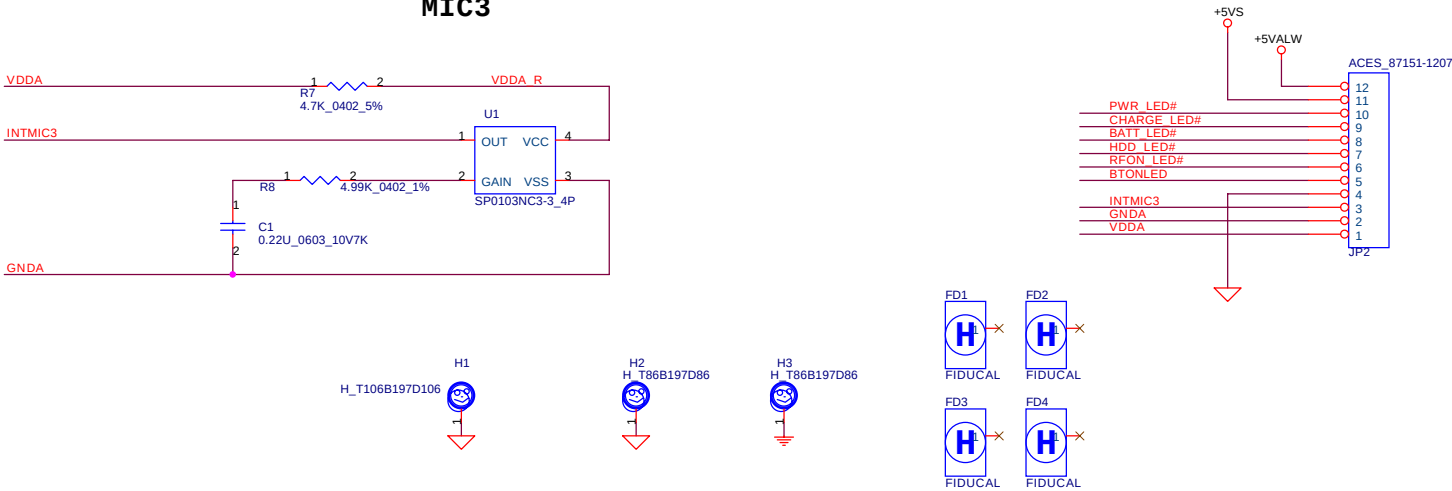
LED Board

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		E	

LED Board



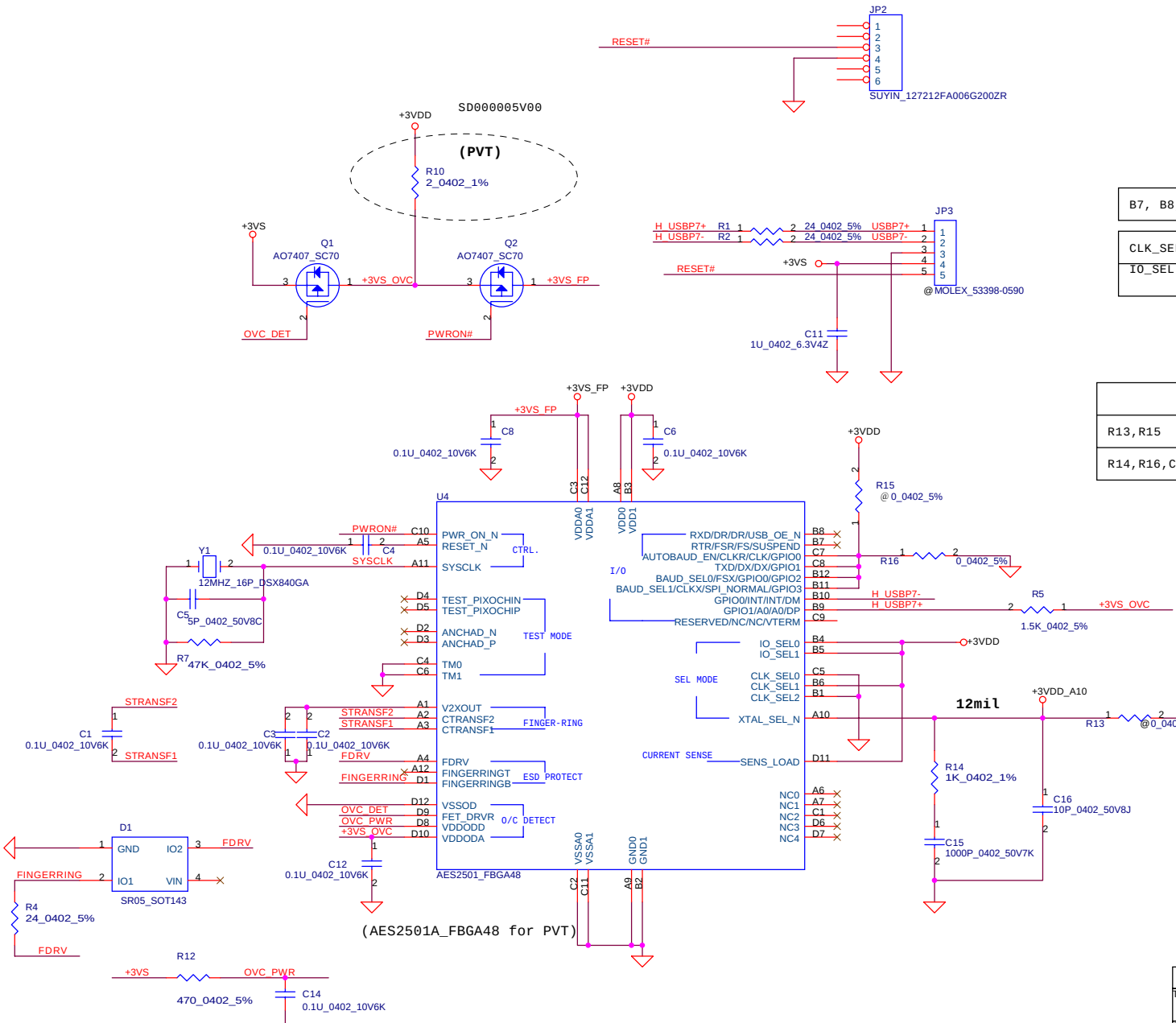
MIC3



COMPAL CONFIDENTIAL

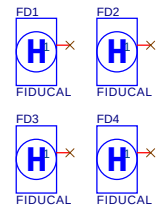
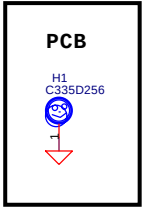
MODEL NAME : EDX20
PCB No : LS 2483
PCB Revision : 0.3
SMT build : PVT

EDX20 Sub_Board Schematics Document
FingerPrint Board



B7, B8, D2, D3, D4, D5 AND D9 MUST KEEP VIA		
CLK_SEL[0..2]	010	USB I/F 12MHz
IO_SEL[0,1]	11	USB 2.0 FULL SPEED

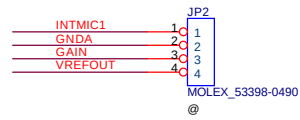
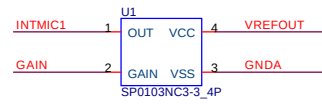
	AES2501	AES2501A
R13, R15	POP	DE_POP
R14, R16, C15, C16	DE_POP	POP



COMPAL CONFIDENTIAL

MODEL NAME : EDX20
PCB No : LS 2484
PCB Revision : 0.3
SMT build : PVT

EDX20 Sub_Board Schematics Document
MIC Board



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Compal Electronics, Inc.			
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MIC			
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MODEL NAME : EDX20

PCB No : LS 2485

PCB Revision : 0.2

SMT build : PVT

EDX20 Sub_Board Schematics Document

ALS_MIC Board

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COMPAL CONFIDENTIAL

MODEL NAME : EDX20

PCB No : LS 2486

PCB Revision : 0.2

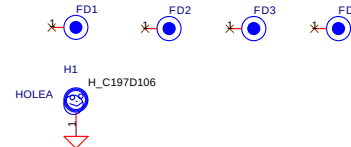
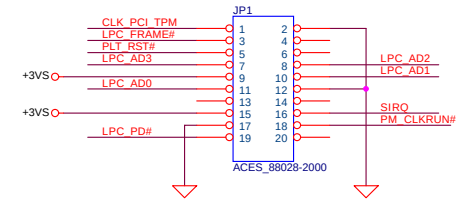
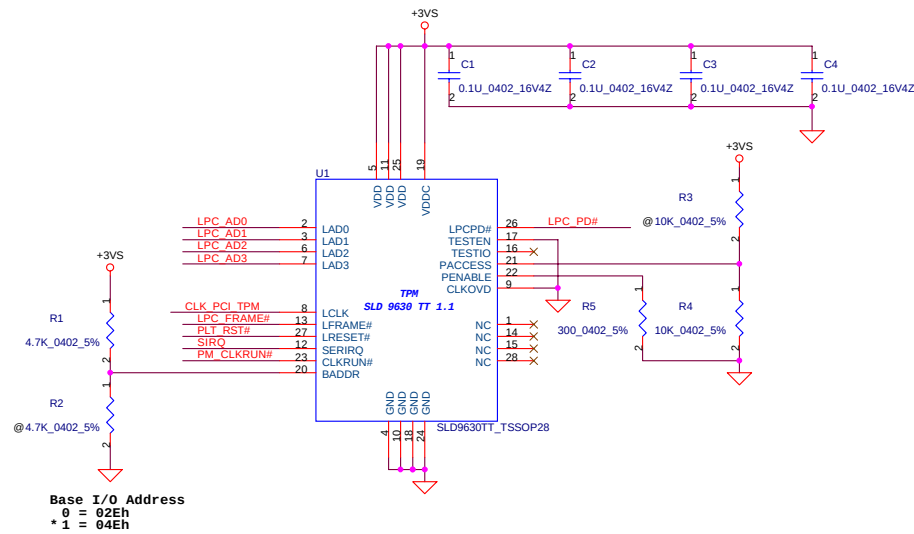
SMT build : PVT

EDX20 Sub_Board Schematics Document

TPM Board

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TPM



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Compal Electronics, Inc.			
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COMPAL CONFIDENTIAL

MODEL NAME : EDX20

PCB No : LS 2487

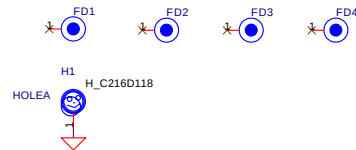
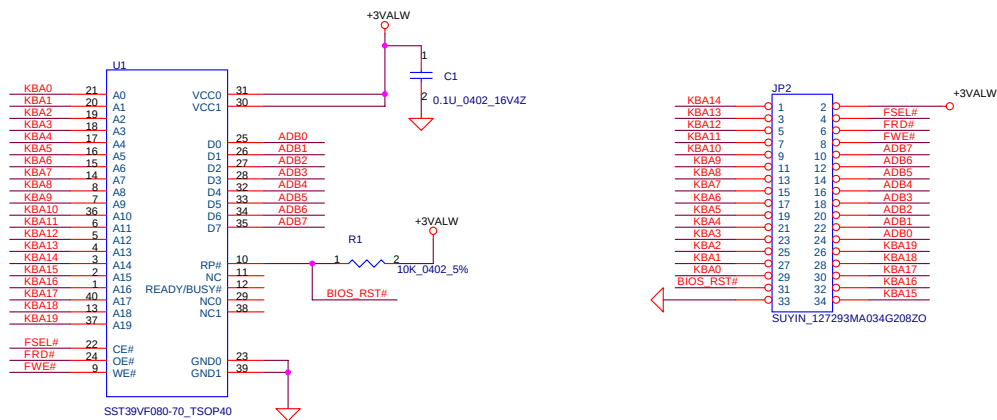
PCB Revision : 0.2

SMT build : PVT

EDX20 Sub_Board Schematics Document

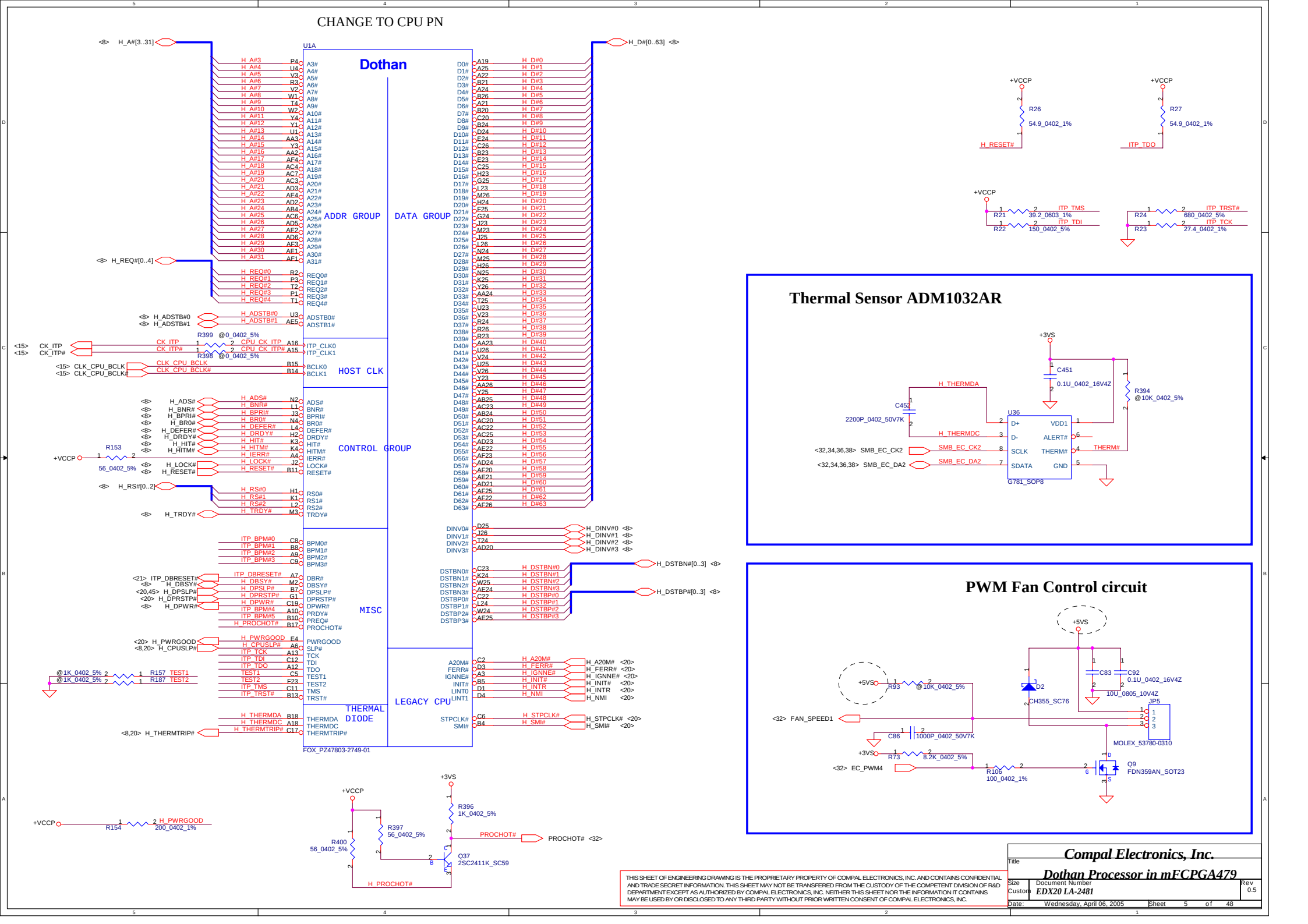
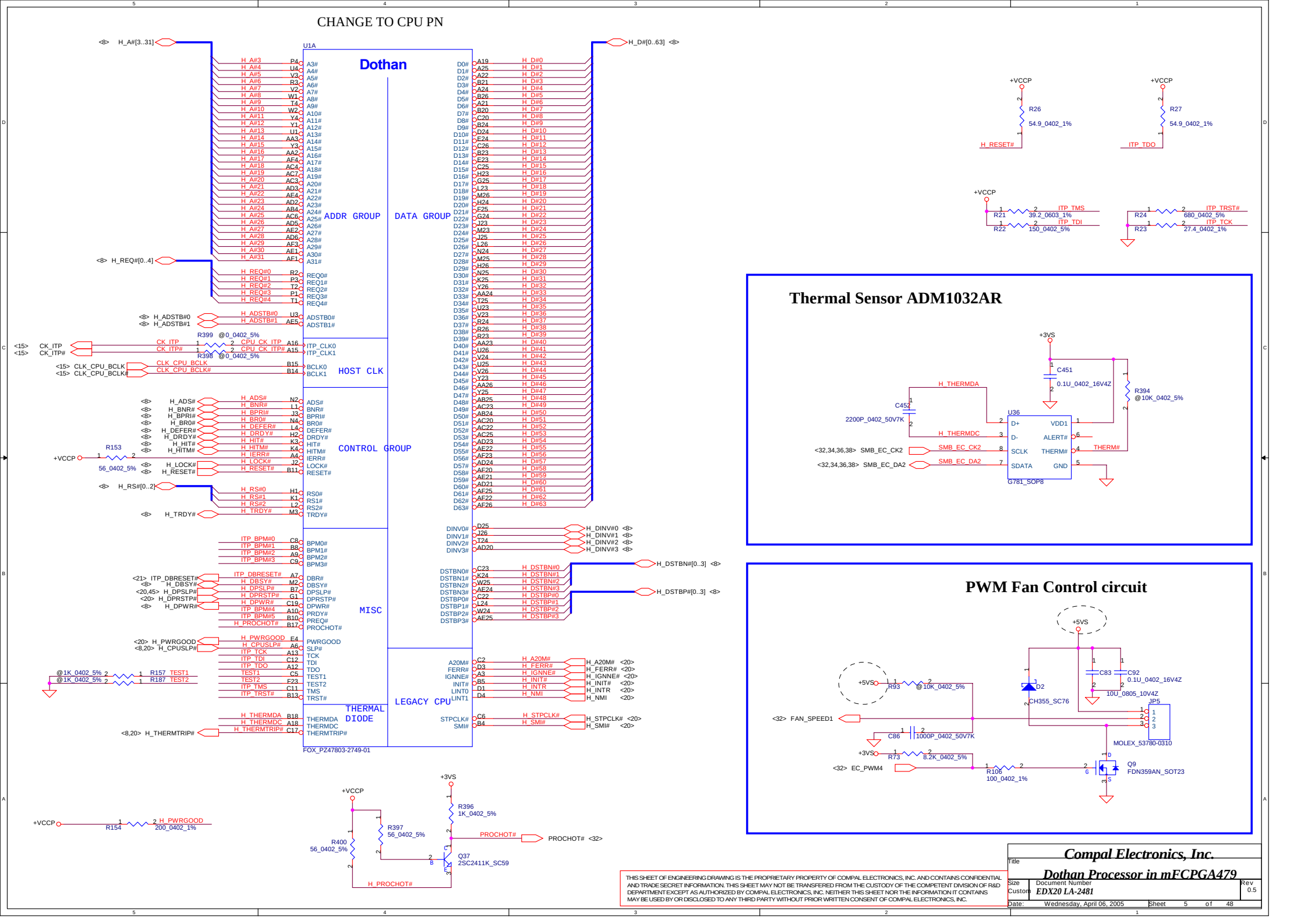
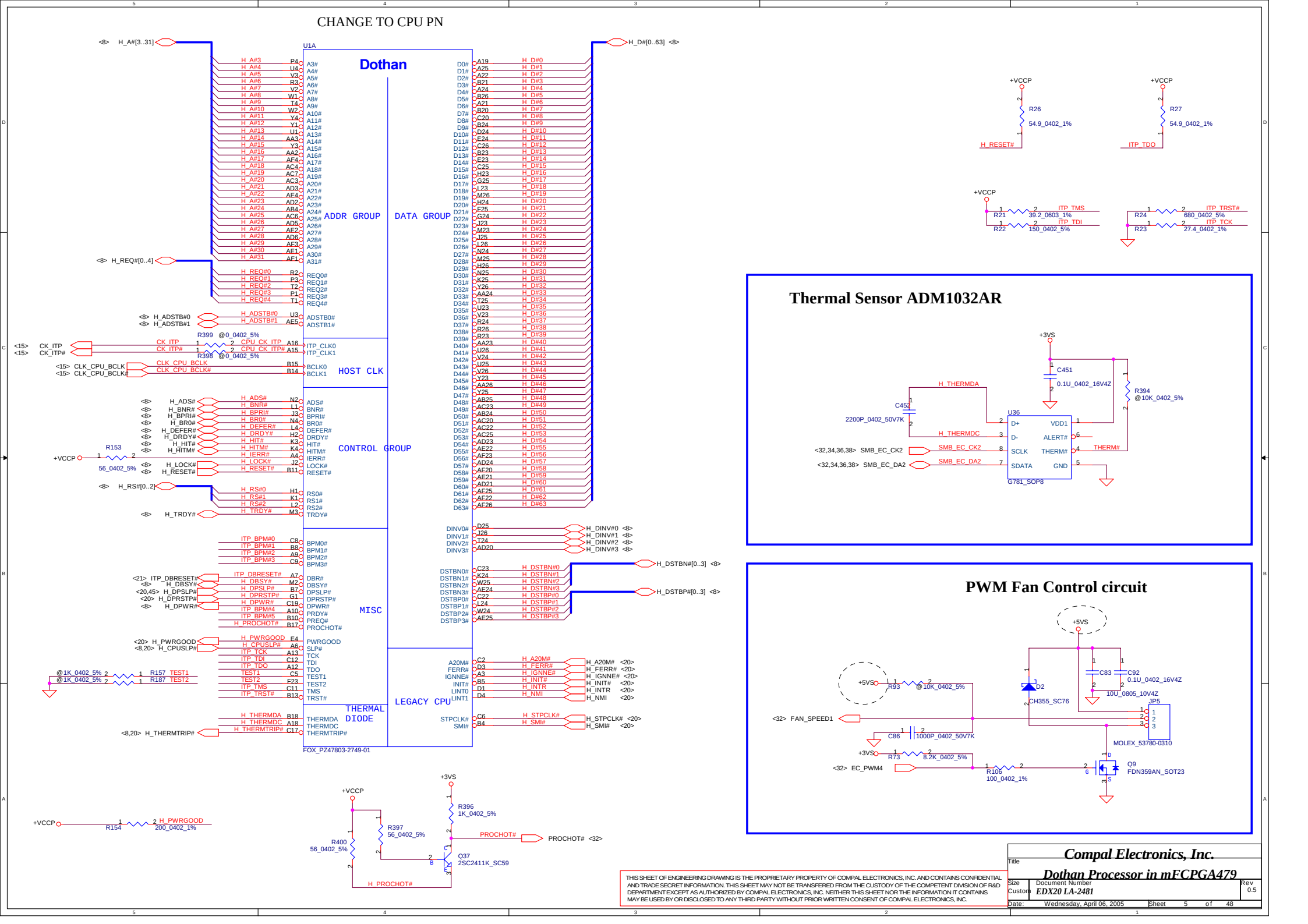
BIOS Board

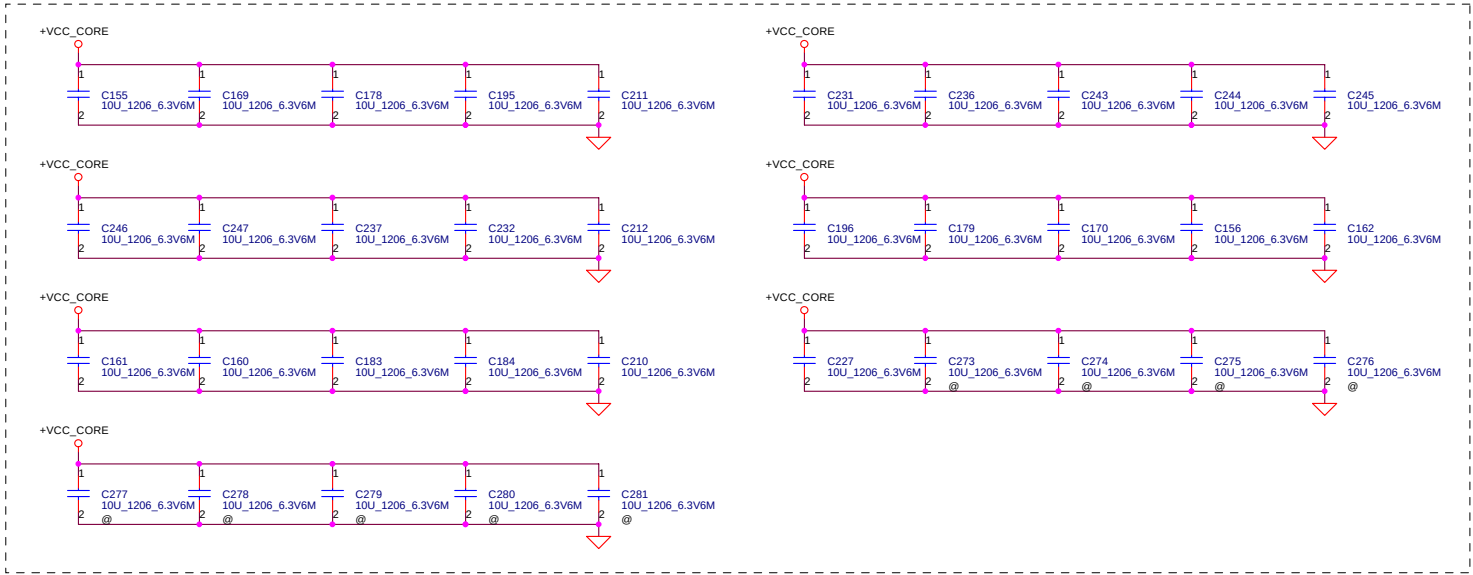
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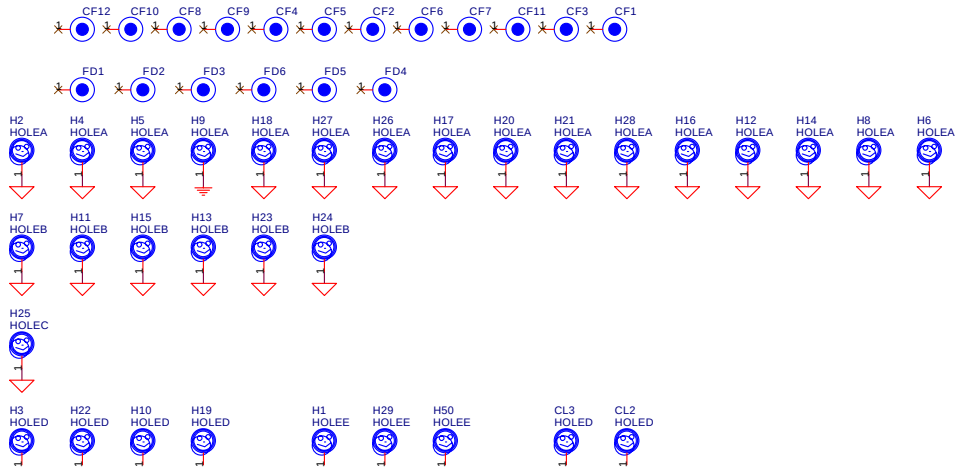
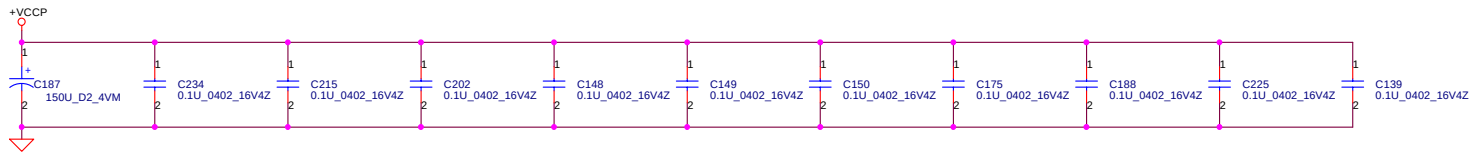
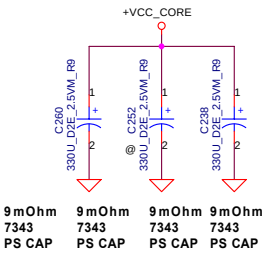
Compal Electronics, Inc.			
Title			
BIOS Board			
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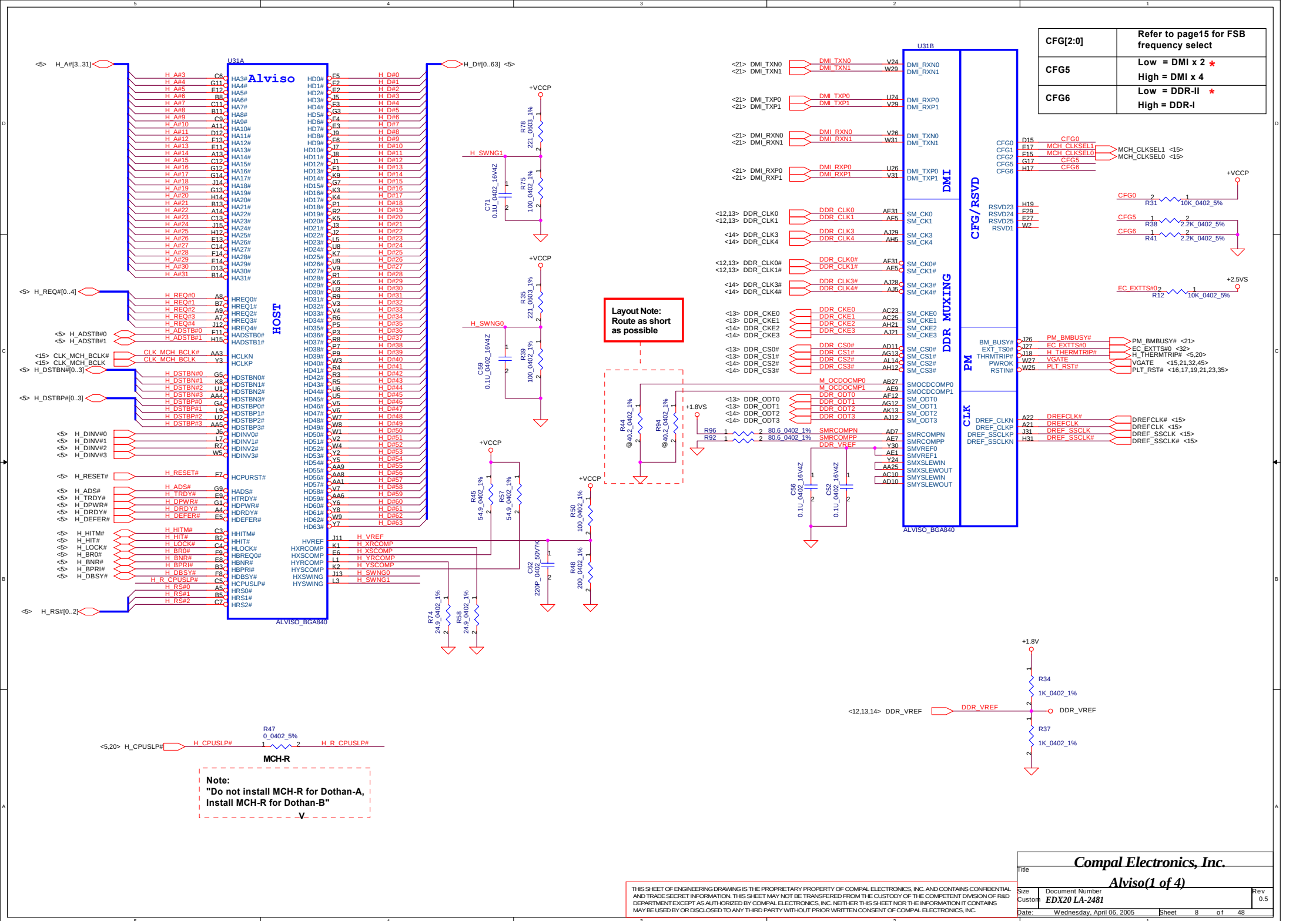


Near VCORE regulator.

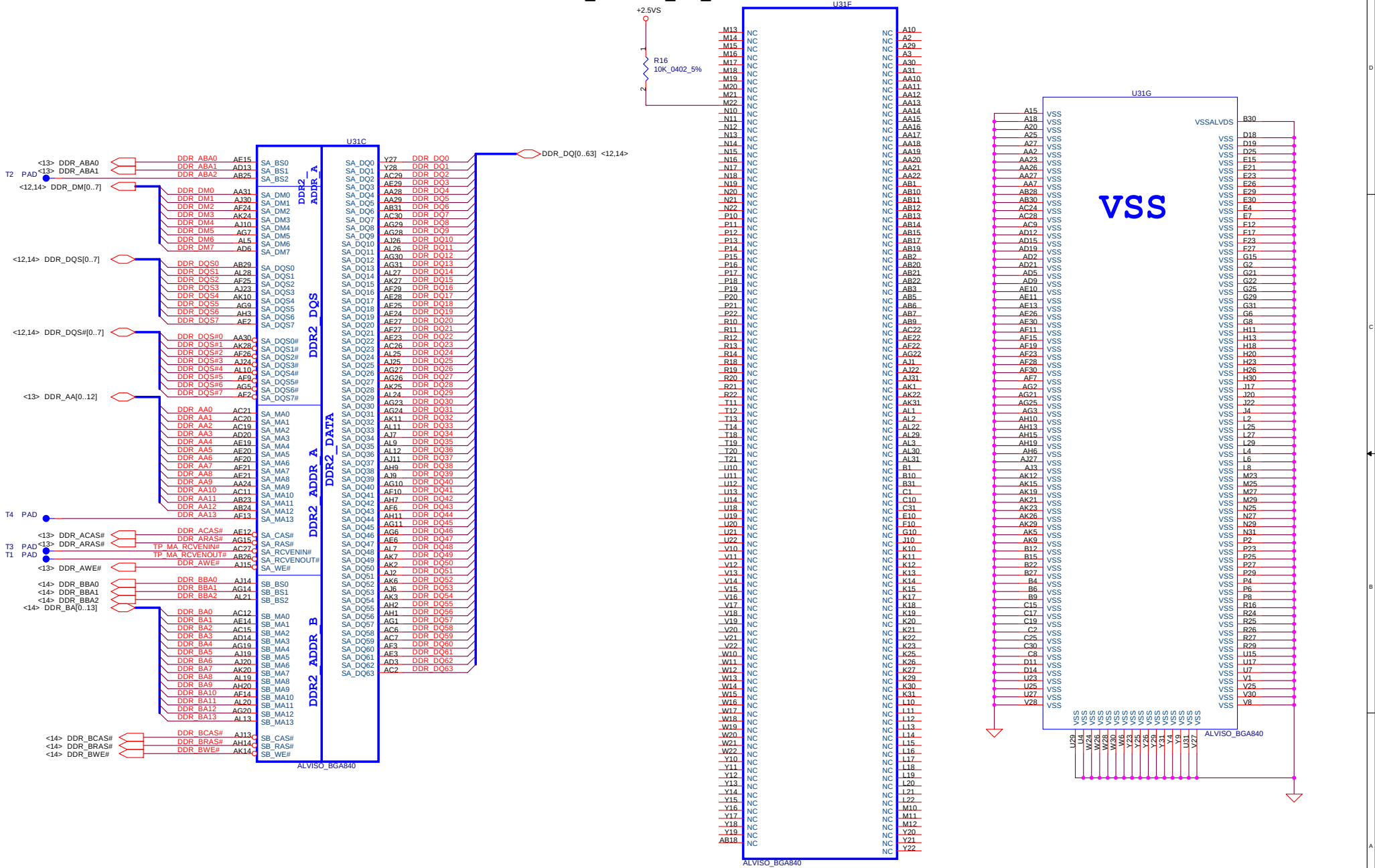


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Title				Compal Electronics, Inc. CPU Bypass			
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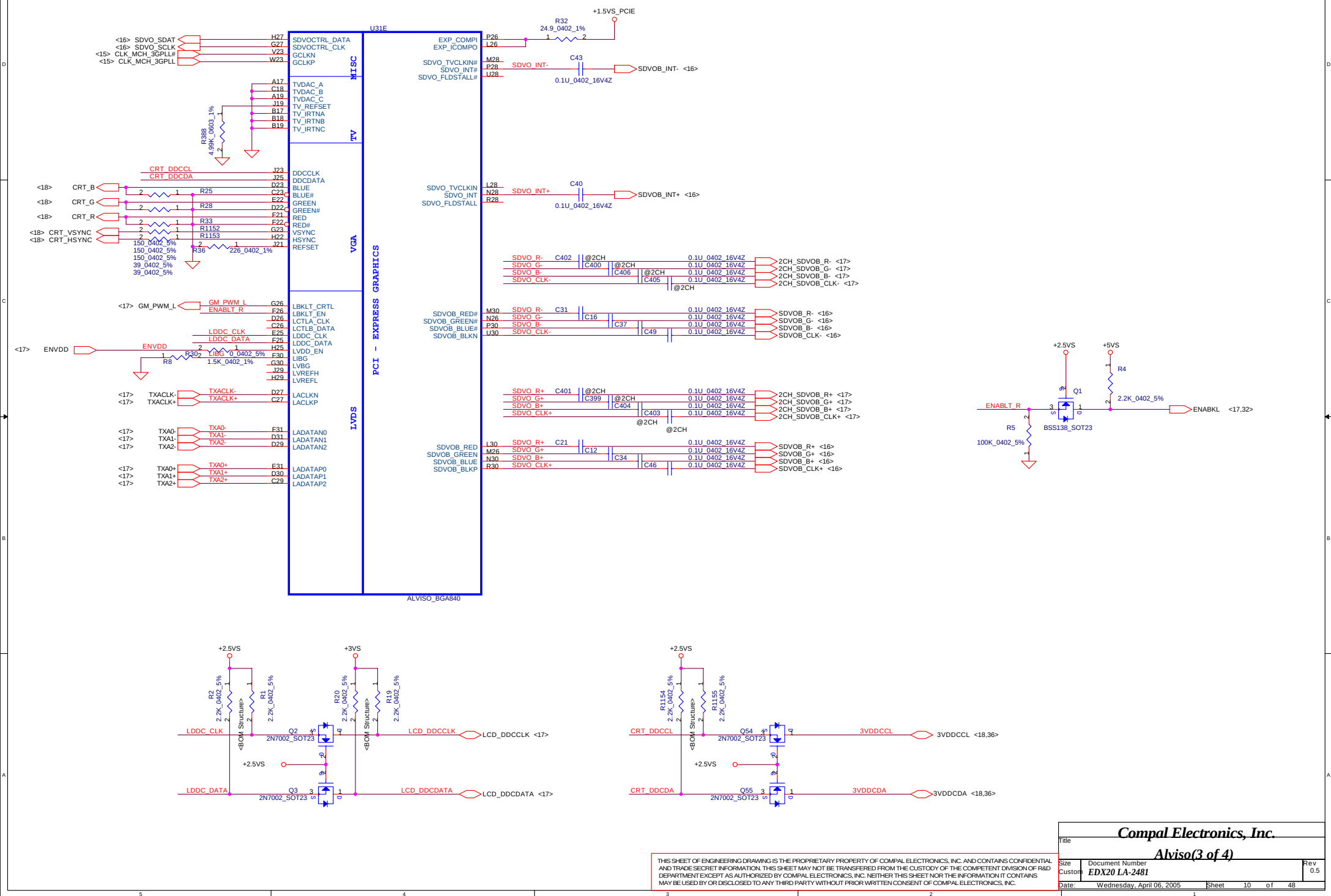


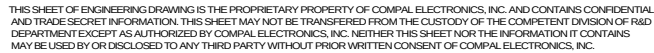
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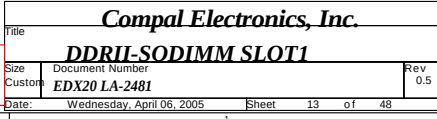


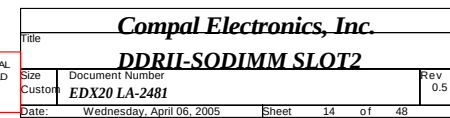
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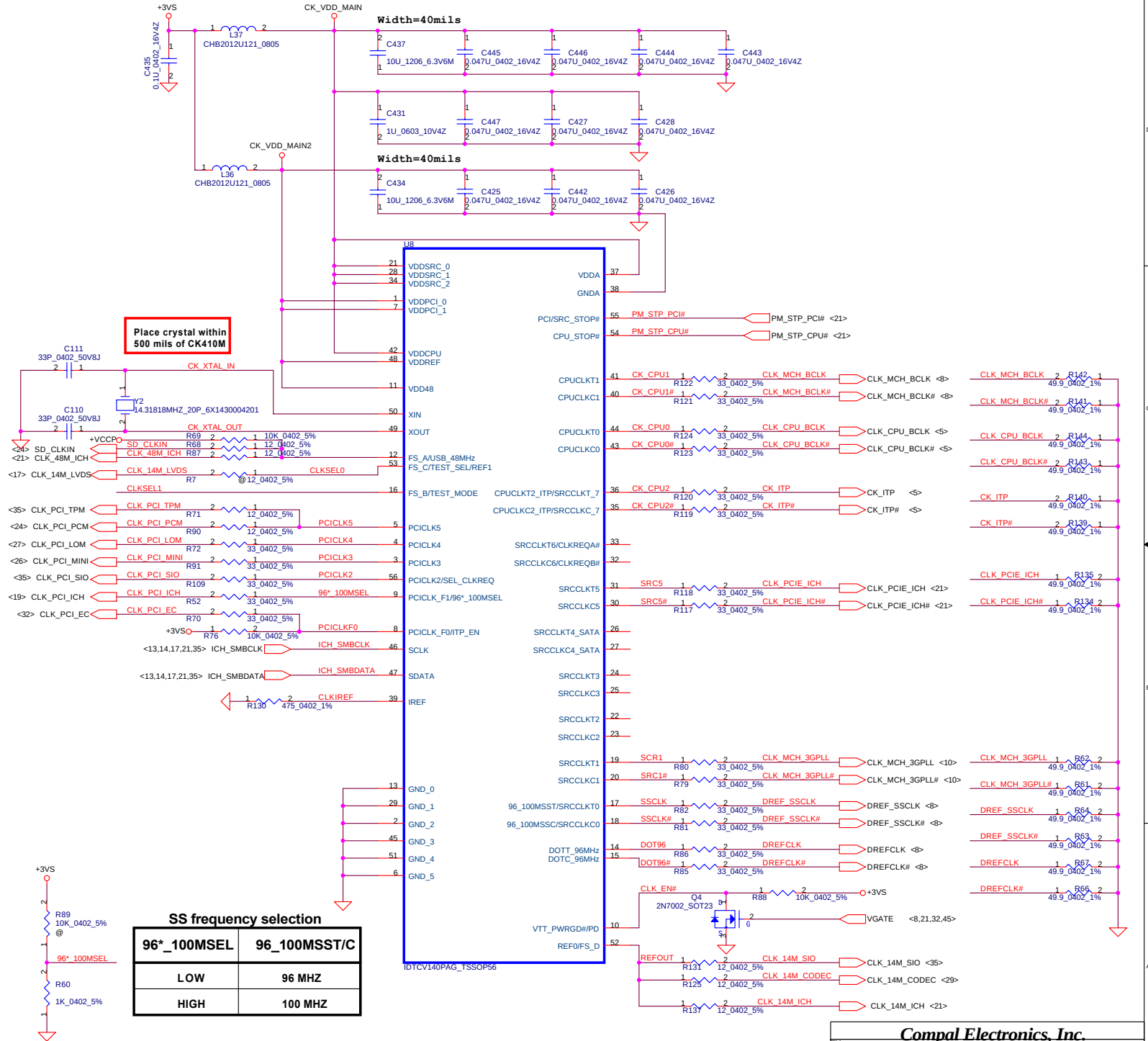
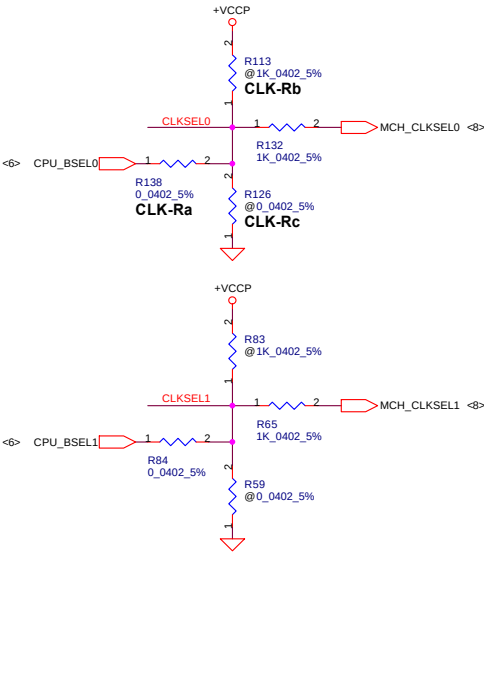
Dothan A step

FSC	FSB	FSA	CPU	SRC	PCI
CLKSEL0	CLKSEL1	CLKSEL2	MHz	MHz	MHz
1	0	1	133	100	33.3
0	0	1	100	100	33.3

Dothan B step

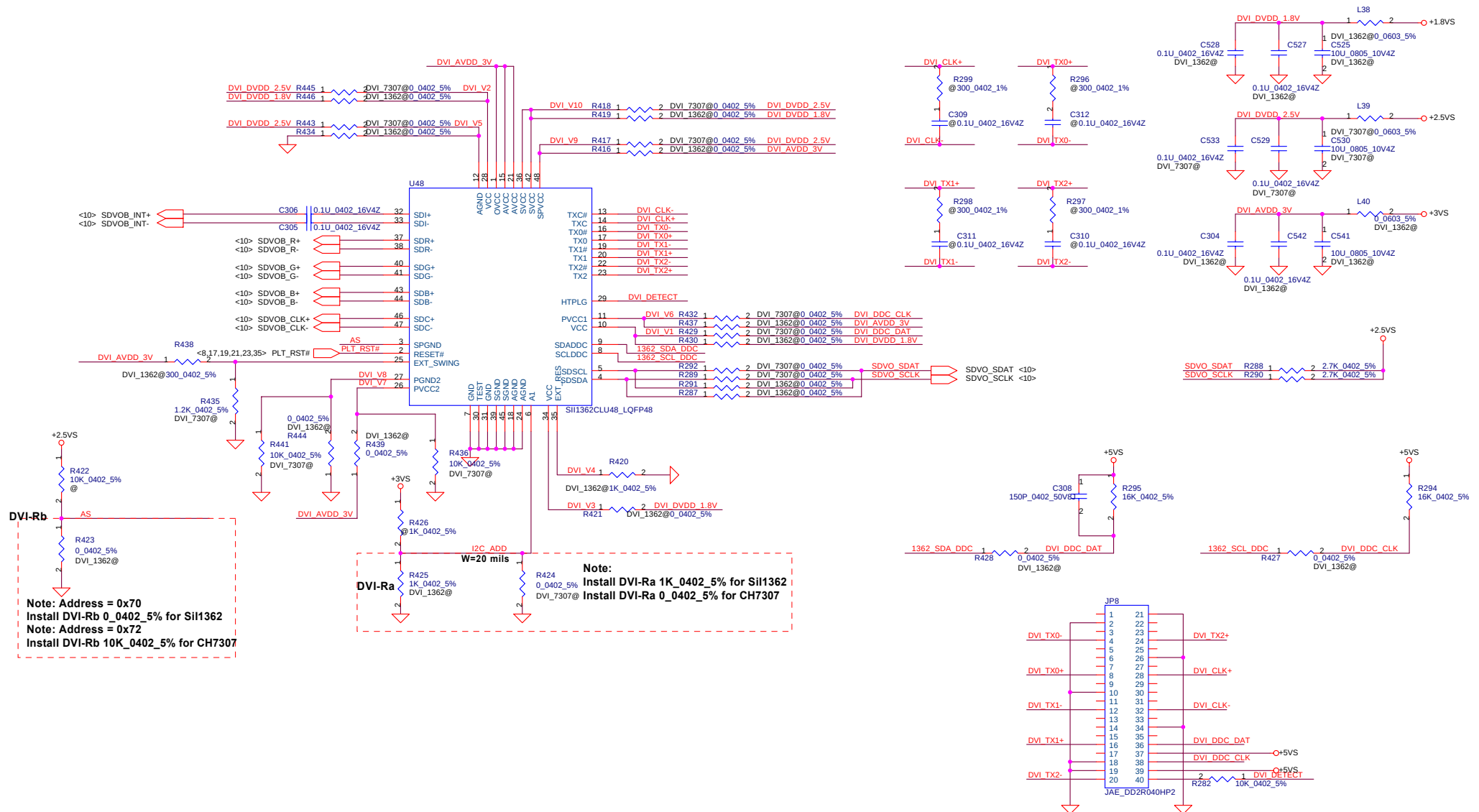
FSC	FSB	FSA	CPU	SRC	PCI
CLKSEL0	CLKSEL1	CLKSEL2	MHz	MHz	MHz
0	0	1	133	100	33.3
1	0	1	100	100	33.3

CPU Type	CLK-Ra	CLK-Rb	CLK-Rc
Dothan-A PSB400	OPEN	OPEN	0 Ohm
Dothan-A PSB533	OPEN	1K Ohm	OPEN
Dothan-B	0 Ohm	OPEN	OPEN



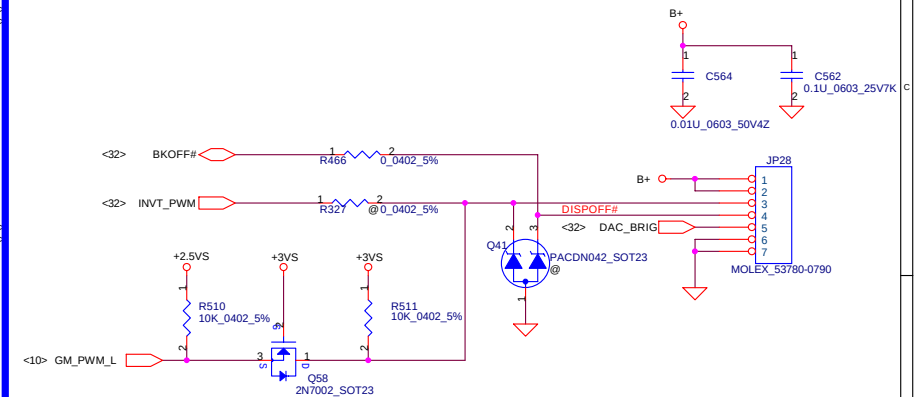
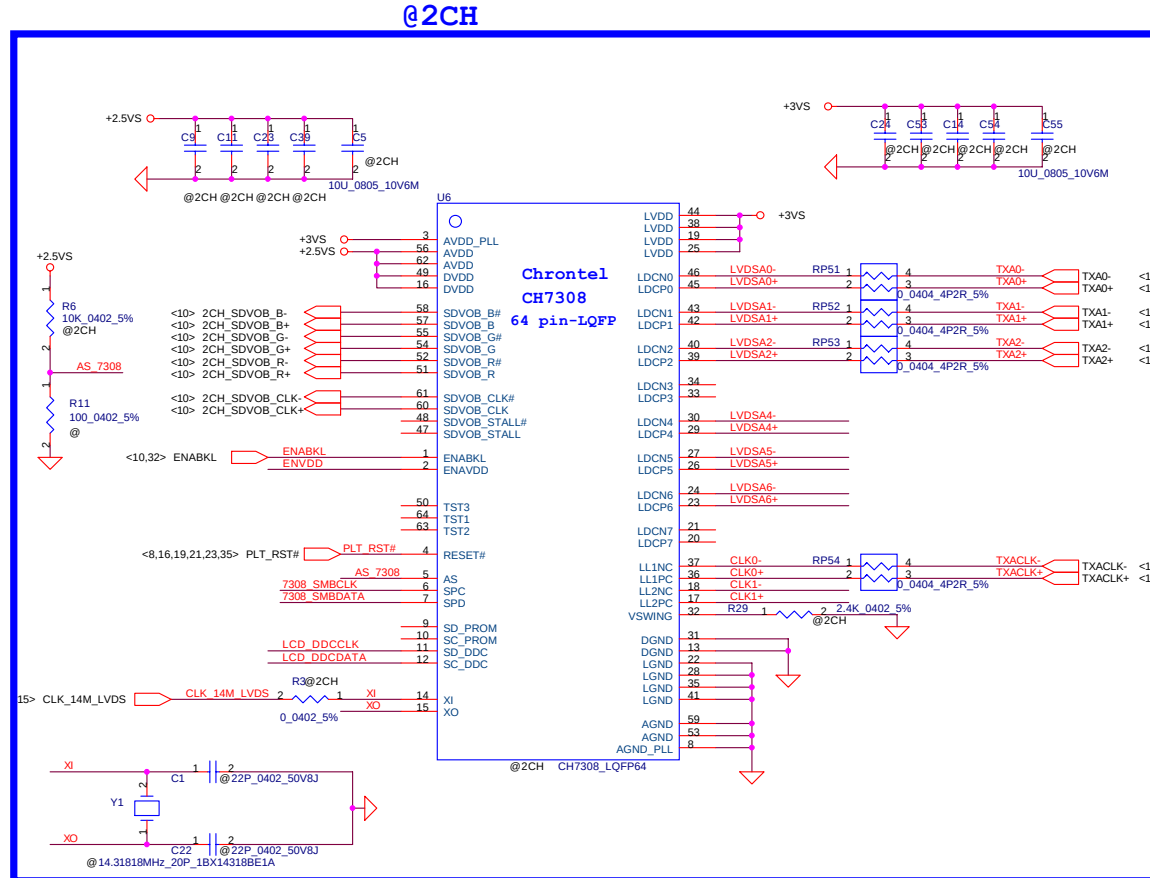
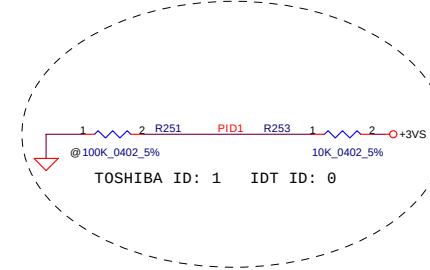
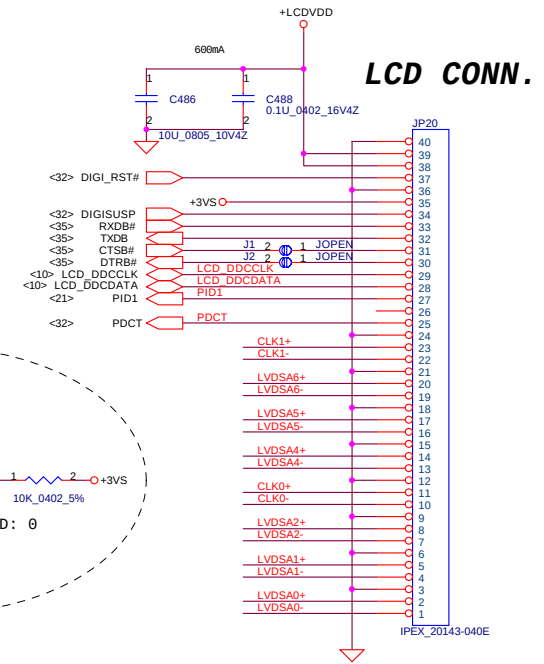
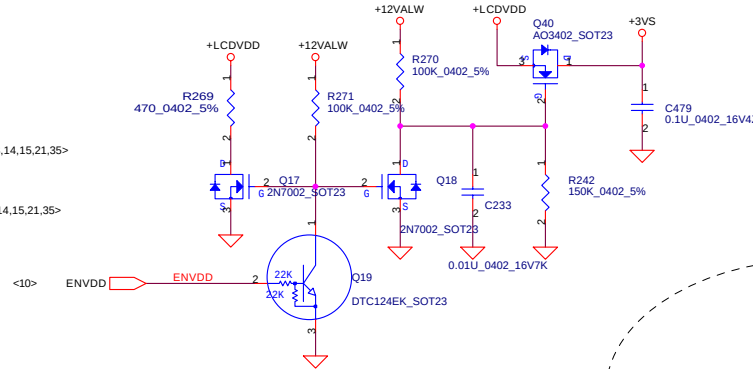
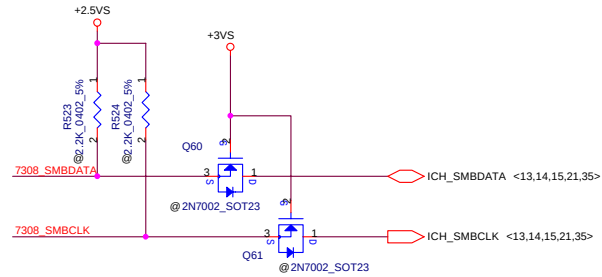
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DVI CONTROLLER



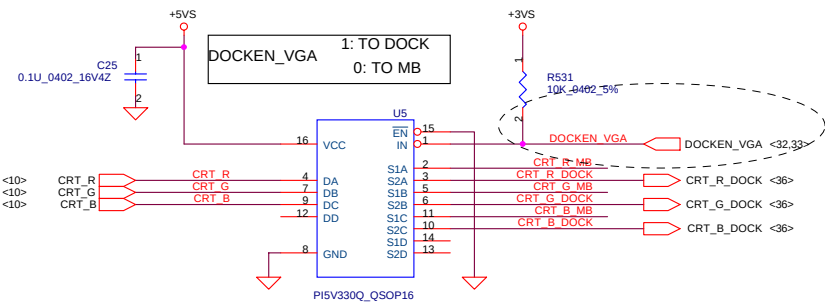
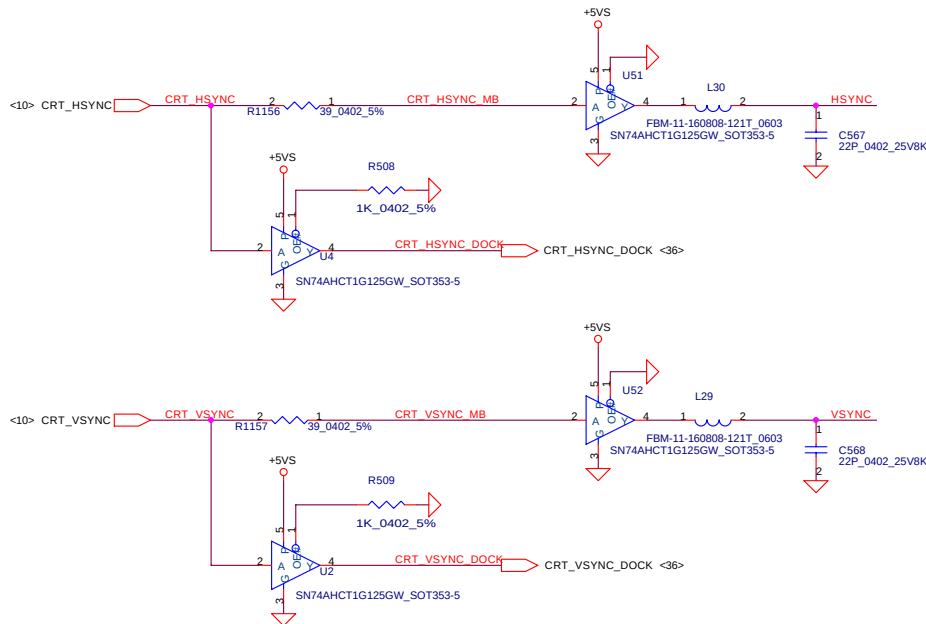
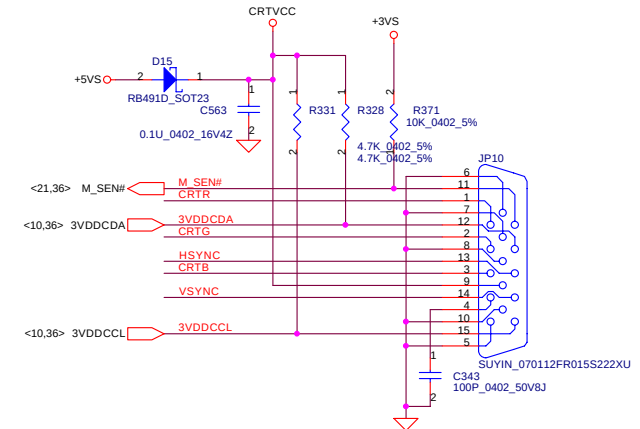
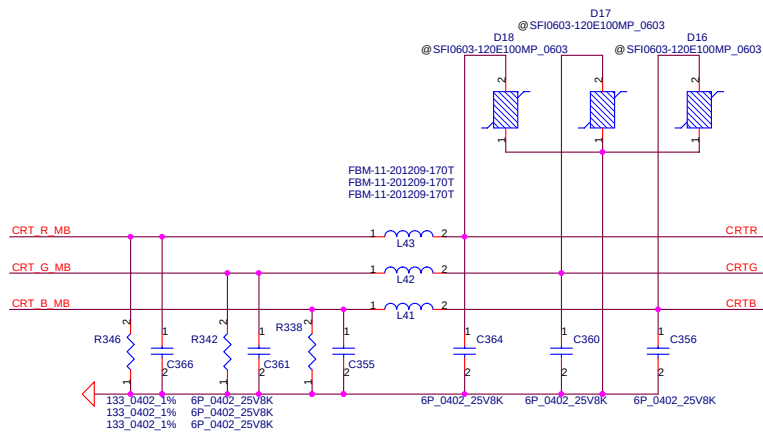
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Compal Electronics, Inc.			
DVI CH7307/SiI1362			
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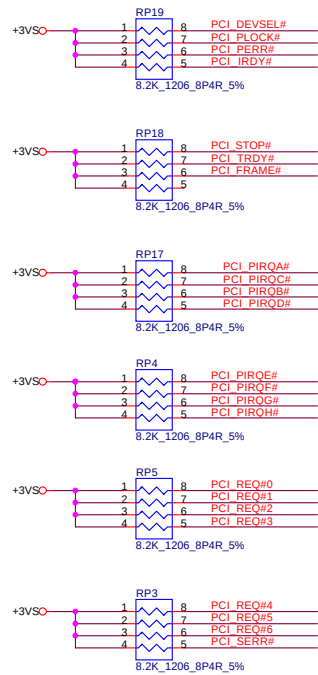
Compal Electronics, Inc.			
Title			
LCD Conn&Inverter			
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Compal Electronics, Inc.			
Title			
LCD Connector & CRT			
Size	Document Number	Rev	
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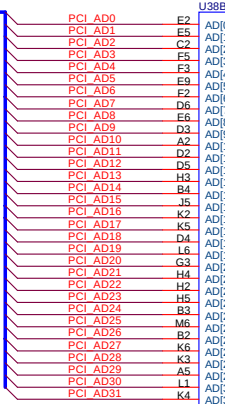


<24,26,27> PCI_AD[0..31]

<24,26,27> PCI_FRAME#

<24> PCI_PIRQA#

<24> PCI_PIRQB#



U38B

PCI

FRAME#

PIRQA#

PIROB#

PIROC#

PIRQD#

PIRQE#

PIROF#

PIROG#

PIRQH#

RESERVED

ICH6_BGA609

PLTRST#

CLK

ICH

PME#

PIRQA#

PIROB#

PIROC#

PIRQD#

PIRQE#

PIROF#

PIROG#

PIRQH#

RESERVED

ICH6_BGA609

PLTRST#

CLK

ICH

PME#

PIRQA#

PIROB#

PIROC#

PIRQD#

PIRQE#

PIROF#

PIROG#

PIRQH#

RESERVED

ICH6_BGA609

PLTRST#

CLK

ICH

PME#

PIRQA#

PIROB#

PIROC#

PIRQD#

PIRQE#

PIROF#

PIROG#

PIRQH#

RESERVED

ICH6_BGA609

PLTRST#

CLK

ICH

PME#

PIRQA#

PIROB#

PIROC#

PIRQD#

PIRQE#

PIROF#

PIROG#

PIRQH#

RESERVED

ICH6_BGA609

PLTRST#

CLK

ICH

PME#

PIRQA#

PIROB#

PIROC#

PIRQD#

PIRQE#

PIROF#

PIROG#

PIRQH#

RESERVED

ICH6_BGA609

PLTRST#

CLK

ICH

PME#

PIRQA#

PIROB#

PIROC#

PIRQD#

PIRQE#

PIROF#

PIROG#

PIRQH#

RESERVED

ICH6_BGA609

PLTRST#

CLK

ICH

PME#

PIRQA#

PIROB#

PIROC#

PIRQD#

PIRQE#

PIROF#

PIROG#

PIRQH#

RESERVED

ICH6_BGA609

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PIRQH#

RESERVED

ICH6_BGA609

PLTRST#

CLK

ICH

PME#

PIRQA#

PIROB#

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PIRQD#

PIRQE#

PIROF#

PIROG#

PIRQH#

RESERVED

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PIRQE#

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PIRQA#

PIROB#

PIROC#

PIRQD#

PIRQE#

PIROF#

PIROG#

PIRQH#

RESERVED

ICH6_BGA609

PLTRST#

CLK

ICH

PME#

PIRQA#

PIROB#

PIROC#

PIRQD#

PIRQE#

PIROF#

PIROG#

PIRQH#

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ICH6_BGA609

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CLK

ICH

PME#

PIRQA#

PIROB#

PIROC#

PIRQD#

PIRQE#

PIROF#

PIROG#

PIRQH#

RESERVED

ICH6_BGA609

PLTRST#

CLK

ICH

PME#

PIRQA#

PIROB#

PIROC#

PIRQD#

PIRQE#

PIROF#

PIROG#

PIRQH#

RESERVED

ICH6_BGA609

PLTRST#

CLK

ICH

PME#

PIRQA#

PIROB#

PIROC#

PIRQD#

PIRQE#

PIROF#

PIROG#

PIRQH#

RESERVED

ICH6_BGA609

PLTRST#

CLK

ICH

PME#

PIRQA#

PIROB#

PIROC#

PIRQD#

PIRQE#

PIROF#

PIROG#

PIRQH#

RESERVED

ICH6_BGA609

PLTRST#

CLK

ICH

PME#

PIRQA#

PIROB#

PIROC#

PIRQD#

PIRQE#

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PLTRST#

CLK

ICH

PME#

PIRQA#

PIROB#

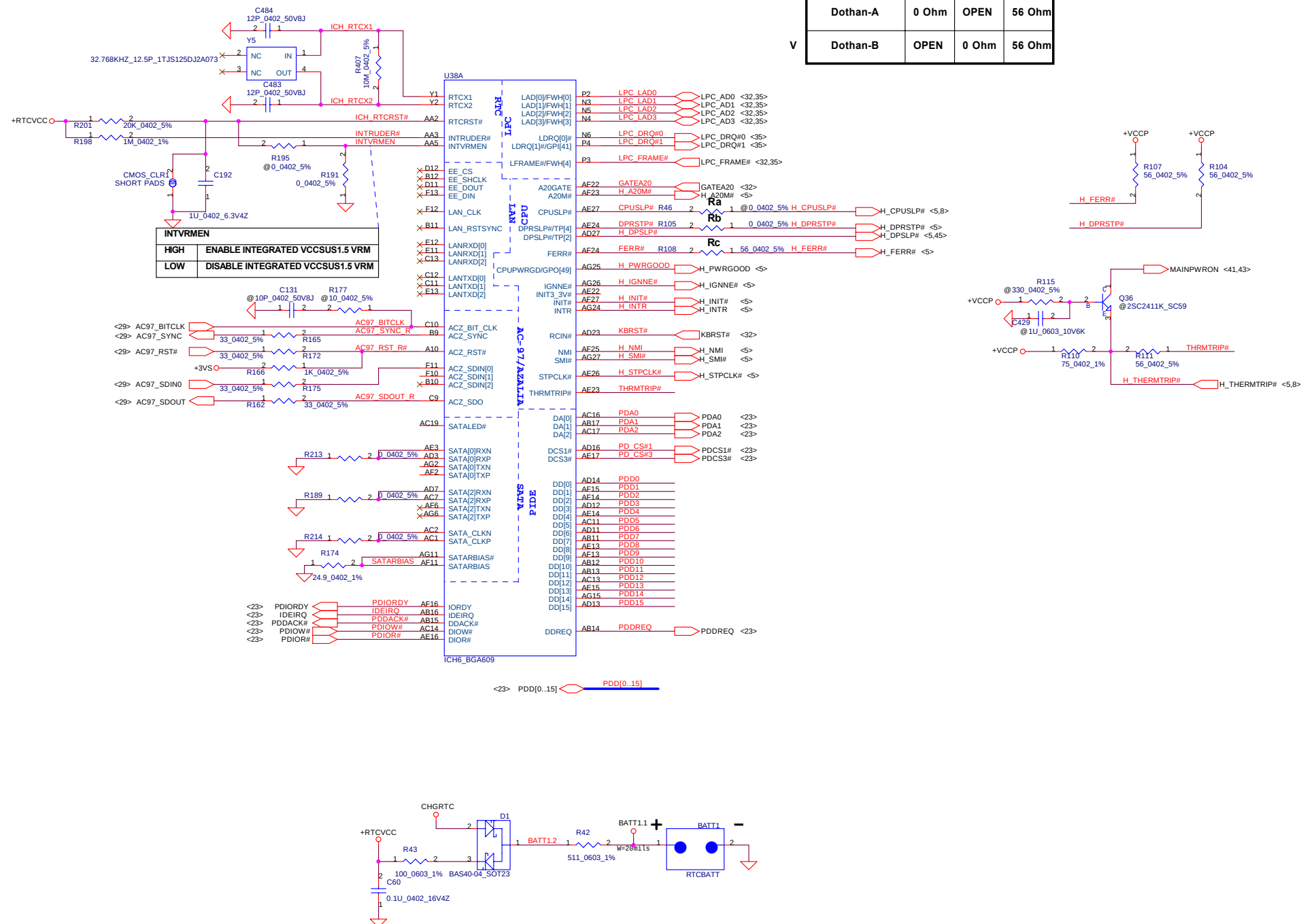
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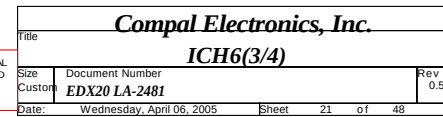
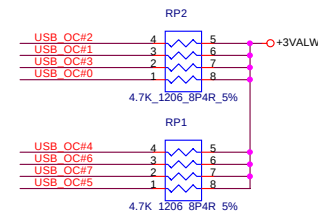
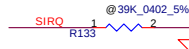
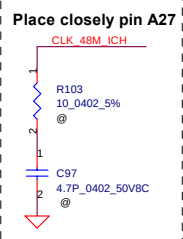
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CPU Type	Ra	Rb	Rc
Dothan-A	0 Ohm	OPEN	56 Ohm
Dothan-B	OPEN	0 Ohm	56 Ohm

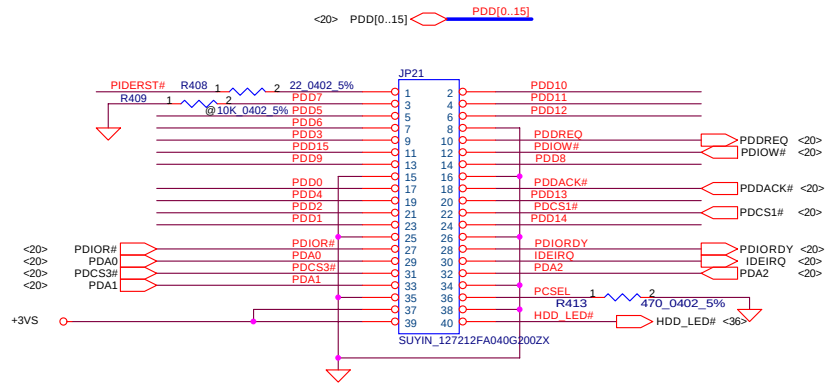
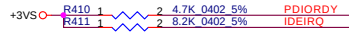
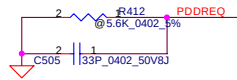
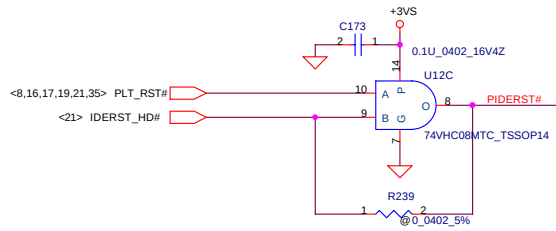


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ICH6(2/4)			
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Custor			
Date:	Wednesday, April 06, 2005	Sheet	20 of 48

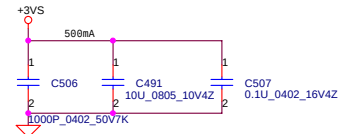


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Place caps. near HDD
CONN.

Layout Note: +VPHDD trace
width 60 mil



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Title			
ICH4-M PIDE&ICH Pull-up			
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Place C877, C878
Close to U9 Bottom Side

Place C871, C872
Close to C694

Reserved layout
for debug used.

Reserved layout
for debug used.

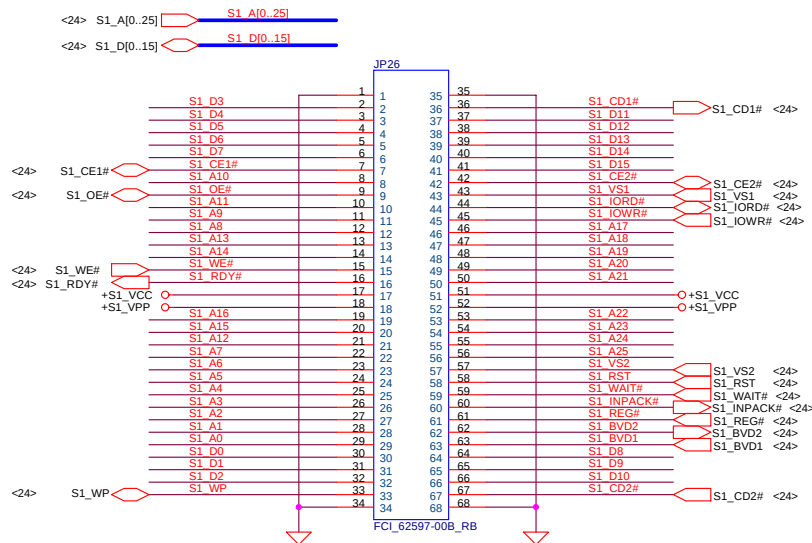
Close to CB712
CD1# and CD2#

CARDBUS

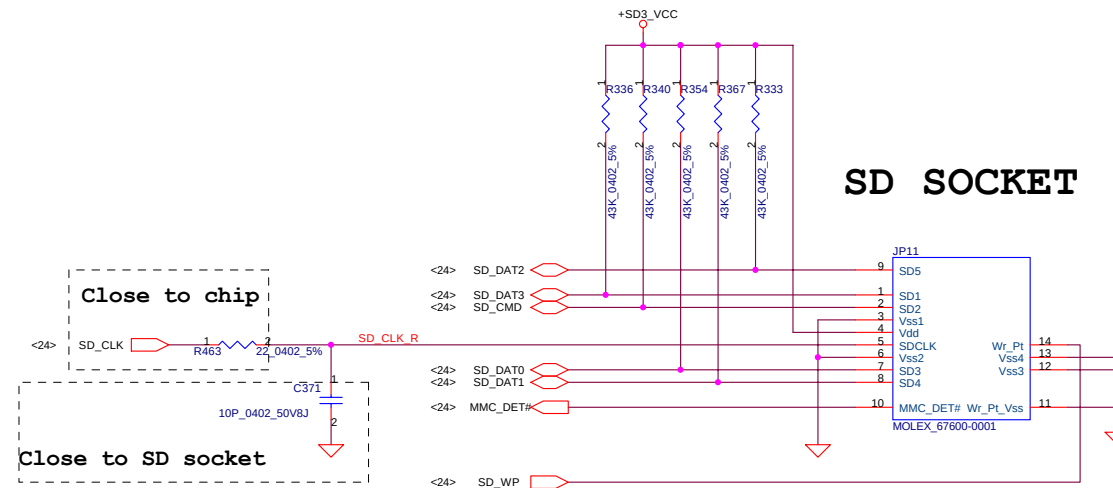
SD

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CARDBUS SOCKET



SD SOCKET



PCMCIA Power Controller

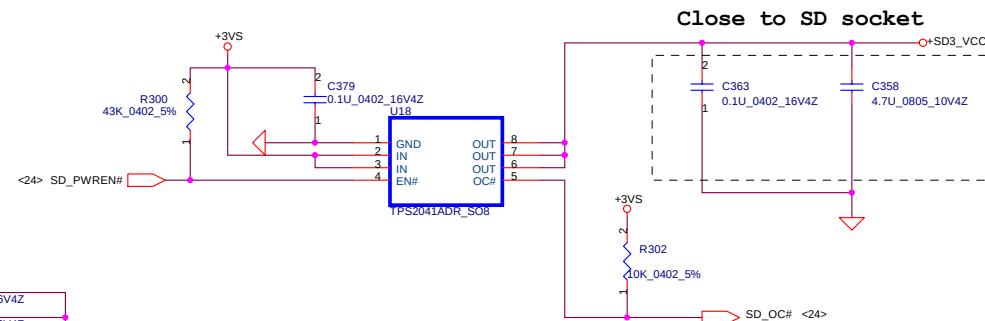
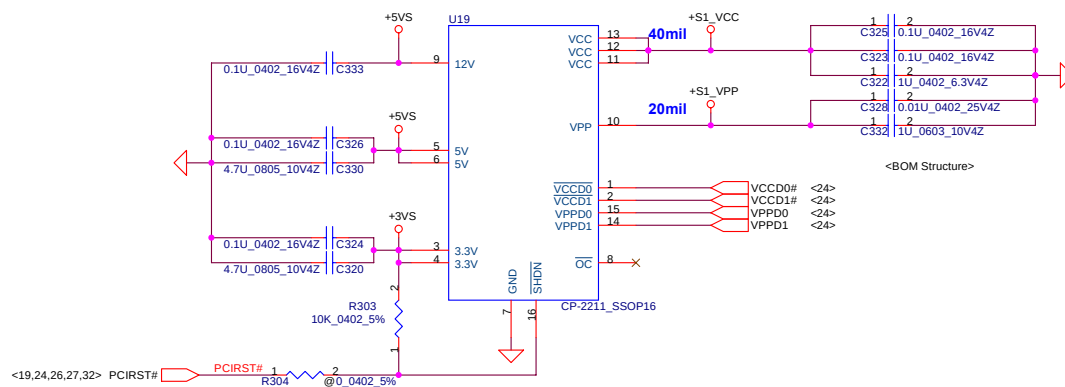
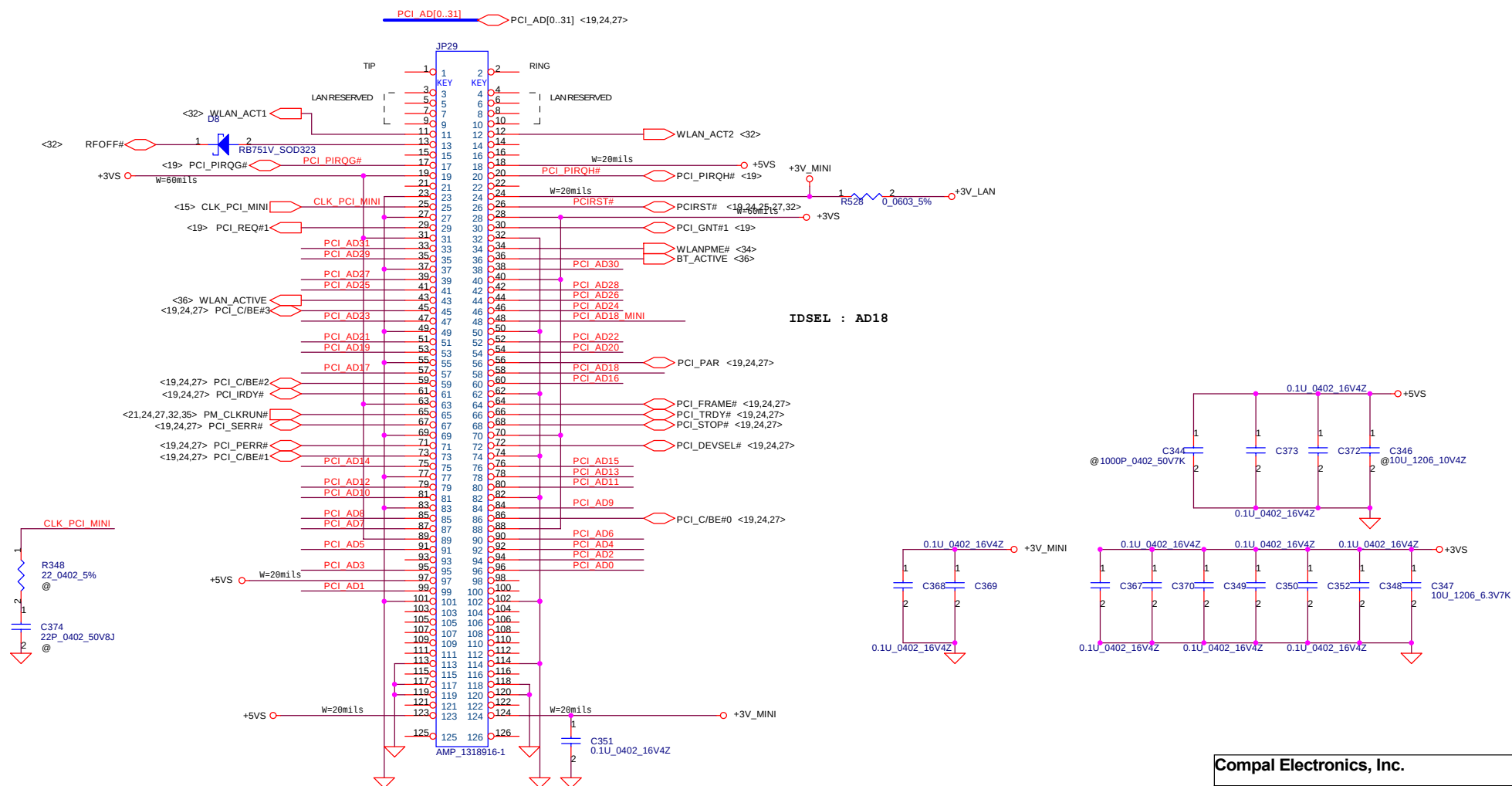
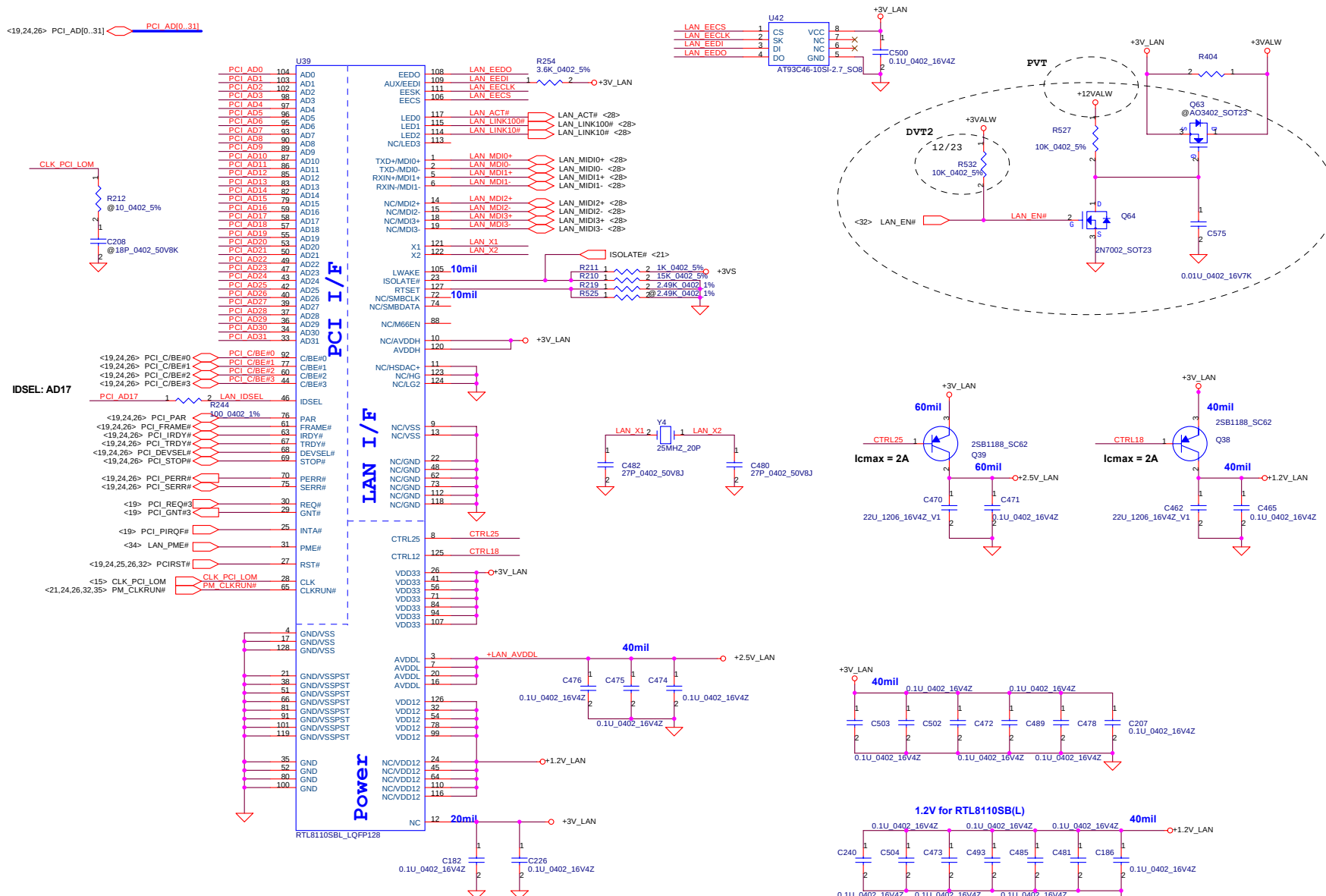


Diagram illustrating the MMBT3904 SOT23 transistor circuit. The circuit includes a base resistor (R521, 100k), a collector resistor (R524, 1k), and an emitter resistor (R523, 5%). The input signal is WL_EN# and the output signal is PCI AD18_MINI.

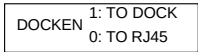
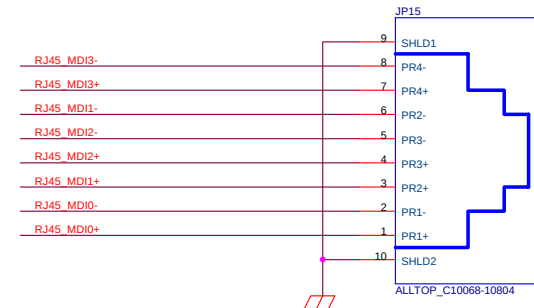


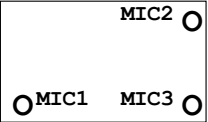
Compal Electronics, Inc.			
Title MiniPCI TYPEIII Slot			
Size	Document Number EDX20 LA-2481		Rev 0.5
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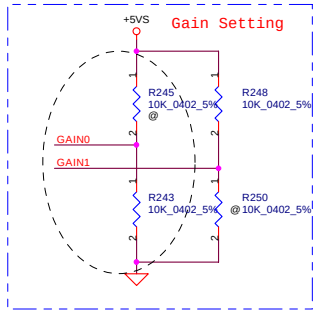
LAN RTL8110SB(L)



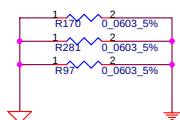
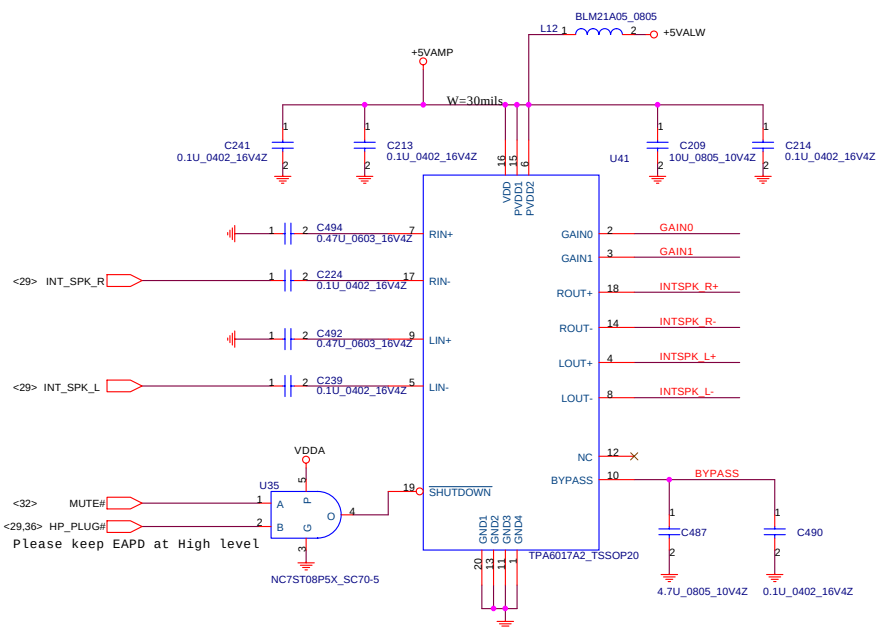
The diagram illustrates the LAN interface connections. It shows four RJ45 ports: LAN MDIO+, LAN MDIO-, LAN MDIO1+, and LAN MDIO1-. Each port is connected to a specific pin (C203, C204, C205, C206) and a corresponding resistor (R202, R203, R204, R205, R206, R207, R208, R209) and a 49.9_0402_1% resistor.

[illegible]

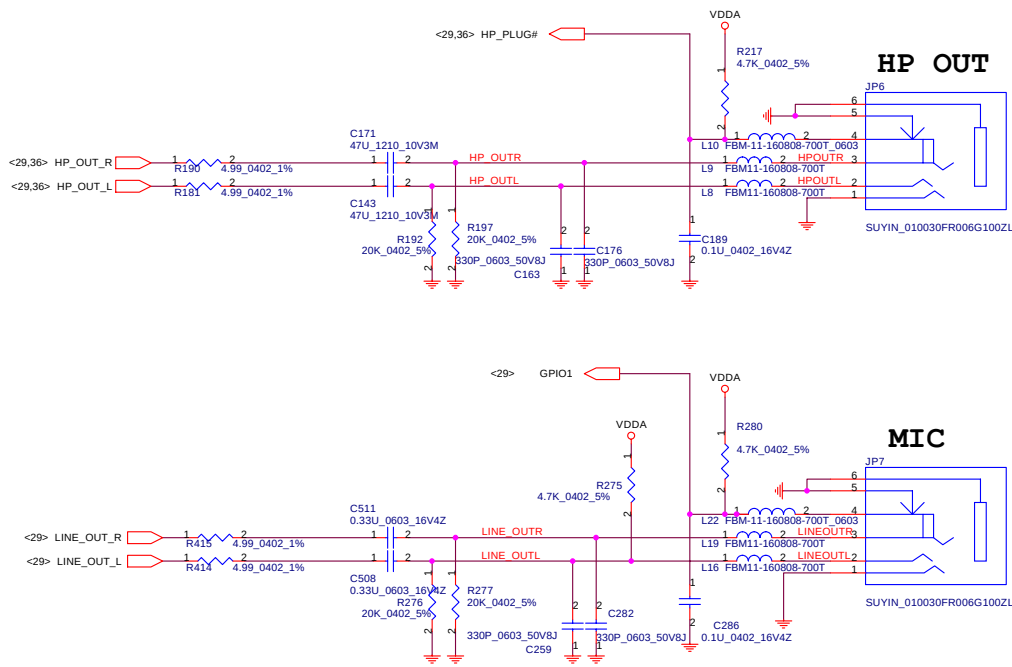
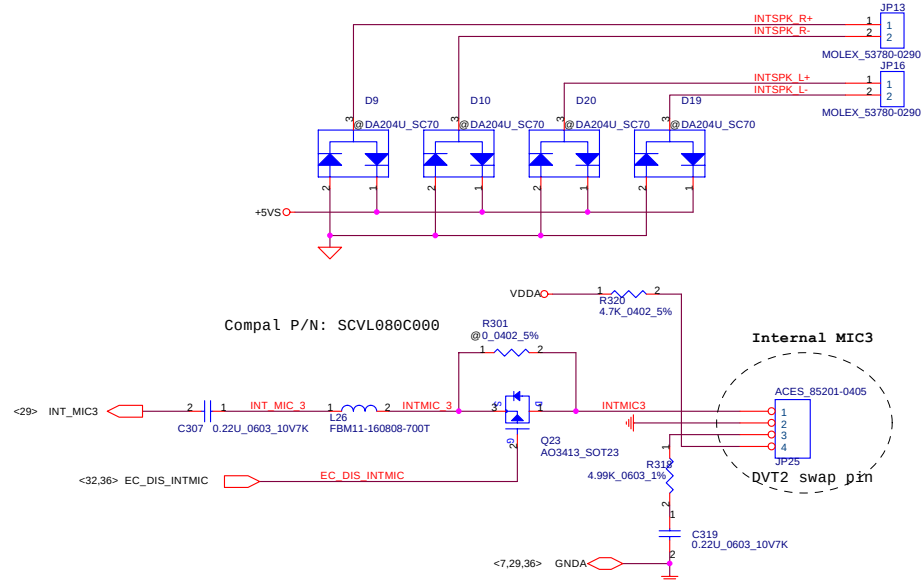




GAIN0	GAIN1	AV(inv)	INPUT IMPEDANCE
0	0	6dB	90K ohm
0	1	10dB	70K ohm
1	0	15.6dB	45K ohm
1	1	21.6dB	25K ohm



Compal P/N: SCVL080C000



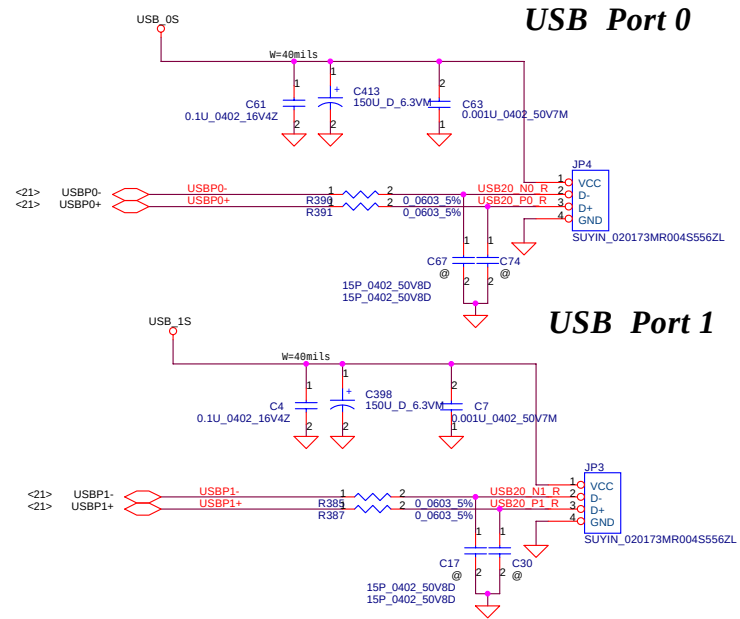
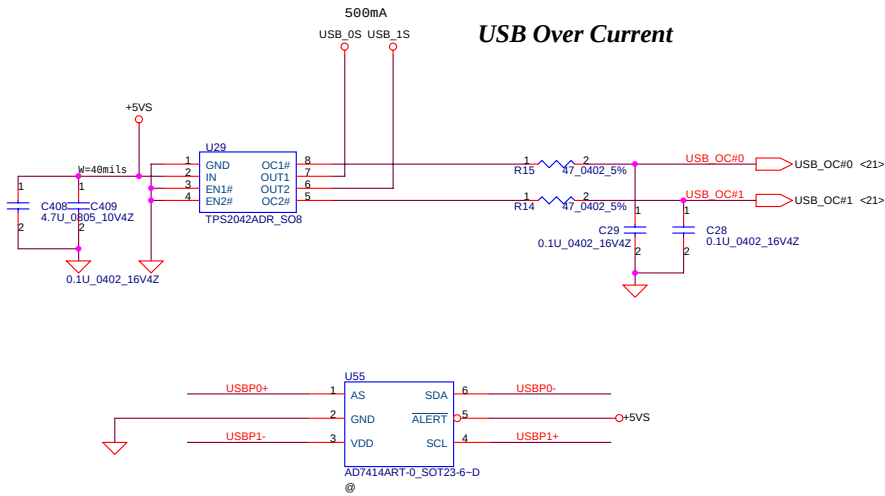
Compal Electronics, Inc.

AMP & Audio Jack

Document Number
EDX20 LA-2481

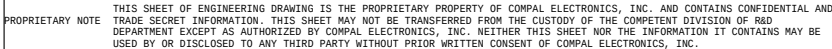
Date: Wednesday, April 06, 2005 Sheet 30 of 48

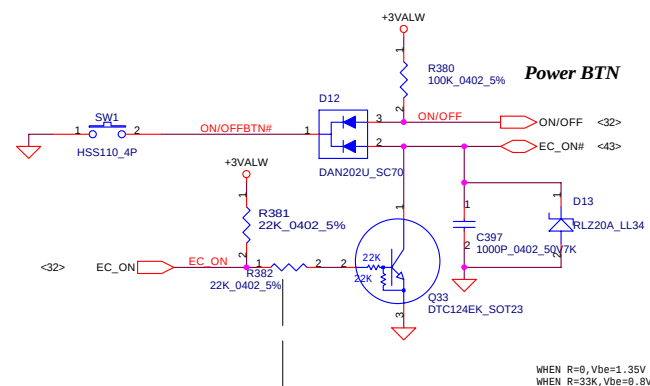
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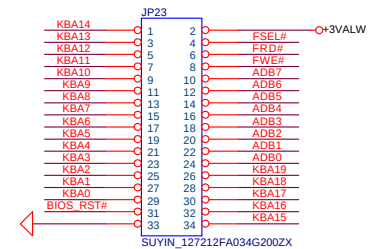
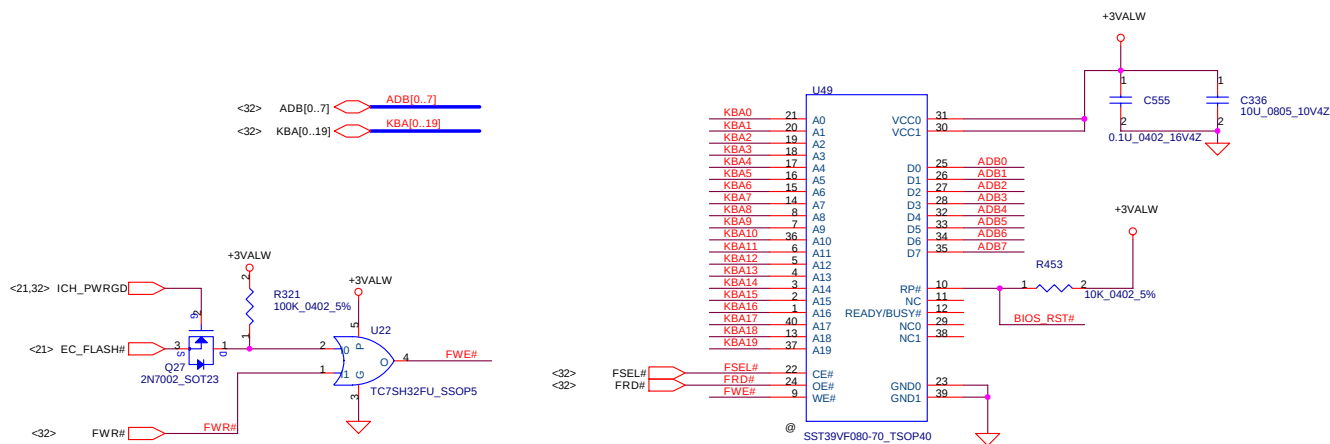
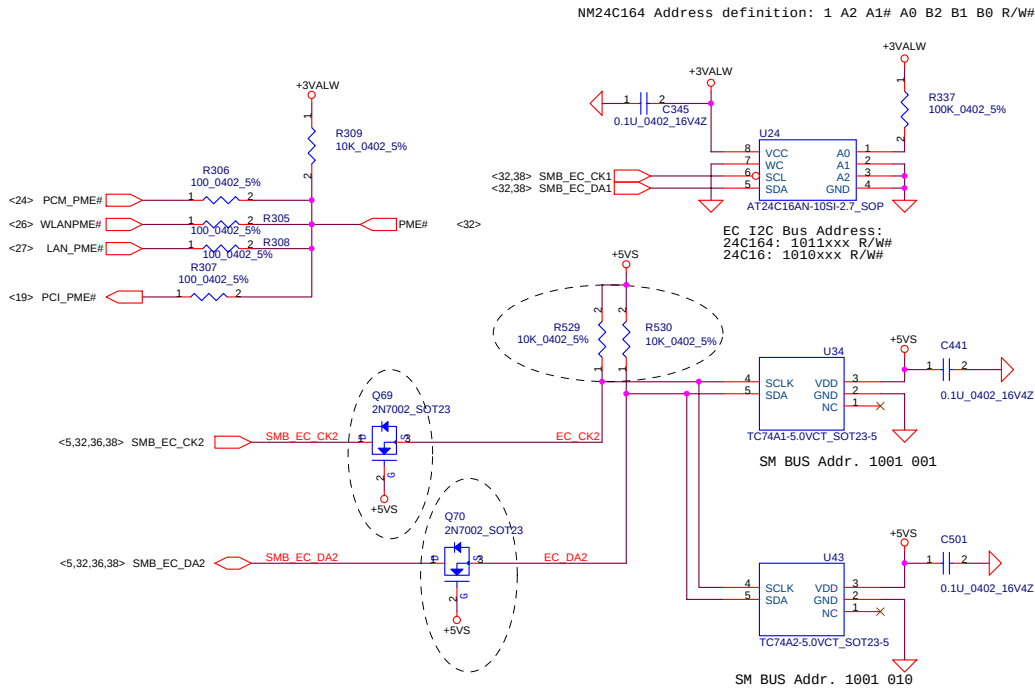
Compal Electronics, Inc.			
Title			
USB Port x2			
Size	Document Number	Rev	
	EDX20 LA-2481	0.5	
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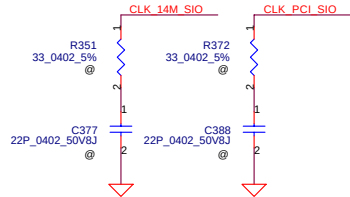
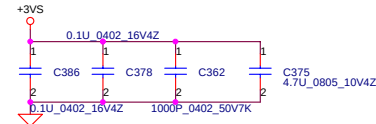
AND E	Title			Power ON/OFF SW		
	Size	Document Number				Rev
		EDX20 LA-2481				0.5
	Date:	Wednesday, April 06, 2005		Sheet	33	of 48

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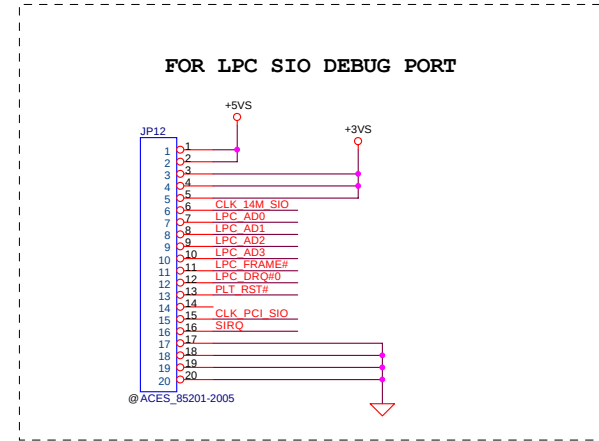
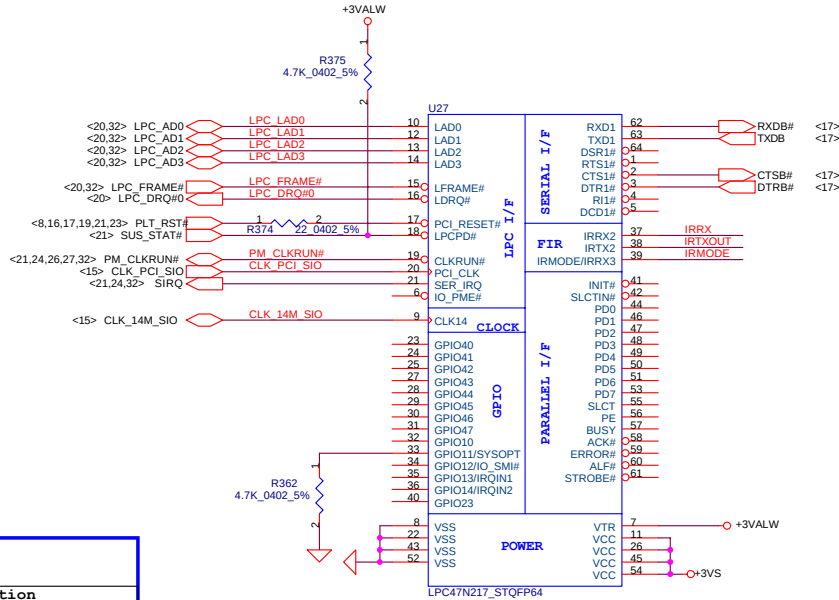


Compal Electronics, Inc.			
Title			
Thermal Sensor & BIOS			
Size	Document Number		Rev
	EDX20 LA-2481		0.5
Date:	Wednesday, April 06, 2005	Sheet	34 of 48

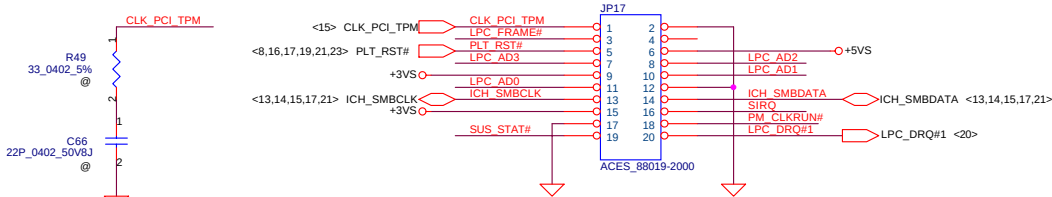
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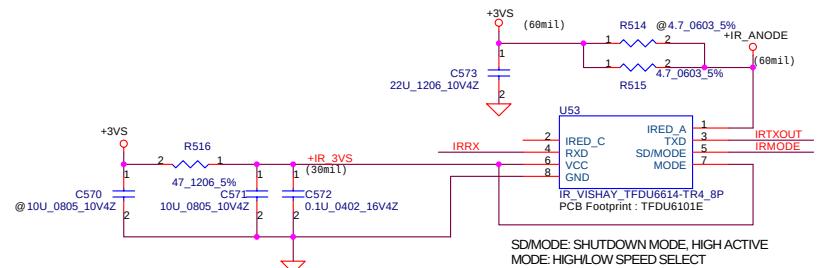
Strap pin	Pin #	Description
BADDR	33	BASE Address Selection "0": 2E~2F (Default) "1": 4E~4F



TPM

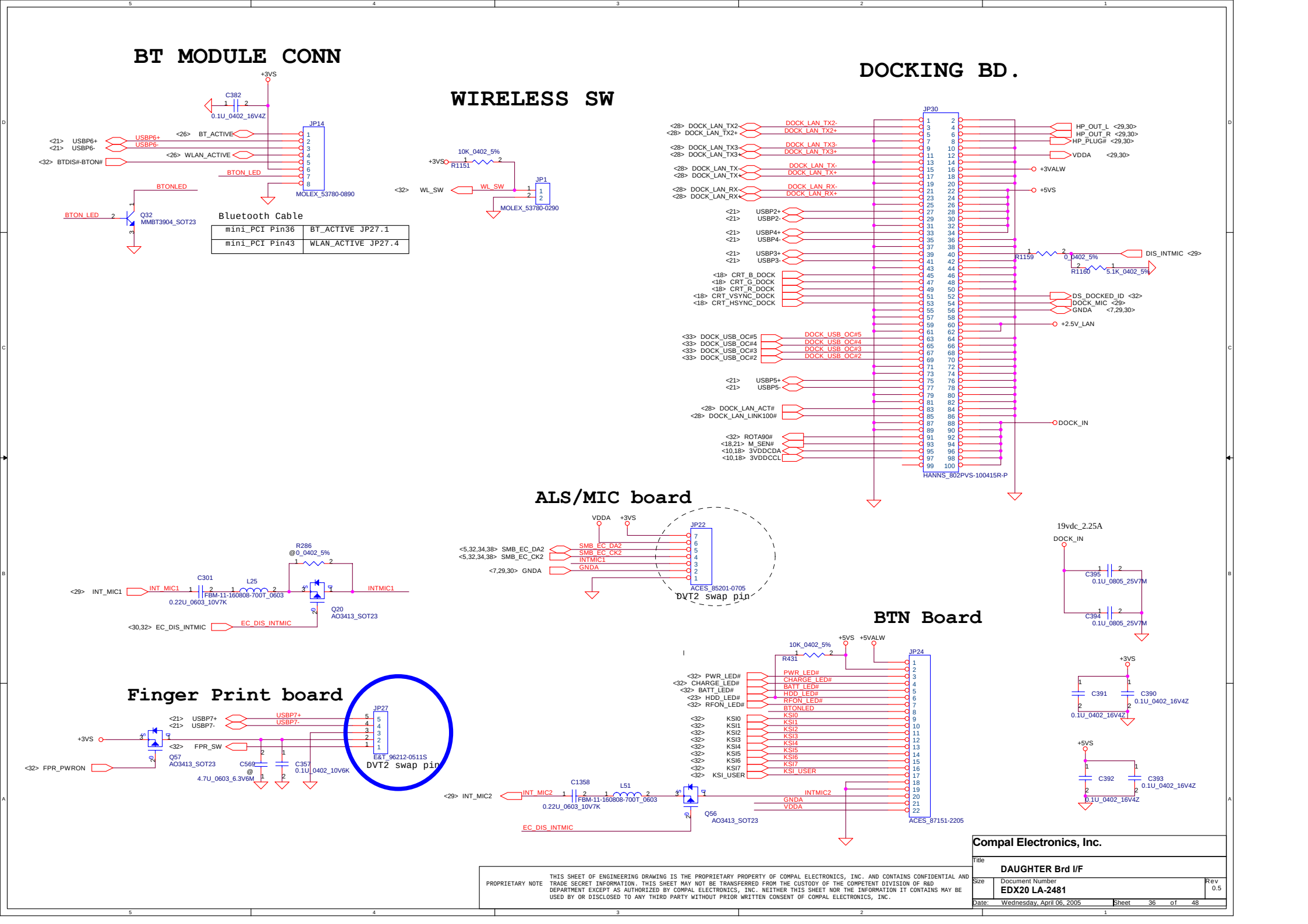
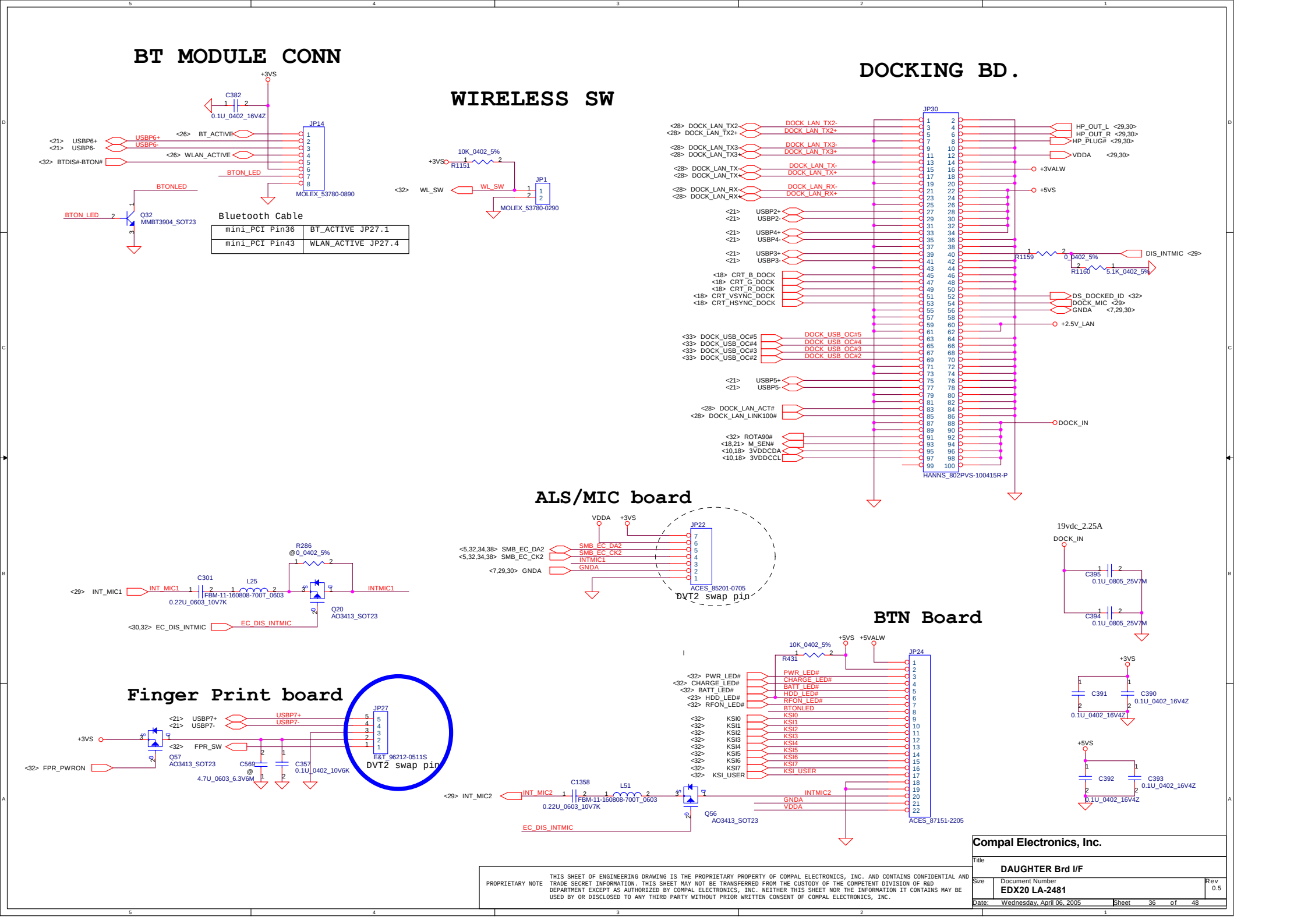
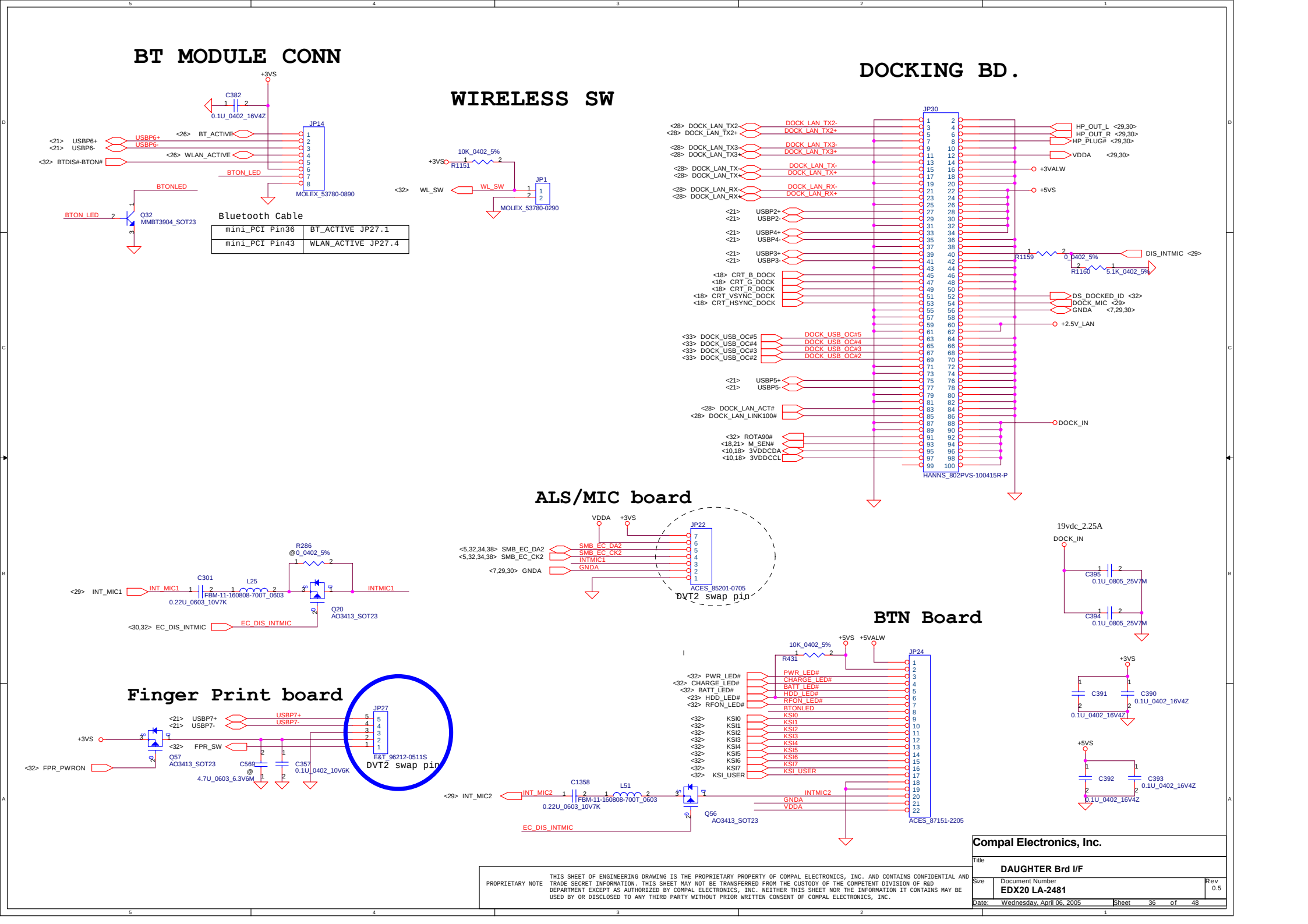
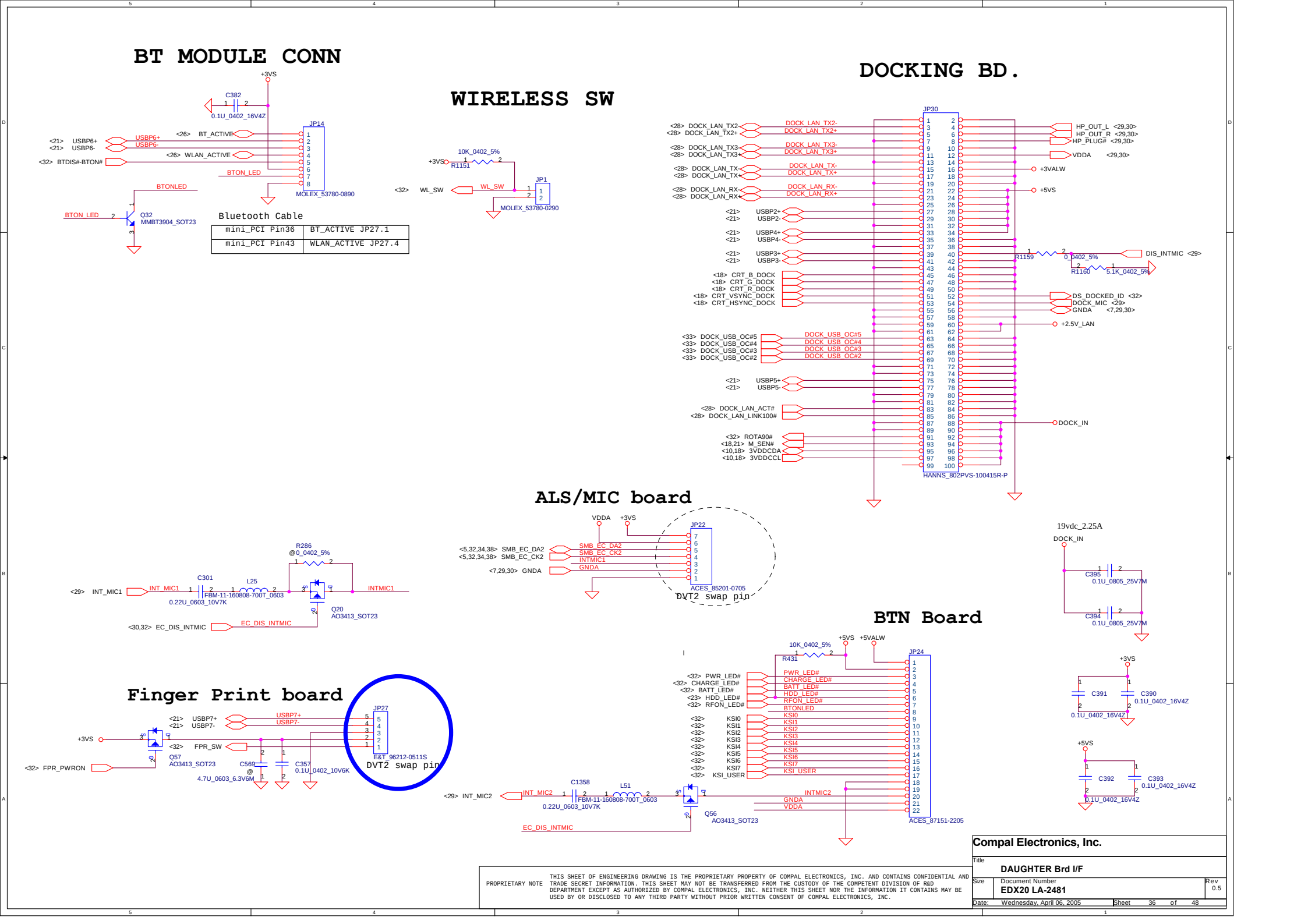
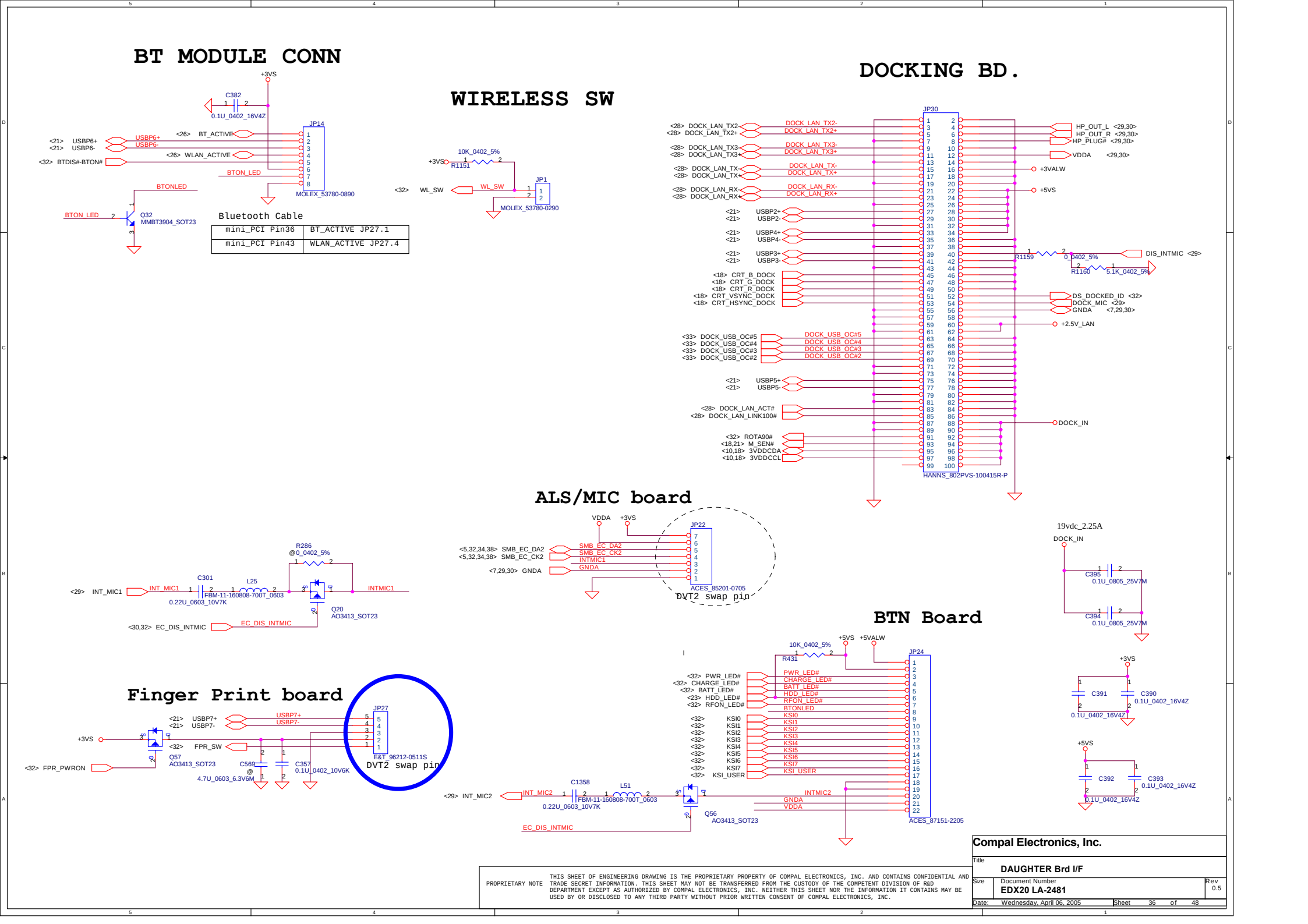


FIR



Compal Electronics, Inc.			
Title			
Super I/O LPC47N217/TPM/FIR			
Size	Document Number	Rev	
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[illegible][illegible][illegible][illegible][illegible][illegible][illegible]

BT MODULE CONN

Circuit diagram showing connections for a Bluetooth module. Key components include USBP6+, BT_ACTIVE, WLAN_ACTIVE, BTON_LED, Q32 MMBT3904_SOT23, C382 0.1U_0402_16V4Z, JP14 MOLEX_53780-0890, and a Bluetooth Cable connector.

mini_PCI Pin36	BT_ACTIVE JP27.1
mini_PCI Pin43	WLAN_ACTIVE JP27.4

WIRELESS SW

Circuit diagram showing connections for a wireless switch. Key components include WL_SW, R1151 10K_0402_5%, JP1 MOLEX_53780-0290, and +3VS.

DOCKING BD.

Detailed circuit diagram of the docking board. It shows numerous connectors (JP30, JP22, JP24) and their pin configurations. Key signals include DOCK_LAN_TX2, DOCK_LAN_TX3, DOCK_LAN_RX, DOCK_USB_OC#5, DOCK_USB_OC#4, DOCK_USB_OC#3, DOCK_USB_OC#2, DOCK_LAN_ACT#, DOCK_LAN_LINK100#, ROTAS90#, M_SEN#, 3VDDCCDA, 3VDDCCL, INT_MIC1, INT_MIC2, EC_DIS_INTMIC, GND_A, VDDA, +3VS, +5VALW, HP_OUT_L, HP_OUT_R, HP_PLUG#, DIS_INTMIC, DS_DOCKED_ID, DOCK_MIC, and +2.5V_LAN.

ALS/MIC board

Circuit diagram showing connections for an ALS/MIC board. Key components include SMB_EC_DA2, SMB_EC_CR2, INTMIC1, GND_A, ACES_85201-0705, DVT2 swap pin, VDDA, +3VS, R286 @ 0.0402_5%, L25 FBM-11-160808-700T_0603, C301 0.22U_0603_10V7K, Q20 AO3413_SOT23, and EC_DIS_INTMIC.

BTN Board

Circuit diagram showing connections for a button board. Key components include PWR_LED#, CHARGE_LED#, BATT_LED#, HDD_LED#, RFON_LED#, BTONLED, KSIO, KSI1, KSI2, KSI3, KSI4, KSI5, KSI6, KSI7, KSI_USER, INT_MIC2, EC_DIS_INTMIC, GND_A, VDDA, +5VALW, +3VS, C395 0.1U_0805_25V7M, C394 0.1U_0805_25V7M, C391 0.1U_0402_16V4Z, C390 0.1U_0402_16V4Z, C392 0.1U_0402_16V4Z, C393 0.1U_0402_16V4Z, and Q56 AO3413_SOT23.

Finger Print board

Circuit diagram showing connections for a finger print board. Key components include USBP7+, USBP7-, FPR_SW, FPR_PWRON, Q57 AO3413_SOT23, C560 @ 4.7U_0603_6.3V6M, C357 0.1U_0402_10V6K, E&T 96212-0511S, DVT2 swap pin, and JP27.

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Title	Size	Date
DAUGHTER Brd I/F	Document Number EDX20 LA-2481	Wednesday, April 06, 2005

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BT MODULE CONN

3VS

C382 0.1U_0402_16V4Z

JP14

MOLEX_53780-0890

BT_ACTIVE JP27.1

WLAN_ACTIVE JP27.4

BTON_LED

BTONLED

Q32 MMBT3904_SOT23

Bluetooth Cable

mini_PCI Pin36	BT_ACTIVE JP27.1
mini_PCI Pin43	WLAN_ACTIVE JP27.4

WIRELESS SW

3VS

10K_0402_5%

R1151

WL_SW

JP1

MOLEX_53780-0290

DOCKING BD.

JP30

<28> DOCK_LAN_TX2 DOCK_LAN_TX2+

<28> DOCK_LAN_TX2 DOCK_LAN_TX2+

<28> DOCK_LAN_TX3 DOCK_LAN_TX3+

<28> DOCK_LAN_TX3 DOCK_LAN_TX3+

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<28> DOCK_LAN_TX DOCK_LAN_TX+

<28> DOCK_LAN_RX DOCK_LAN_RX+

<28> DOCK_LAN_RX DOCK_LAN_RX+

<21> USBP2+ USBP2+

<21> USBP2- USBP2-

<21> USBP4+ USBP4+

<21> USBP4- USBP4-

<21> USBP3+ USBP3+

<21> USBP3- USBP3-

<18> CRT_B DOCK

<18> CRT_G DOCK

<18> CRT_R DOCK

<18> CRT_VSYNC DOCK

<18> CRT_HSYNC DOCK

<33> DOCK_USB_OC#5 DOCK_USB_OC#5

<33> DOCK_USB_OC#4 DOCK_USB_OC#4

<33> DOCK_USB_OC#3 DOCK_USB_OC#3

<33> DOCK_USB_OC#2 DOCK_USB_OC#2

<21> USBP5+ USBP5+

<21> USBP5- USBP5-

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<18,21> M_SEW#

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<10,18> 3VDDCCL

HANNIS_802PVS-100415R-P

HP_OUT_L <29,30>

HP_OUT_R <29,30>

HP_PLUG# <29,30>

VDDA <29,30>

+3VALW

+5VS

DIS_INTMIC <29>

DS_DOCKED_ID <32>

DOCK_MIC <29>

GND

+2.5V_LAN

DOCK_IN

ALS/MIC board

VDDA

3VS

JP22

SMB_EC_DA2

SMB_EC_CR2

INTMIC1

GND

ACES_85201-0705

DVT2 swap pin

BTN Board

19vdc_2.25A

DOCK_IN

C395 0.1U_0805_25V7M

C394 0.1U_0805_25V7M

3VS

C391 0.1U_0402_16V4Z

C390 0.1U_0402_16V4Z

5VS

C392 0.1U_0402_16V4Z

C393 0.1U_0402_16V4Z

Finger Print board

3VS

JP27

USBP7+ USBP7+

USBP7- USBP7-

FPR_SW

Q57 AO3413_SOT23

C560 4.7U_0603_6.3V6M

C357 0.1U_0402_10V6K

E&T_96212-0511S

DVT2 swap pin

INT_MIC2

C1358 0.22U_0603_10V7K

L51

Q56 AO3413_SOT23

EC_DIS_INTMIC

Compal Electronics, Inc.

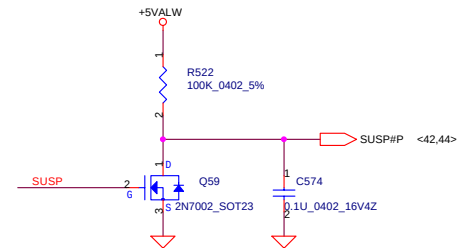
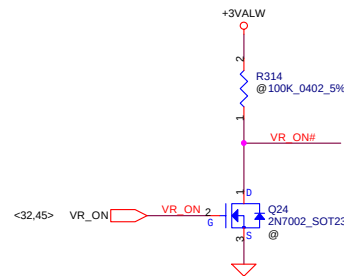
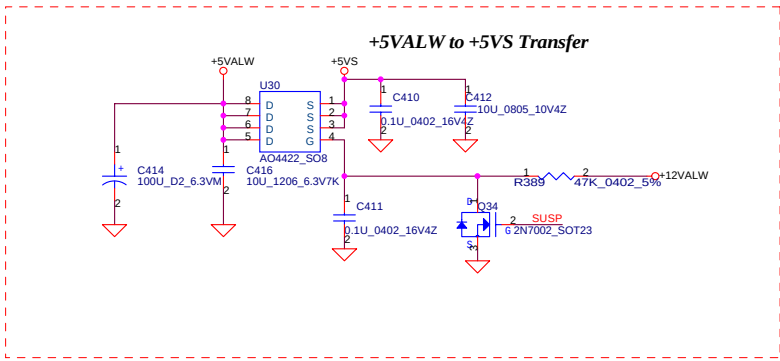
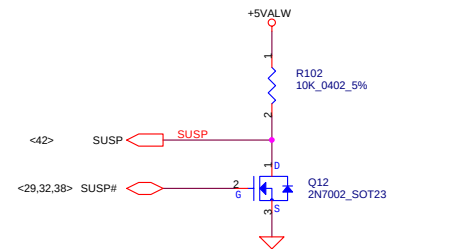
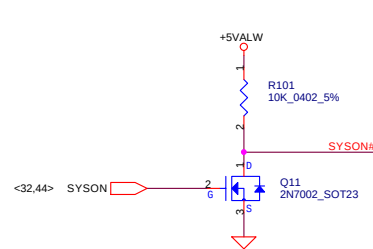
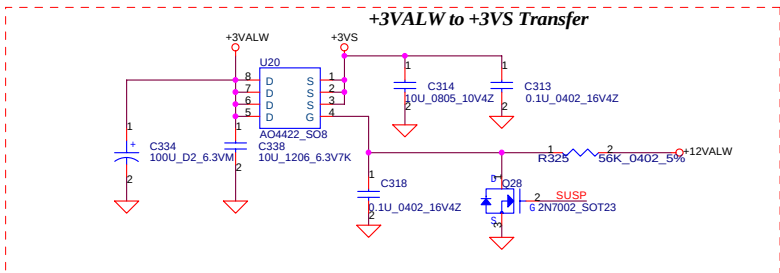
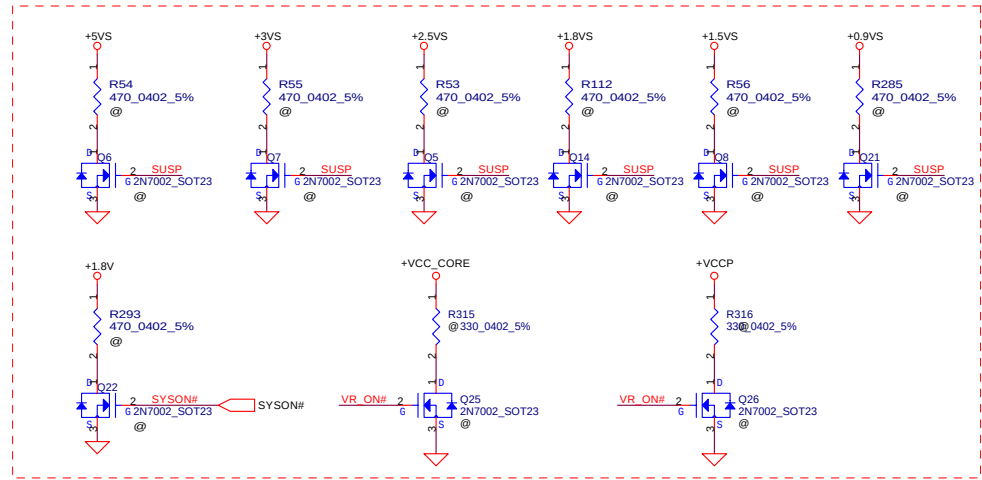
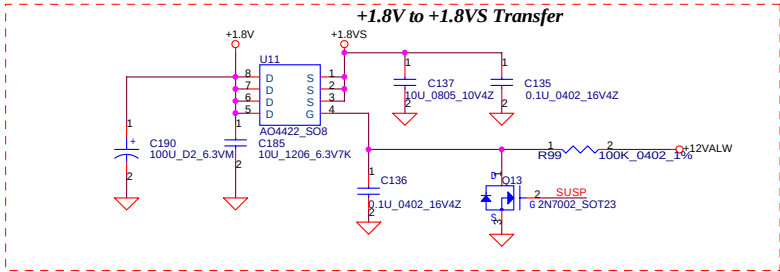
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Size Document Number EDX20 LA-2481

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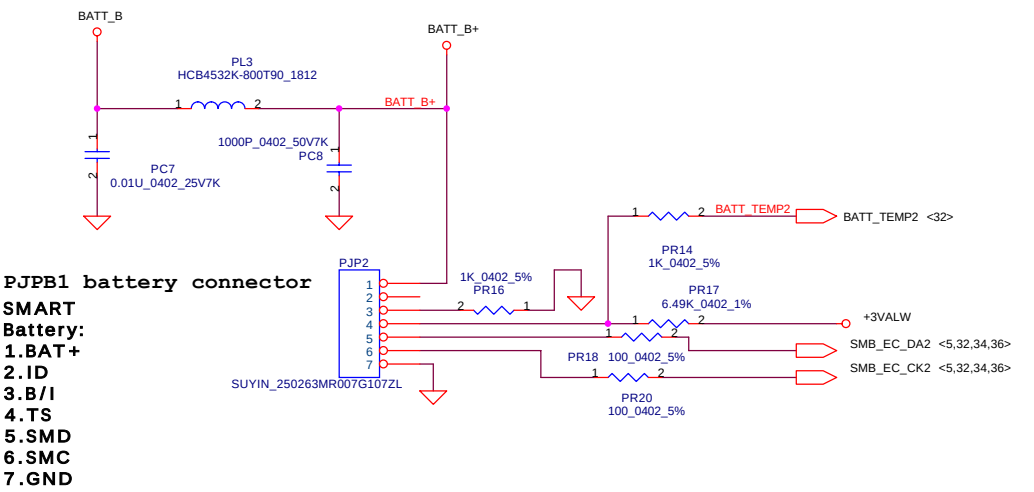
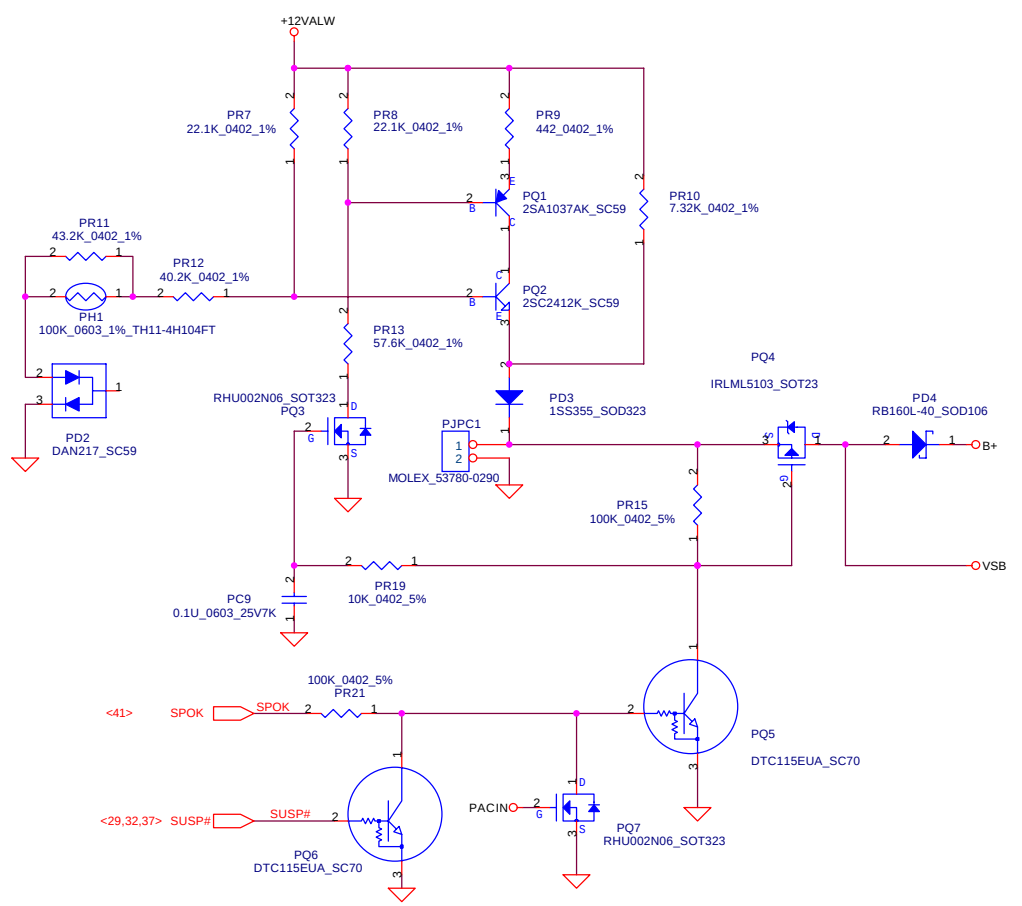
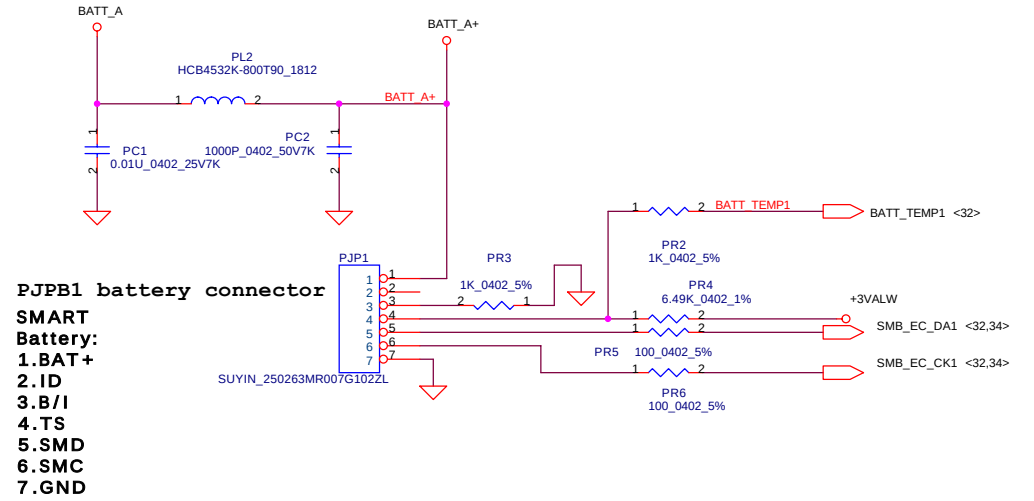
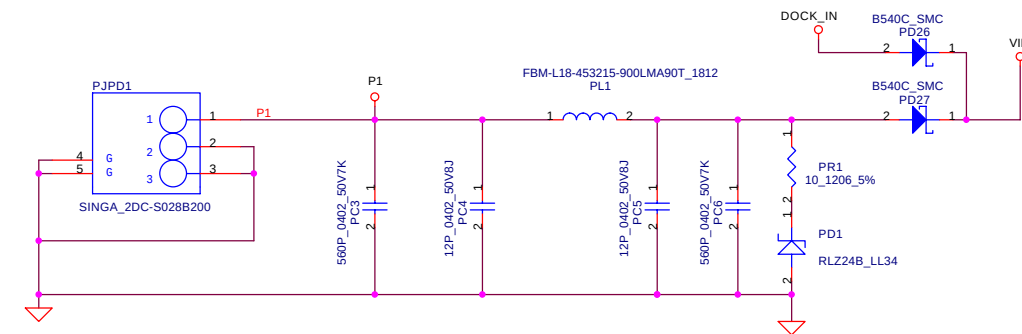
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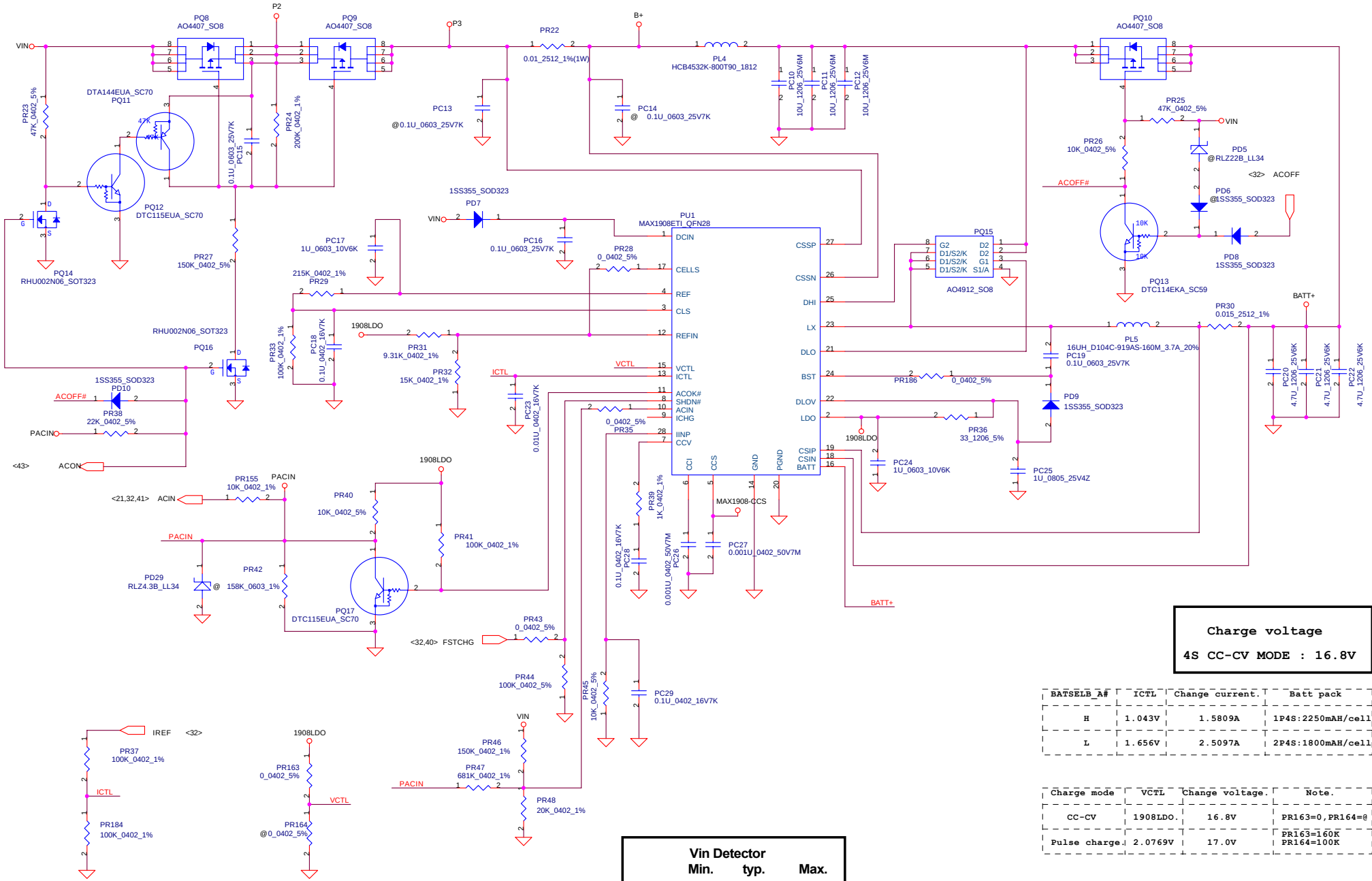
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Title			
DC/DC Interface			
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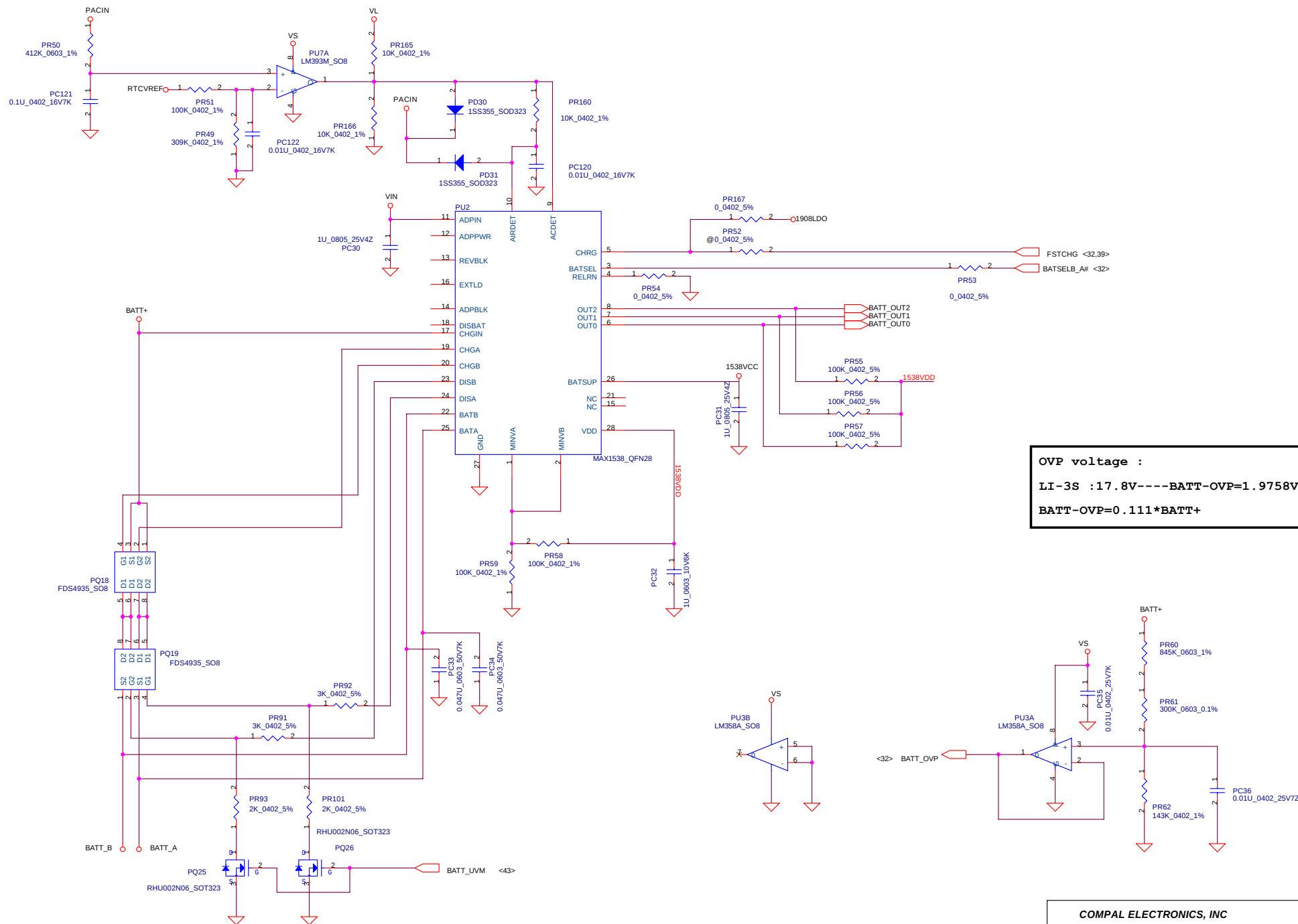
COMPAL ELECTRONICS, INC			
Title			
Bridge batt & Batt conn & DC in			
Size	Document Number	Rev	
	EDX20 LA-2481	0.5	
Date: Wednesday, April 06, 2005		Sheet	38 of 48

I_{adp}=0~2.25A (42.75W)

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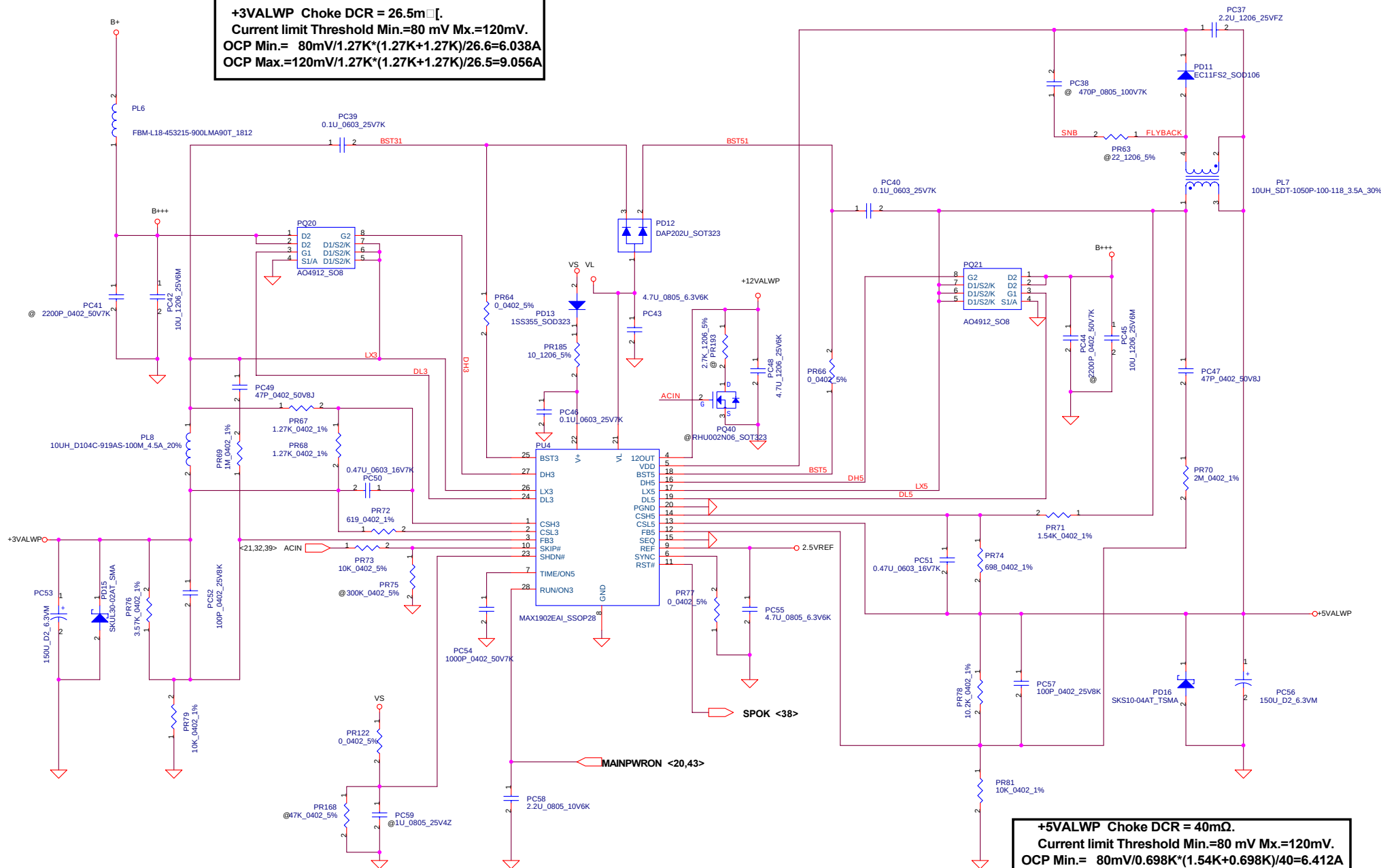
COMPAL ELECTRONICS, INC

AND E	Title			
	Charger			
	Size	Document Number		Rev
	EDX20 LA-2481			0.5
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+3.3V/+5V/+12V

+3VALWP Choke DCR = 26.5mΩ.
Current limit Threshold Min.=80 mV Mx.=120mV.
OCP Min.= 80mV/1.27K*(1.27K+1.27K)/26.6=6.038A
OCP Max.=120mV/1.27K*(1.27K+1.27K)/26.5=9.056A



$$RS2(PR64)=RS1(PR58)*RS3(PR61)/(RS1+RS3)$$
$$L/RL(DCR)=RS1*RS3(PR61)/(RS1+RS3)*Cs(PC56)$$

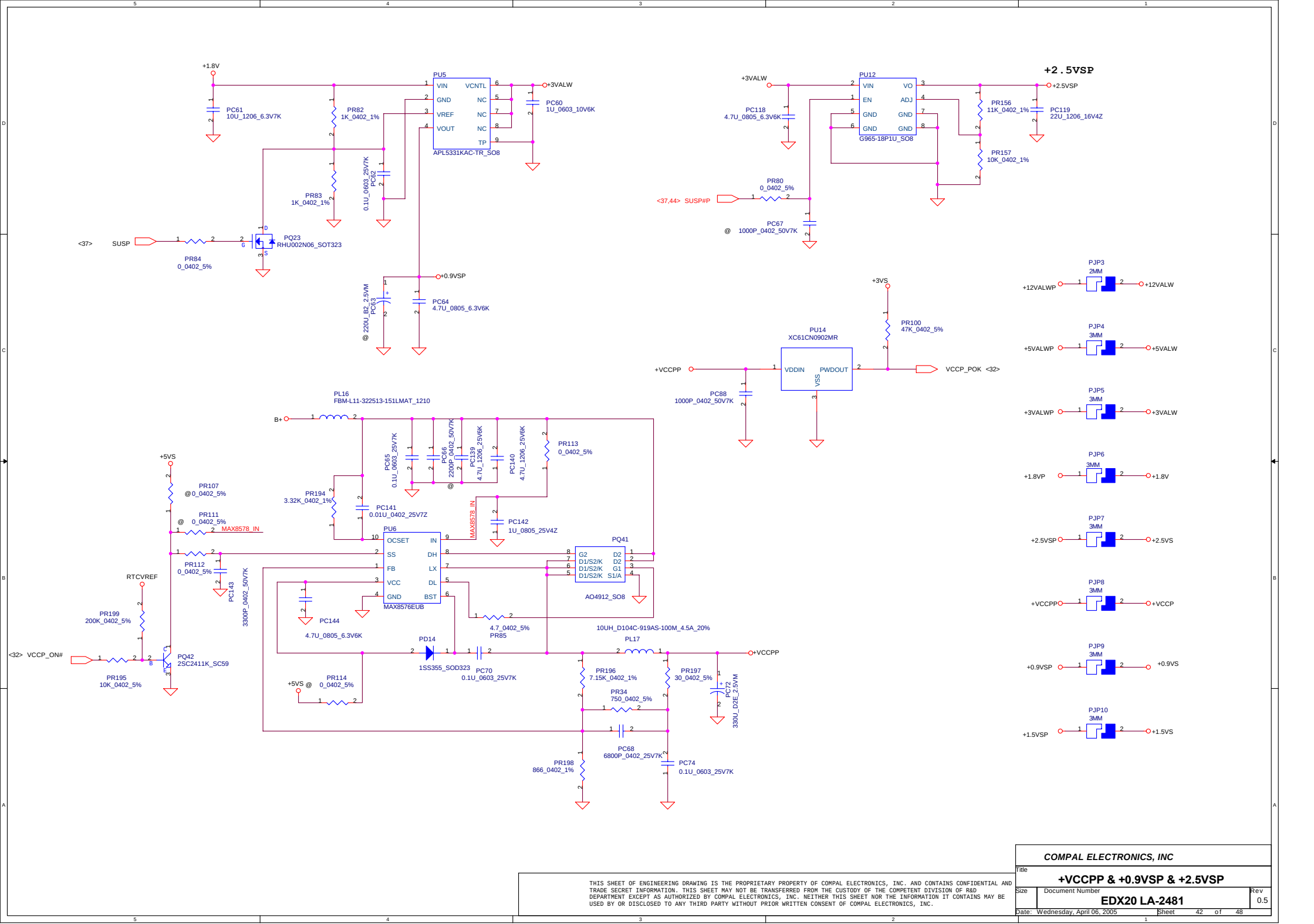
+5VALWP Choke DCR = 40mΩ.
Current limit Threshold Min.=80 mV Mx.=120mV.
OCP Min.= 80mV/0.698K*(1.54K+0.698K)/40=6.412A
OCP Max.=120mV/0.698K*(0.698K+1.54K)/40=9.593A

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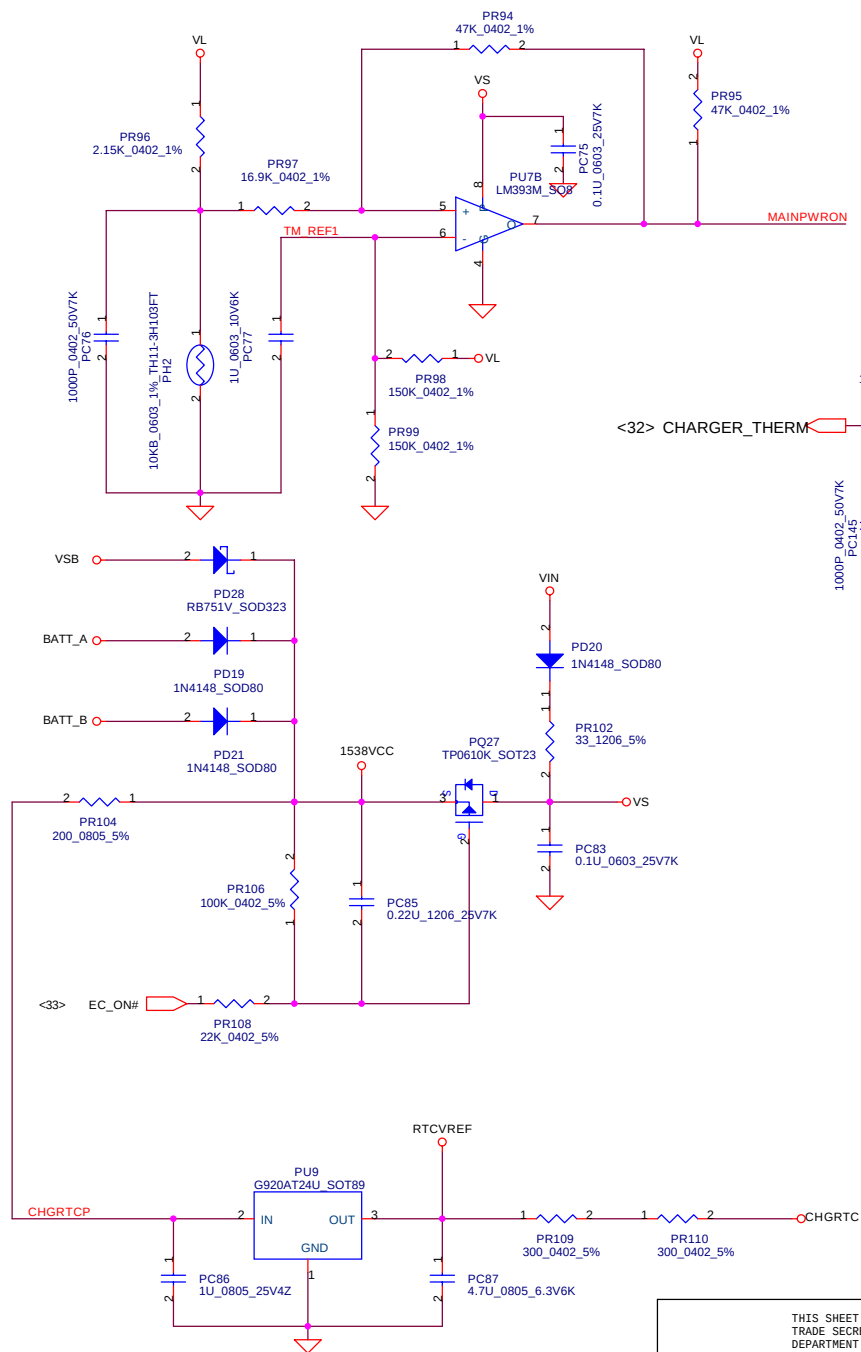
+3VALWP & +5VALWP & +12VALWP

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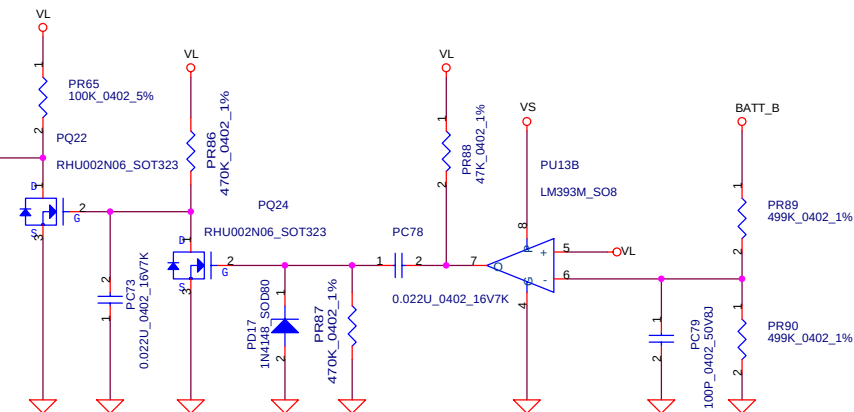
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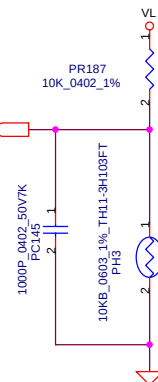
PH2 under CPU botten side :
CPU thermal protection at 80 degree C
Recovery at 44(45) degree C



<40> BATT_UVM



<32> CHARGER_THERM



<20,41> MAINPWON

<39> ACON

ACIN

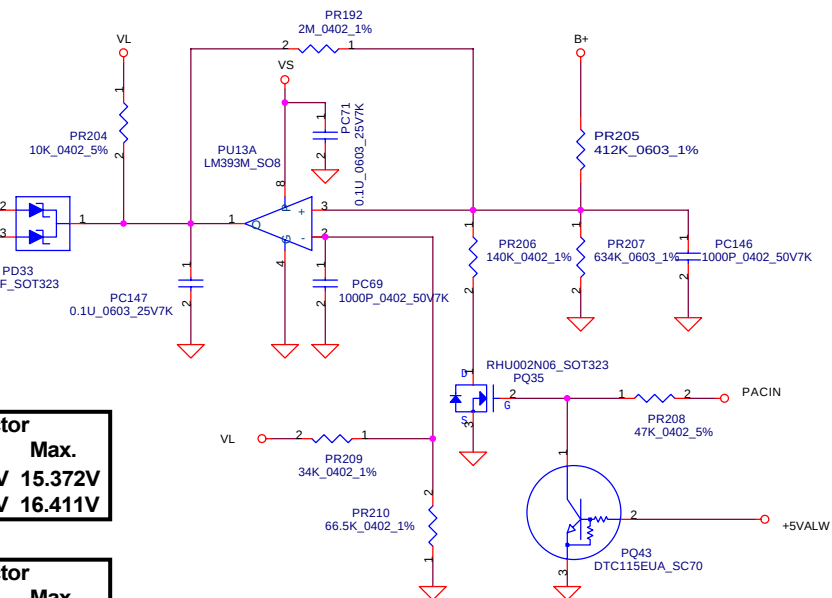
Precharge detector

	Min.	typ.	Max.
H->L	14.556V	14.807V	15.372V
L->H	15.276V	15.836V	16.411V

BATT ONLY

Precharge detector

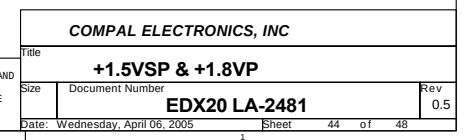
	Min.	typ.	Max.
H->L	5.044V	5.096V	5.205V
L->H	6.008V	6.124V	6.243V



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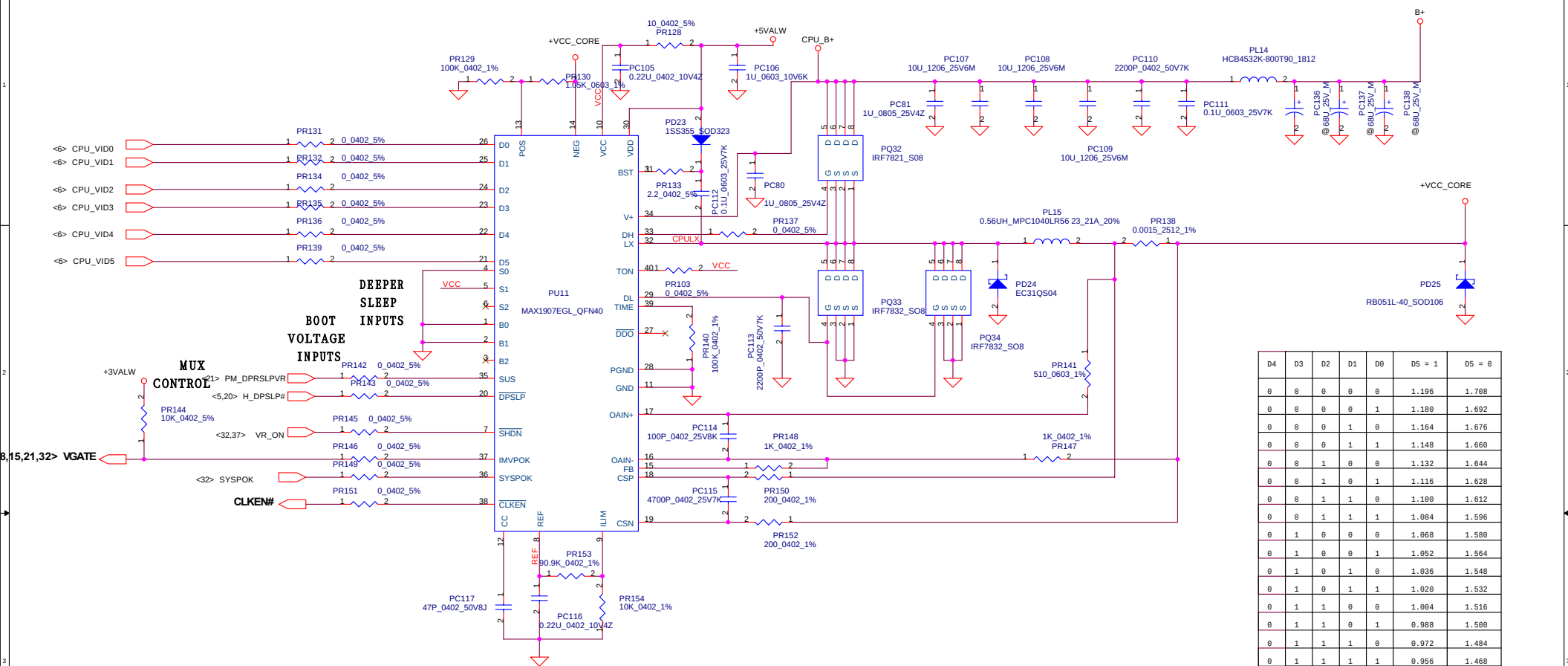
Title			RTC batt & +1.2VP & OTP	
Size	Document Number		EDX20 LA-2481	
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CPU-CORE



D4	D3	D2	D1	D0	D5 = 1	D5 = 0
0	0	0	0	0	1.196	1.788
0	0	0	0	1	1.180	1.692
0	0	0	1	0	1.164	1.676
0	0	0	1	1	1.148	1.660
0	0	1	0	0	1.132	1.644
0	0	1	0	1	1.116	1.628
0	0	1	1	0	1.100	1.612
0	0	1	1	1	1.084	1.596
0	1	0	0	0	1.068	1.580
0	1	0	0	1	1.052	1.564
0	1	0	1	0	1.036	1.548
0	1	0	1	1	1.020	1.532
0	1	1	0	0	1.004	1.516
0	1	1	0	1	0.988	1.500
0	1	1	1	0	0.972	1.484
0	1	1	1	1	0.956	1.468
1	0	0	0	0	0.940	1.452
1	0	0	0	1	0.924	1.436
1	0	0	1	0	0.908	1.420
1	0	0	1	1	0.892	1.404
1	0	1	0	0	0.876	1.388
1	0	1	0	1	0.860	1.372
1	0	1	1	0	0.844	1.356
1	0	1	1	1	0.828	1.340
1	1	0	0	0	0.812	1.324
1	1	0	0	1	0.796	1.308
1	1	0	1	0	0.780	1.292
1	1	0	1	1	0.764	1.276
1	1	1	0	0	0.748	1.260
1	1	1	0	1	0.732	1.244
1	1	1	1	0	0.716	1.228
1	1	1	1	1	0.700	1.212

Delta I=0.0528A REF MAX=2.01V*10K/(10K+90.2K)=0.2005V
 REF Min=1.99V*10K/(10K+90.2K)=0.1986V
 Iimit Max=0.2005V*10/1.485mΩ+1/2 Delta=13.528
 Iimit Max=0.1986V*10/1.515mΩ+1/2 Delta=13.135

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+CPU_CORE

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Item	Fixed Issue	Reason for change	Rev.	PG #	Modify List	B.Ver #	Phase
1		Change FAN design	0.4	5	Change FAN circuit to PWM contrl		
2		Delete unused parts	0.4	7	Del C285		
3		Change CPU Ref Location name	0.4	5	Change CPU Ref location name from JP18 to U1		
4		Delete unused parts	0.4	17	Del R252,Del PID0 net on JP20 pin 26		
5		Design Change	0.4	21	Change PID0 net name to ICH GPIO33 Add Q62(2N7002) to contrl ISOLATE# pin		
6		Design Change	0.4	27	Add net ISOLATE# on U39 pin23		
7		Delete SD card LED	0.4	24	Del D11,R377		
8	Save more power in S3 battery mode	Add LAN power control MOSFET	0.4	27	Add Q63(A03402) ,Q64(2N7002) ,R527(10K_0402) ,C575(0.01uF_0402)		
9		Add MOS on Docking USB_OVC pins	0.4	36	Add Q65,Q66,Q67 on USB_OVC#3, USB_OVC#4, USB_OVC#5		
10		ME change cable design to FFC	0.4	36	Swap JP27,JP22 pin assignment		
11		ME change cable design to FFC	0.4	30	Swap JP25		
12		decrease Audio Gain setting	0.4	30	Depop R245, Pop R243		
13							
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Compal Electronics, Inc.			
Title PIR			
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Item	Fixed Issue	Reason for change	Rev.	PG #	Modify List	B.Ver #	Phase
1		Del MOS on Docking USB_OVC pins	0.5	36	Del Q65,Q66,Q67,Q68 on USB_OVC#3, USB_OVC#4, USB_OVC#5		
2		Add Docking USB_OVC circuit	0.5	33	Add U54,R533,534,R535,R536,R537,C576,C577,C578,C579		
3		Cost-Down	0.5	7	Del C252		
4		Design Change	0.5	31	Change USB Connector type		
5	External VGA display quality	Design Change	0.5	10	Change R36 to 220 ohm(EE Change List by Motion item 1)		
6	Support new version audio driver	Design Change	0.5	36	Change R1160 to 5.1K ohm(EE Change List by Motion item 2)		
7		Material Change	0.5	32	Change Y6 to KDS_SJ132P7KB10 part. (TXC EOL)		
8				5	Change CPU to C0 step		
9							
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Title		
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Version change list (P.I.R. List)

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Item	Fixed Issue	Reason for change	Rev.	PG #	Modify List	B.Ver #	Phase
1	Add the pre-charge circuit.	Pre-charge function fail.	0.3	43	1.Add the Pre-charge circuit. Add the PD32,PR188,PR189,PR190,PR191,PR192,PR182,PD33,PC71,PC127,PC12,PR194,PR87,PR196,PQ35,PR198,PR199,PR197,PQ38	0.3	DVT
2	To adjust sequence for +5VALWP and +3VALWP.	To adjust sequence for +5VALWP and +3VALWP.	0.3	41	1.Delete PC59. 2.Change the PR122 from 100K to 0.	0.3	DVT
3	Chnage the power solution for +VCCP,+1.8VP and +1.5VSP.	Solve Digitizer issue.	0.3	42,44	1.Change the control IC of +1.5VALWP from FAN5234 to MAX8743. 2.Change the control IC of +1.8VP from ISL6227 to MAX8743. 3.Change the control IC of +VCCPP from ISL6227 to MAX8576.	0.3	DVT
4	Delete the circuit of 1.2VP.	Because H/W donot need.	0.3	43	1.Delete the PC100,PQ29,PR121,PR122,PC99,PC101,PR123,PR127,PQ30,PR158,PC102,PR125,PC103.	0.3	DVT
5	To adjust the input-current limit.	Change the input-cuurrent from 3A to 2.25A	0.3	39	1.Change the PR29 from 150K to 232K.	0.3	DVT
6	The jumping cursor when CPU_CORE heavy loading.	Improve Digitizer issue.	0.4	45	1.Change the PL15 form TOKIN .56UH to Panasoic .36UH.	0.4	DVT2
7	The system has shut down issue when swap battery.	The max1538 has some issue when sawp battery. So add the one shot circuit to improve the issue.	0.4	40,43	1.Add the 2n7002 on PQ22,PQ24,PQ25 and PQ26. 2. Add the 2K_0402_5% on PR93 and PR101. 3. Add the 3K_0402_5% on PR91 and PR92. 4.Add the 470K_0402_5% on PR86 and PR87. 5.Add the 100K_0402_5% on PR65. 6.Add the 47K_0402_1% on PR88. 7.Add the 499_0402_1% on PR89 and PR90. 8.Add the 1N4148 on PD17. 9.Add the .022U_0402_16V on PC73 and PC78. 10.Add the 100P_0402_50V on PC79.	0.4	DVT2
8	Battery drain issue when battery only on system off.	It did not turn off the +VCCP (1.05V) power no matter system was ON or OFF.Add pill down resistor.	0.4	42	1.Add the resistor on the PR199 and value is 100K_0402_5%.	0.4	DVT2
9	The customer request add the cap.	Improve Digitizer issue.	0.4	45	1.Add the 1U_0805_25V on PC80 and PC81.	0.4	DVT2
10	Disable Vin OVP function.	Disable Vin OVP function.	0.4	39	1.Delete the PD5 RLZ22B_LL34. 2.Delete the PD6 1SS355_S0T323.	0.4	DVT2
11.	Change Main battery connect.	For ME request.	0.4	39	1.Change the main battery from SUYIN_250263MR007G102ZL to SUYIN_250263MR007G110ZR.	0.4	DVT2
12.	Because the FDS4935A will phase out.	Because the FDS4935A will phase out.	0.4	40	1.Change the main battery from SUYIN_250263MR007G102ZL to SUYIN_250263MR007G110ZR.	0.4	DVT2

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COMPAL CONFIDENTIAL

MODEL NAME : EDX20

PCB No : LS 2488

Revision : 1.0

EDX20 Sub_Board Schematics Document

MB DOCK Board

2005-03-31

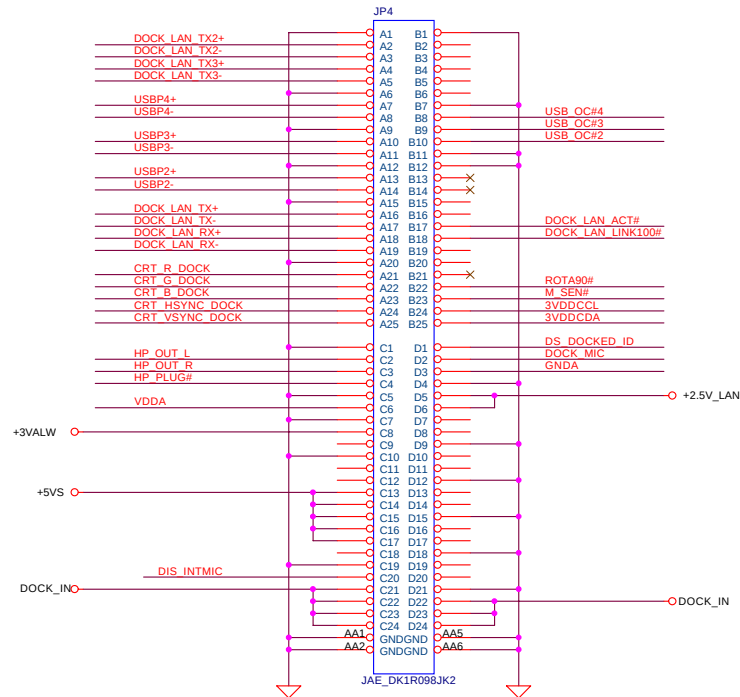
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MB_to_Docking_I/F board

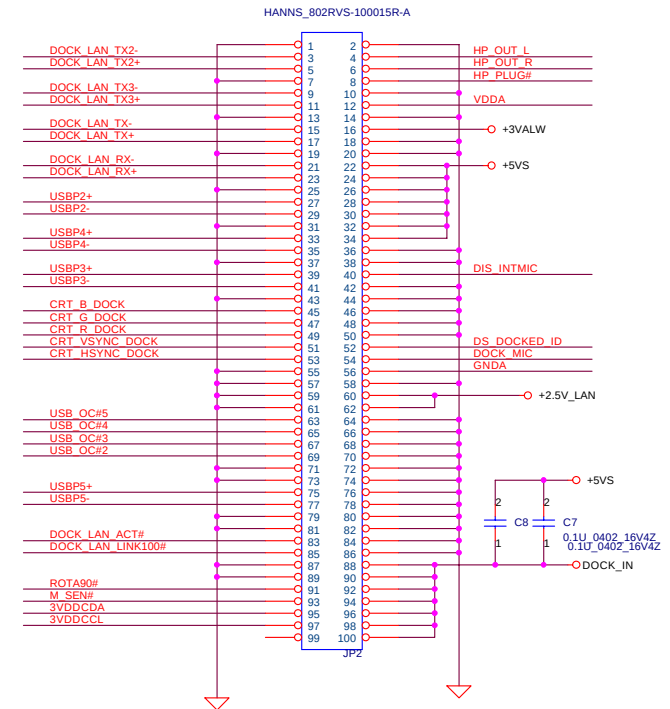
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FILE Name : LS 2488

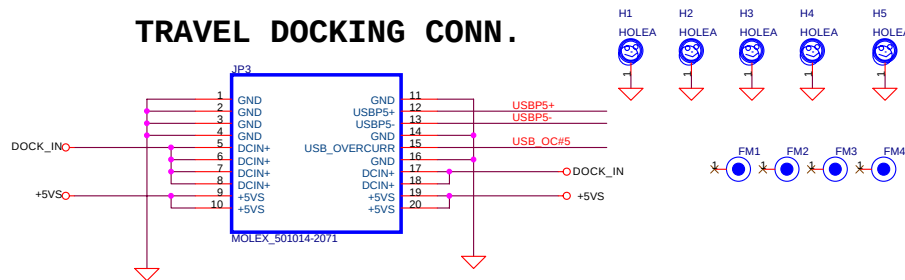
TO LLANO DOCKING



FROM MB 100P Brd-to_Brd CONNECTOR



TRAVEL DOCKING CONN.



19vdc 2.25A

DOCK_IN

C1 1 2

0.1U_0805_25V7M

C6 1 2

0.1U_0805_25V7M

+5VS

C9 1 2

220U_10V_M

C10 1 2

@220U_10V_M

C2 1 2

0.1U_0402_16V4Z

C3 1 2

0.1U_0402_16V4Z

+3VALW

C4 1 2

0.1U_0402_16V4Z

C5 1 2

0.1U_0402_16V4Z

Compal Electronics, Inc.

Title

MB to Docking Brd I/F

Size

Document Number

EDX20 LS 2488

Rev

1.0

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