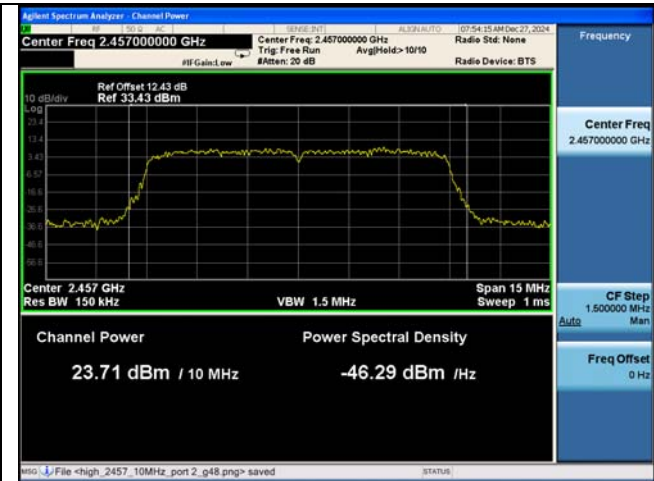


Annex B - Maximum Output Power

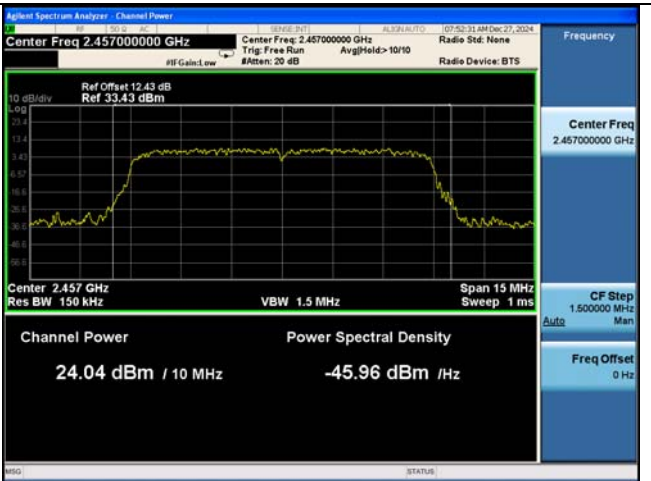
[Naming Convention]

Channel (Low, Mid, High)_Frequency (MHz)_ Bandwidth (10MHz, 20MHz)_Measurement (Output Power)_Port (1, 2)

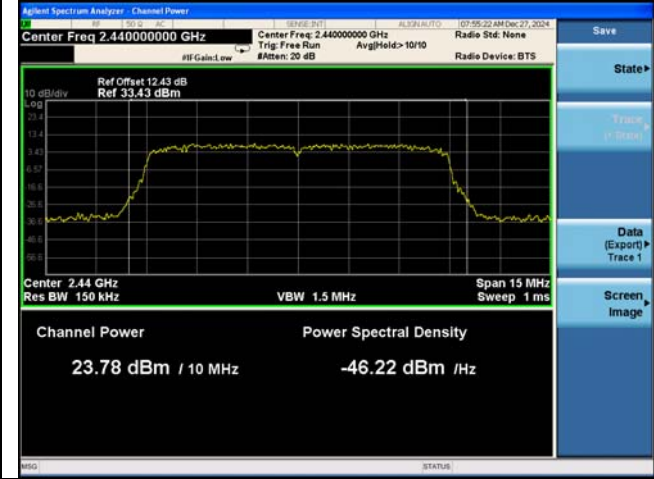
Note: Duty Cycle Correction Factor (DCCF) is already included in reference offset.



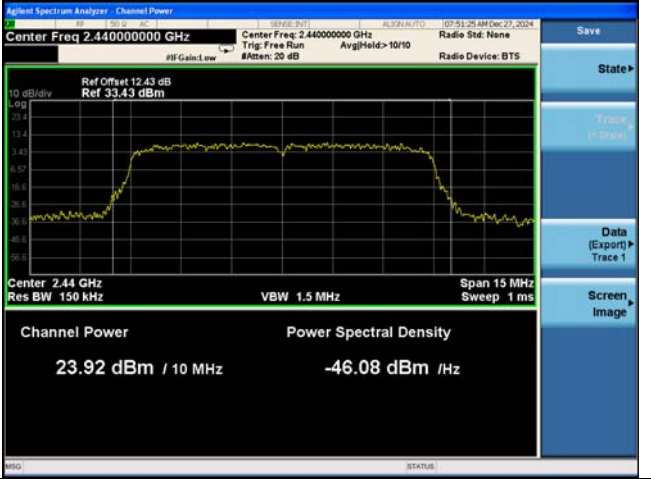
High Ch_2457MHz_10MHz_Output Power_Port 1



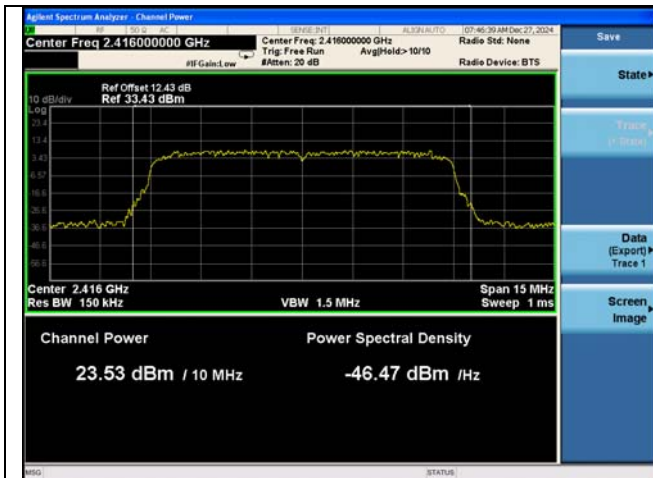
High Ch_2457MHz_10MHz_Output Power_Port 2



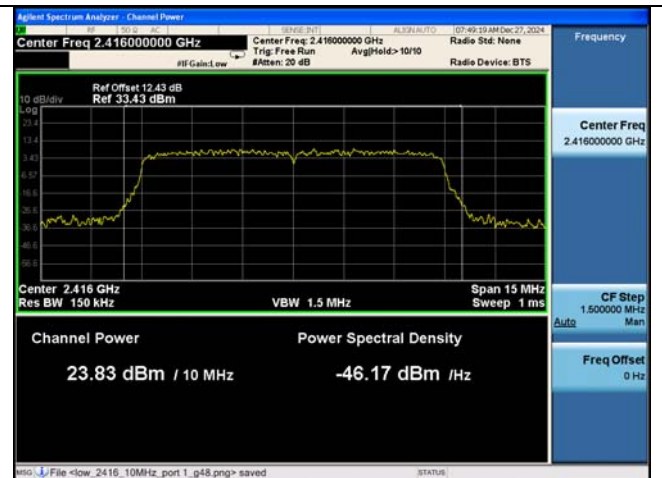
Mid Ch_2440MHz_10MHz_Output Power_Port 1



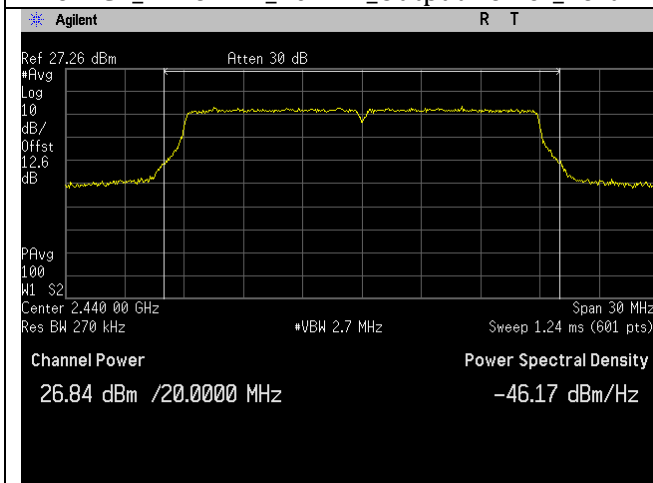
Mid Ch_2440MHz_10MHz_Output Power_Port 2



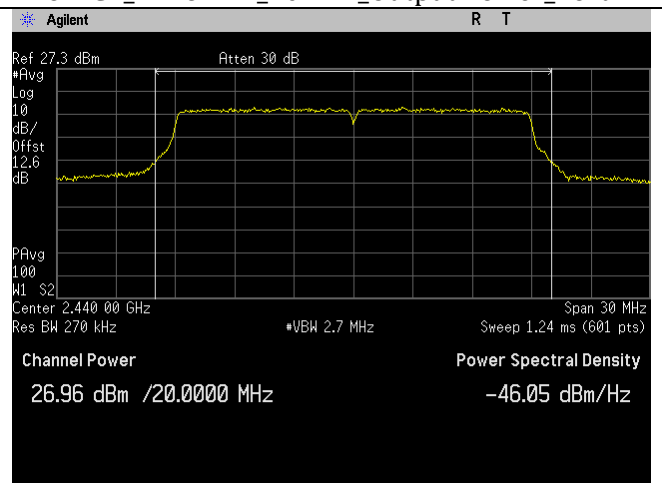
Low Ch_2416MHz_10MHz_Output Power_Port 1



Low Ch_2416MHz_10MHz_Output Power_Port 2



Mid Ch_2440MHz_20MHz_Output Power_Port 1



Mid Ch_2440MHz_20MHz_Output Power_Port 2