

05/09/2025

Equipment Authorisation Division
Federal Communications Commission
7435 Oakland Mills Road
Columbia, MD 21046

Subject: Class II permissive change Request
FCC ID: N4ZG7P00

Granted on: 12/28/2021

To whom may concern:

Pursuant to CRF Section 2.1043, FLYSKY RC MODEL TECHNOLOGY CO., LTD requests a Class II permissive change to update the filing. All of the changes are not effect to the RF performance, function and power.

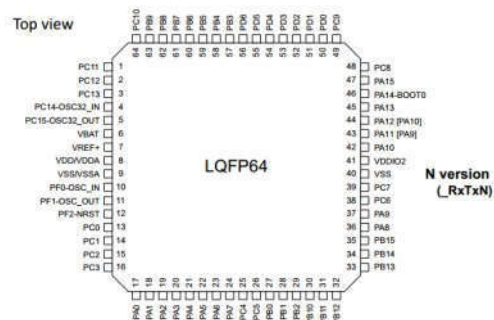
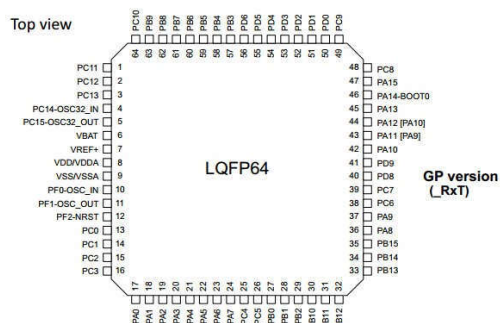
Change list:

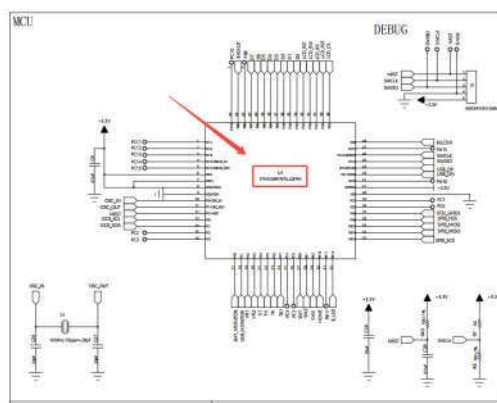
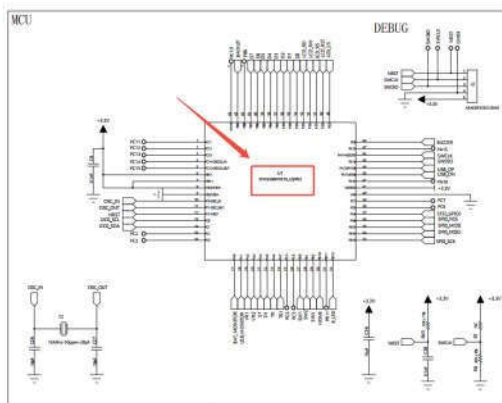
1. Change flash memery and sample appearance

Original:



New:





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STM32G0B1xCx6 Description

Table 2. Features and peripheral counts

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Peripheral		STM32G0B1									
		_K0/ _KE	_K0xN/ _RExN	_C0/ _CE	_C0xN/ _CExN	_NE	_R0/ _RE	_R0xN/ _RExN	_M0/ _ME	_V0/ _VE	
Timers	Flash memory (Kbyte)	256/512	256/512	256/512	256/512	512	256/512	256/512	256/512	256/512	
	SRAM (Kbyte)	128 (parity-protected) or 144 (not parity-protected)									
	Advanced control	1 (16-64) high frequency									
	General-purpose	8 (16-64) + 1 (16-64) high frequency + 1 (32-64)									
	Basic	2 (16-64)									
	Low-power	2 (16-64)									
	SysTick	1									
	Watchdog	2									
	SPI (psig ¹)	3 (2)									
	PC	3									
	USART	6									
	LPUART	2									
	USB	1									
	UOPD	10	2	1			1		2		
	FDICAN	2									
CEC	1										
Comm. interfaces	RTC	Yes									
	Temper pins	3									
	VDDIO2 pin / VSS pin	No/No	Yes/No	No/No	Yes/Yes	Yes/Yes	No/No	Yes/Yes	Yes/Yes	Yes/Yes	
	Random number generator	No									
	AES	No									
	GPIOs	30	29	44	42	46	60	58	74	84	
	Wake-up pins	4	3		4	4	5	5	7	8	
	ADC channels (ext. + int.)	11 + 2	10 + 2	14 + 3	12 + 3	14 + 3	16 + 3	14 + 3	16 + 3	16 + 3	
	DAC channels	2									
	Internal voltage reference buffer	No					Yes				
	Analog comparators	3									
	Max. CPU frequency	64 MHz									
	Operating voltage	1.7 to 3.6 V									
	Operating temperature ⁽²⁾	Ambient: -40 to 85 °C / -40 to 105 °C / -40 to 125 °C Junction: -40 to 105 °C / -40 to 125 °C / -40 to 130 °C									
	Number of pins	32		48	52	64		60	100		

- The numbers in brackets denote the count of SPI interfaces configurable as PS interface.
- One port with only one CC line available (supporting limited number of use cases).
- Depends on order code. Refer to Section 6: Ordering information for details.



PK0073 Rev 2

9/91

Sincerely,

Signature: *Cool Chen*

Name: COOL Chen

Company Name: FLYSKY RC MODEL TECHNOLOGY CO., LTD