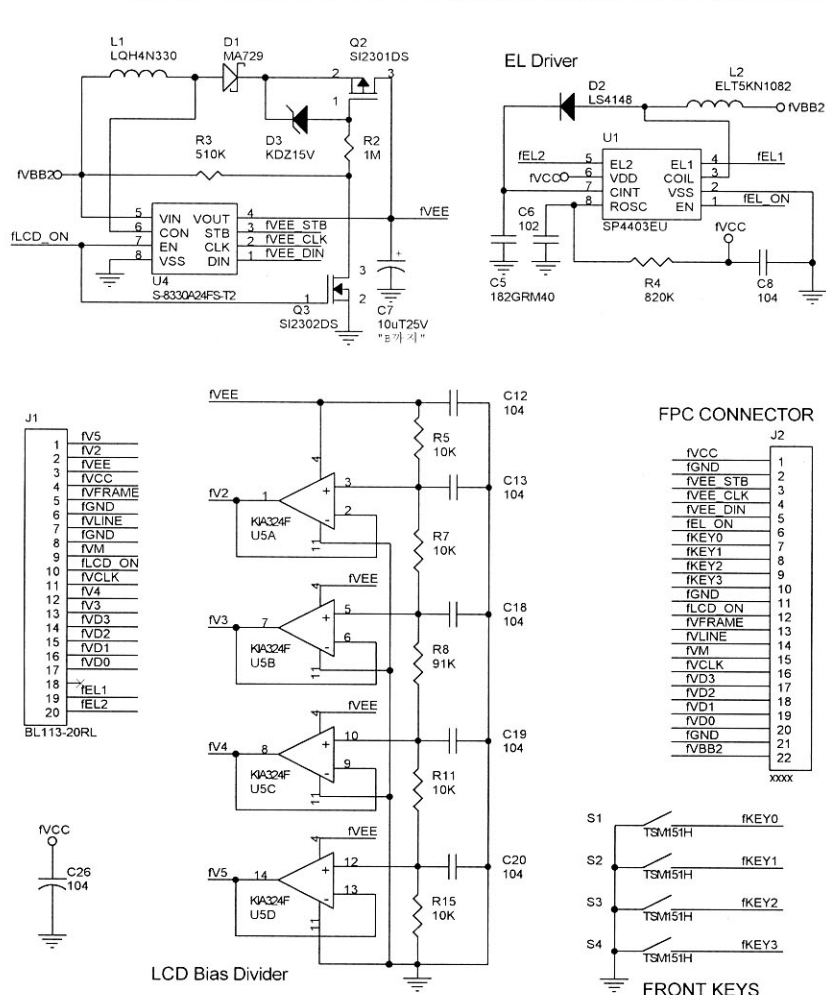
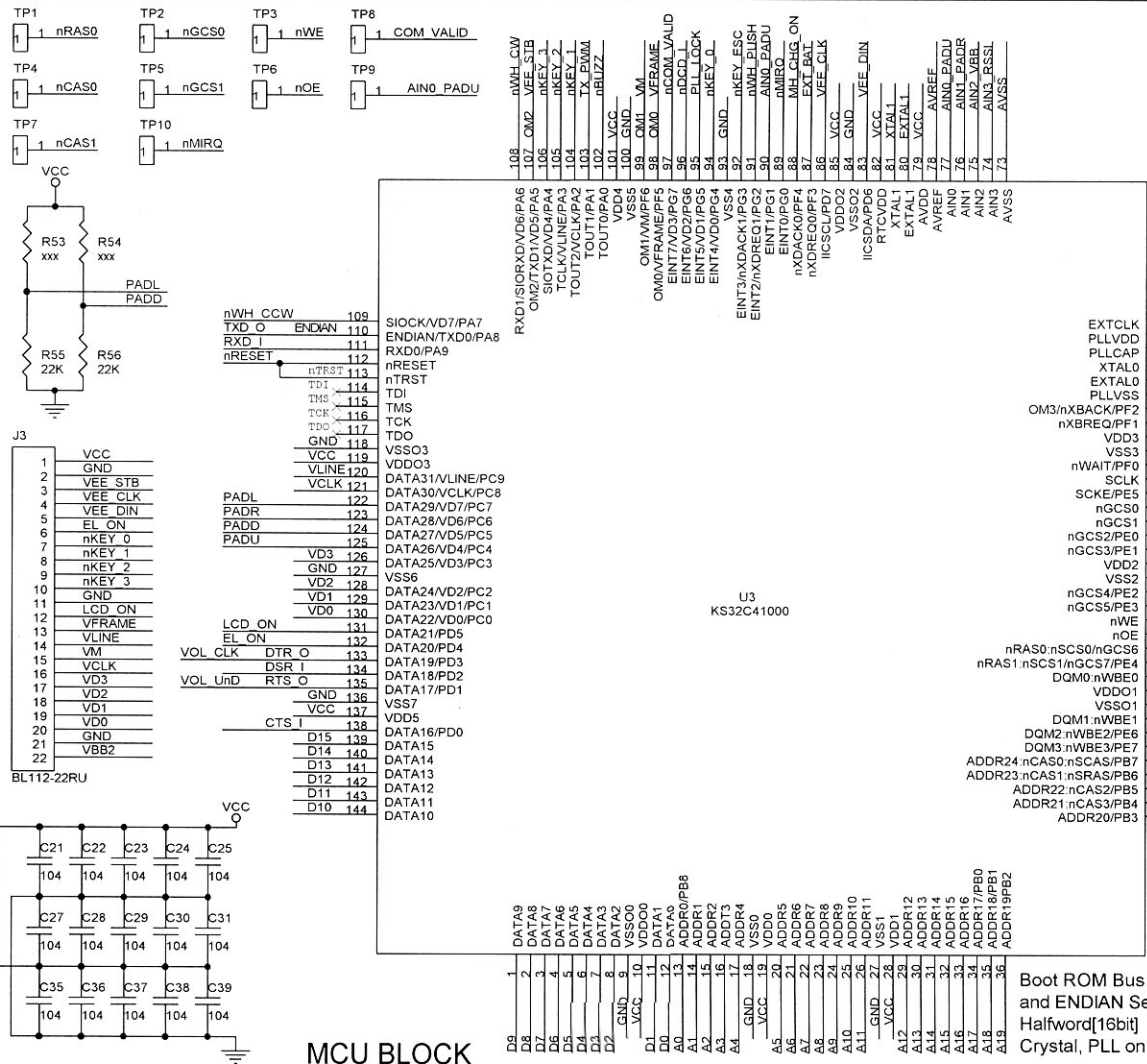
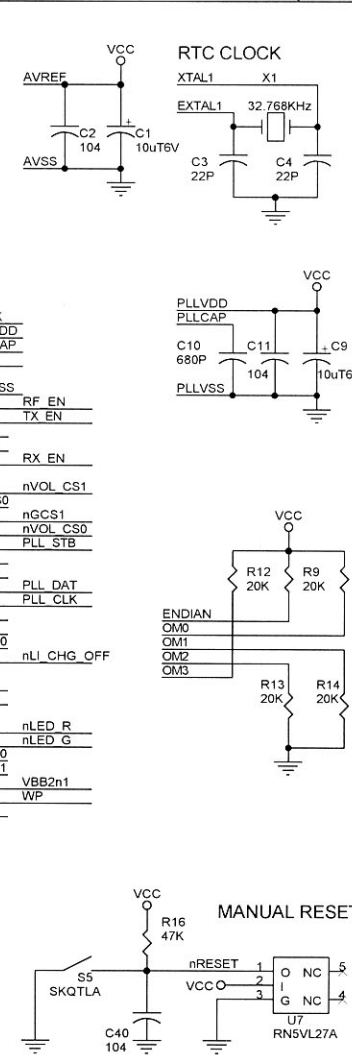




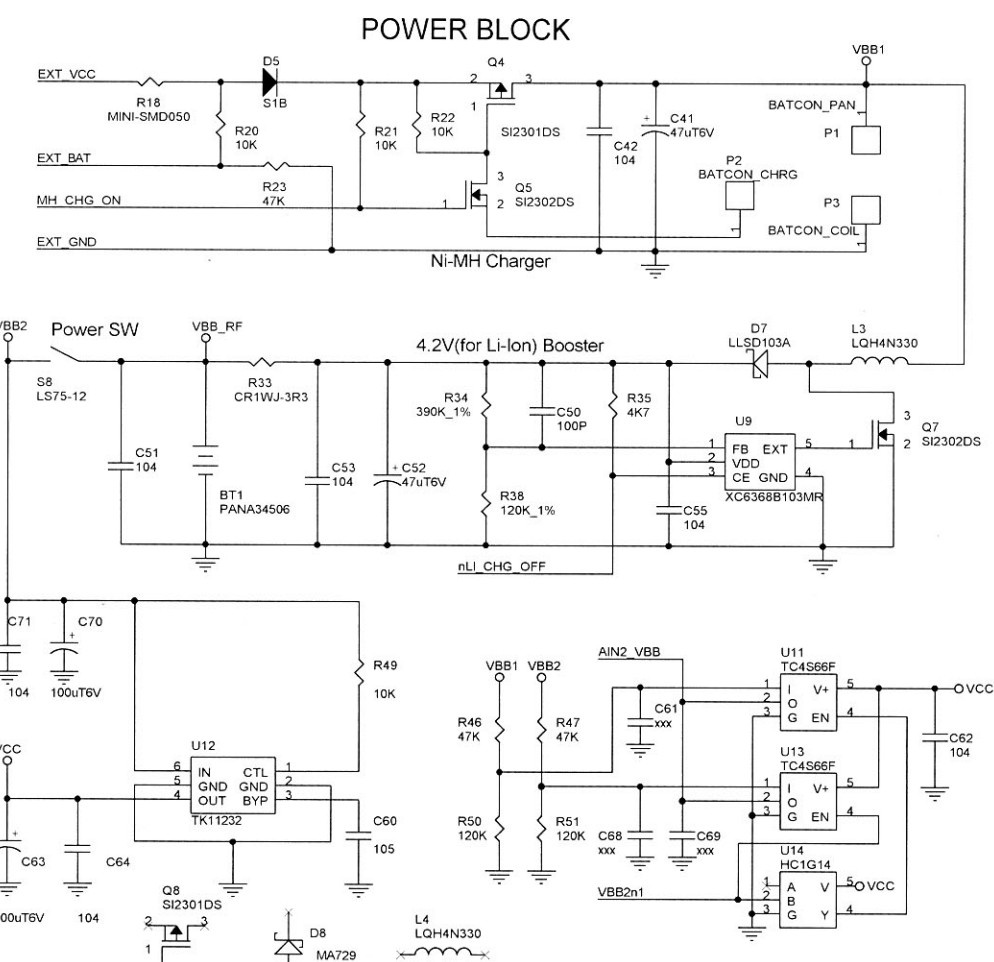
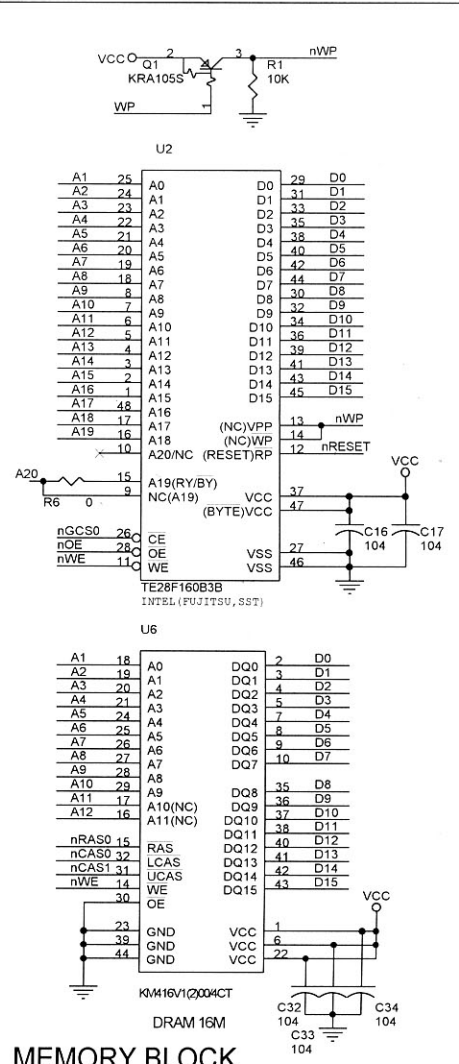
LCD FPC BLOCK



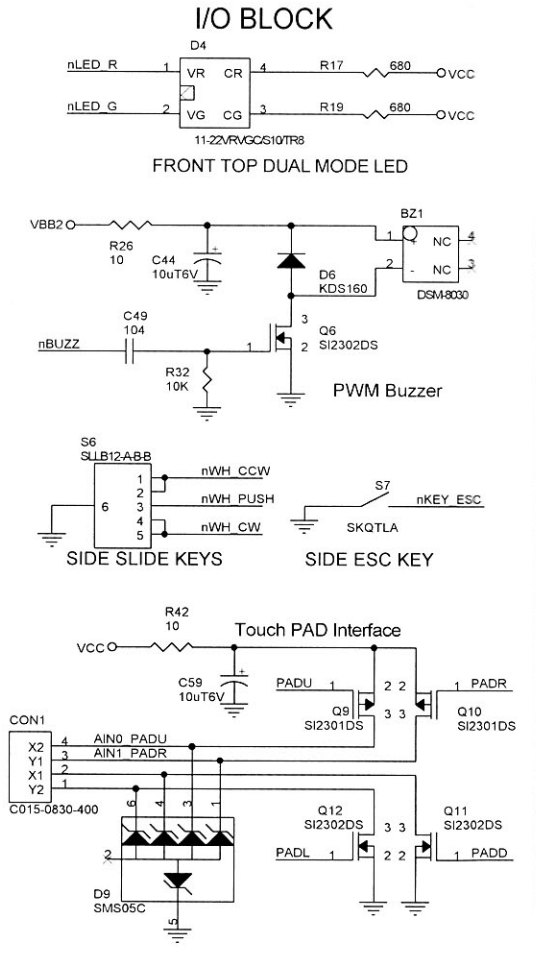
MCU BLOCK



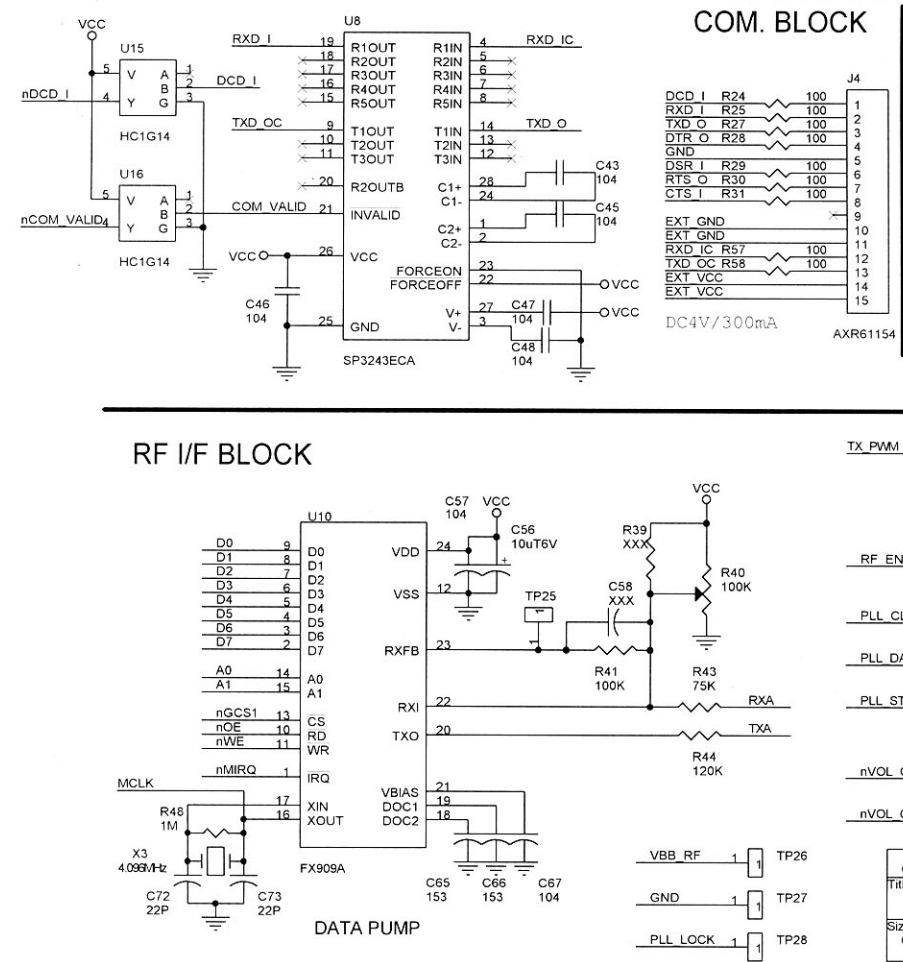
COM. BLOCK



POWER BLOCK



I/O BLOCK



*** Reversion

1. 외부Clock으로 교체
2000.4.11 (OM3, EXTCLK, XTAL0, EXTAL0)
2. DRAM 전원 R3C0D0를 VCC로 교체, 2000.4.18
3. VBB2를 S5 펄스원으로 이동, 2000.5.3
4. Main DC-DC Stepdown을 LDO(U12)로 교체, 2000.5.3
5. C4 104를 100으로 교체, 2000.5.3
6. R45, R52를 TP13, TP20로 이동, 2000.5.3
7. PLL LOCK(TP28) 추가, 2000.5.3
8. R53-56 Mode 3/4추가, 2000.5.3
9. U15, U16추가 및 CPU p56, p77변경, 2000.5.3
10. J4 22pin->15pin으로 변경, R57, 58 추가, 5.8
11. Buzzer쪽 R32, C49 서로 자리이동, 5.8