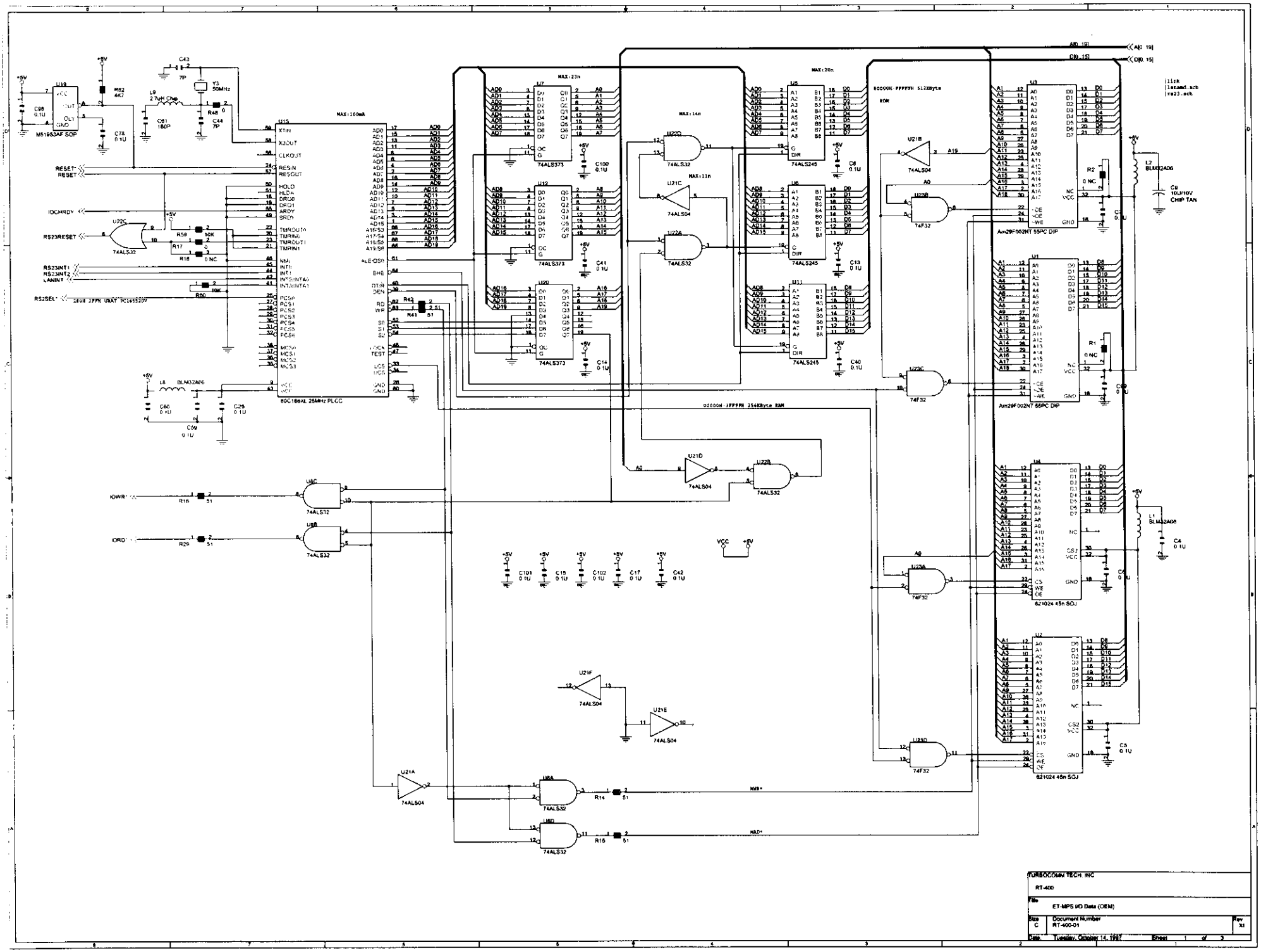
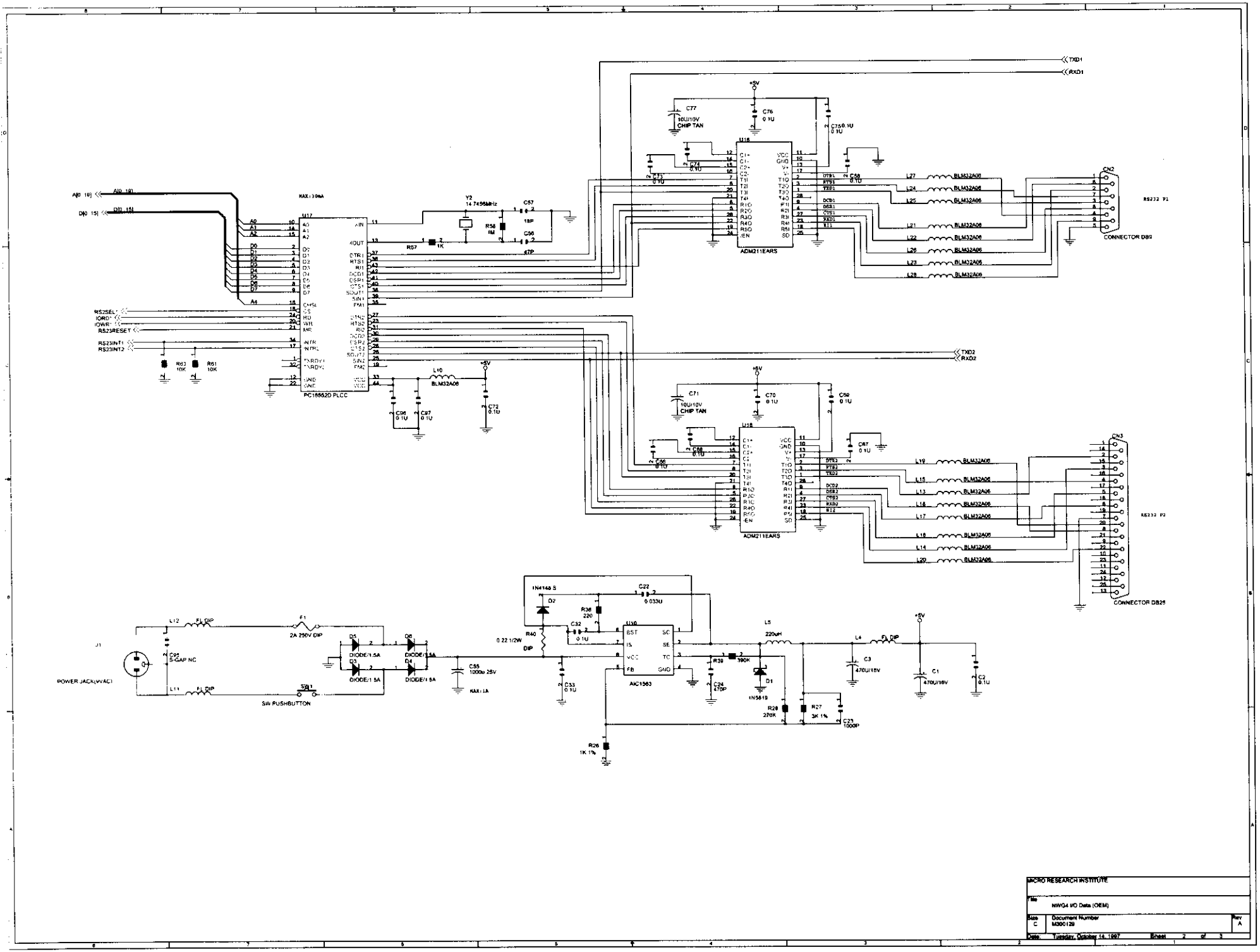
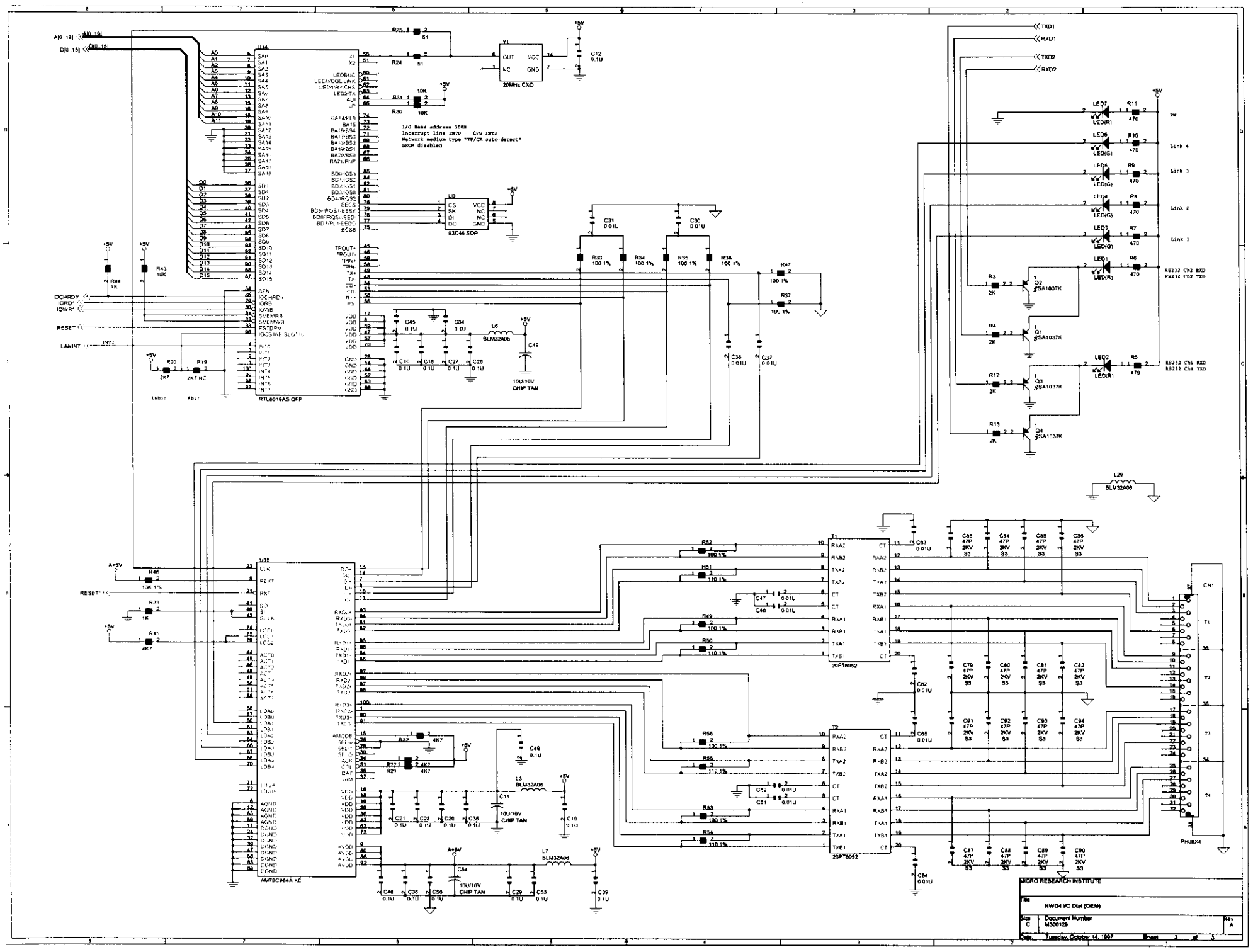
EXHIBIT D

Circuit Diagram


80C186XL 25MHz PLCC
74ALS373
74ALS245
74ALS32
74ALS04
74F32
Am29F002NT 55PC DIP
62V1024 45n SOJ
M51953AF SOP
RESET
IOCHRDY
RS23RESET
RS23INT1
RS23INT2
LANINT
RS2SEL
IOWR
IORD
TURBOCOMM TECH. INC
RT-400
ET-MPS I/O Data (OEM)
Document Number
RT-400-01
Tuesday, October 14, 1997
Sheet 1 of 3


TXD1
RXD1
TXD2
RXD2
A[0 19]
D[0 15]
MAX130MA
U17
PC16552D PLCC
Y2
14.7456MHz
RS232SEL*
IORD*
IOWR*
RS23RESET
RS23INT1
RS23INT2
R63 10K
R61 10K
L10 BLM32A06
C96 0.1U
C97 0.1U
C72 0.1U
+5V
C77 10U/10V CHIP TAN
C76 0.1U
C75 0.1U
U18
ADM211EARS
CN2
CONNECTOR DB9
RS232 P1
C71 10U/10V CHIP TAN
C70 0.1U
C69 0.1U
U19
ADM211EARS
CN3
CONNECTOR DB25
RS232 P2
J1
POWER JACK(WVAC)
L12 FL DIP
L11 FL DIP
C95 S-GAP NC
F1 2A 250V DIP
SW1 SW PUSHBUTTON
D5 DIODE/1.5A
D6 DIODE/1.5A
D3 DIODE/1.5A
D4 DIODE/1.5A
C55 1000u 25V
MAX1.1A
IN4148 S
D2
R38 220
C22 0.033U
C32 0.1U
R40 0.22 1/2W DIP
C33 0.1U
U10
AIC1563
BST
IS
VCC
FB
SC
SE
TC
GND
R39 390K
C24 470P
L5 220uH
D1 IN5819
R28 270K
R27 3K 1%
C23 1000P
R26 1K 1%
L4 FL DIP
C3 470U/16V
C1 470U/16V
C2 0.1U
MACRO RESEARCH INSTITUTE
Title
NWG4 I/O Data (OEM)
Size C
Document Number M900129
Rev A
Date Tuesday, October 14, 1997
Sheet 2 of 3


RTL8019AS QFP
93C46 SOP
20MHz CXO
I/O Base address 300h
Interrupt line INT0 - CPU INT3
Network medium type "TP/CX auto-detect"
SROM disabled
AM79C964A KC
20PT8052
20PT8052
PHJ8X4
MICRO RESEARCH INSTITUTE
NWG4 I/O Dist (OEM)
Document Number
M300128
Tuesday, October 14, 1997