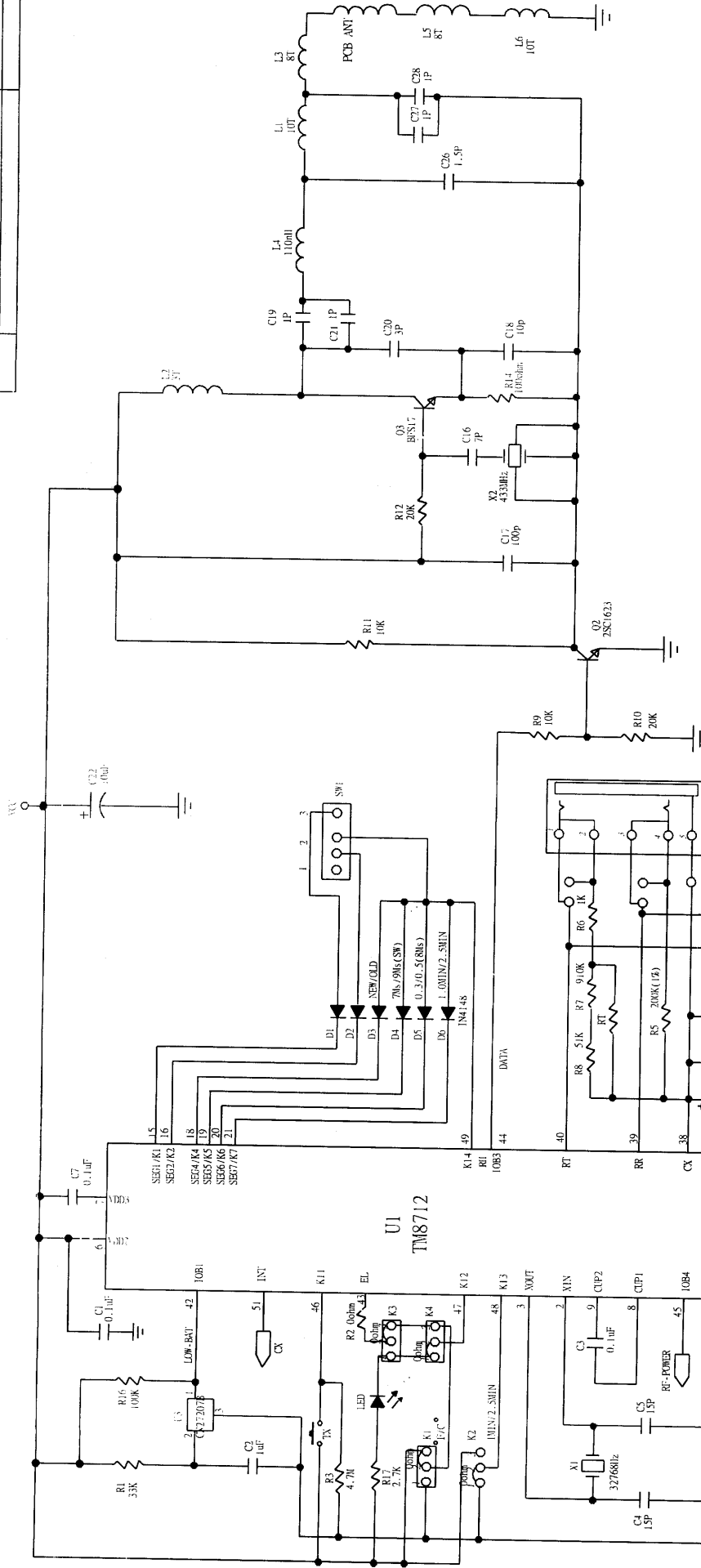


REVISION RECORD

VER	ECO/REASON	APPROVAL	DATE
0	Release For Production	<i>[Signature]</i>	<i>[Date]</i>



- 1) K1: short pin2 and3 for 7; short pin1 and2 for F.
- 2) Selecting time with k2: short pin1 and pin2.
- 3) Old IC: short pin1 and2 of k3, pin2 and3 of k4;
New IC: short pin2 and3 of k3, pin1 and2 of k4.
- 4) Short pin1 and2, pin3 and4 without jack.
- 5) New Code with D3; Old Code without D3.
- 6) C14, C15 are 18M Resistor and 1nF Capacitor.

EWIG INDUSTRIES CO. LTD.

DESIGN: <i>[Signature]</i>	DATED: <i>[Date]</i>	MODEL NO.	217S53	PCB NO.	REV: 0
CHECKED: <i>[Signature]</i>	DATED: <i>[Date]</i>	FILE NAME:	217S53	PAGE NO.	1 OF 1
APPROVAL: <i>[Signature]</i>	DATED: <i>[Date]</i>				