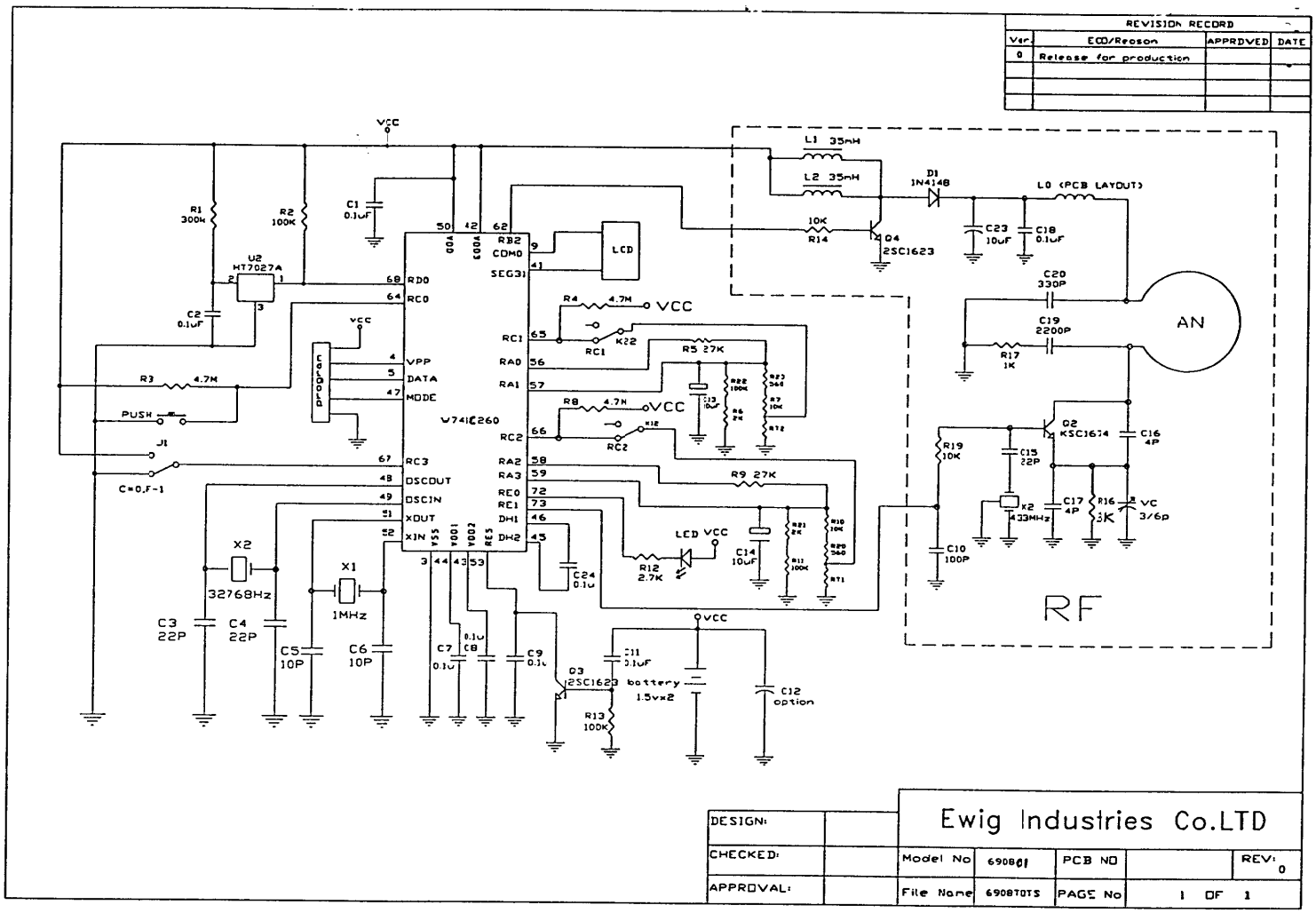

REVISION RECORD
Ver. | ECO/Reason | APPROVED | DATE
0 | Release for production
VCC
L1 35nH
L2 35nH
D1 1N4148
L0 (PCB LAYOUT)
10K R14
Q4 2SC1623
C23 10uF
C18 0.1uF
C20 330P
C19 2200P
R17 1K
AN
R1 300K
R2 100K
C1 0.1uF
U2 HT7027A
C2 0.1uF
R3 4.7M
PUSH
J1
C=0.F-1
LCD
R4 4.7M
K22
RC1
RS 27K
R8 4.7M
K12
RC2
R9 27K
LCD VCC
R12 2.7K
C14 10uF
C24 0.1u
W741C260
RB2
COM0
SEG31
RD0
RC0
RC1
RA0
RA1
RC2
RA2
RA3
RE0
RE1
DH1
DH2
VPP
DATA
MODE
RC3
OSCOUT
OSCIN
XOUT
XIN
VSS
VDD1
VDD2
RES
X2
32768Hz
X1
1MHz
C3 22P
C4 22P
C5 10P
C6 10P
C7 0.1u
C8 0.1u
C9 0.1u
C11 0.1uF
battery 1.5vx2
C12 option
D3 2SC1623
R13 100K
Q2 KSC1674
C16 4P
C15 22P
R19 10K
X2 433MHz
C17 4P
R16 3K
VC 3/6p
C10 100P
RF
DESIGN:
CHECKED:
APPROVAL:
Ewig Industries Co.LTD
Model No 6908@1
PCB NO
REV: 0
File Name 6908T0TS
PAGE No 1 OF 1