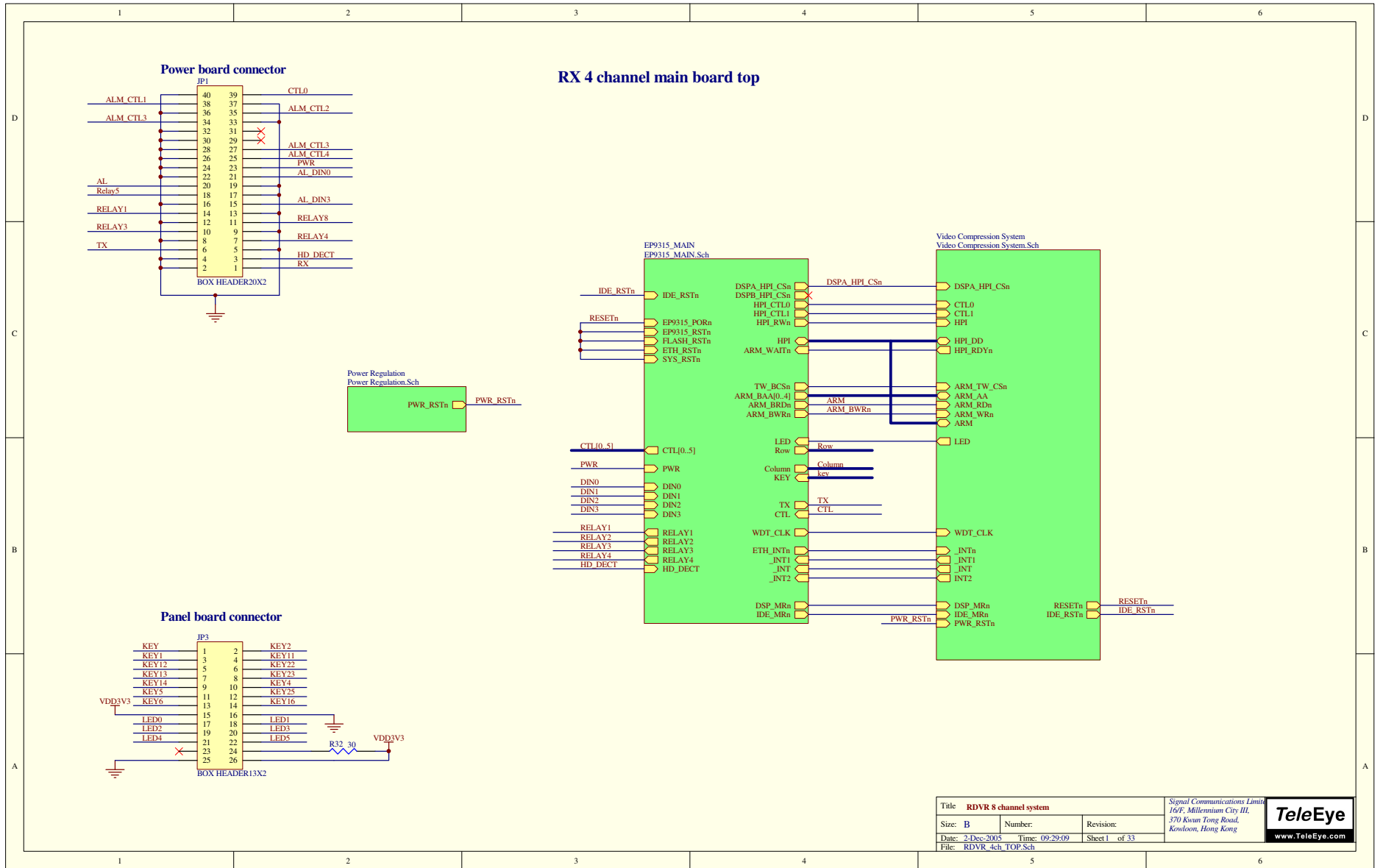
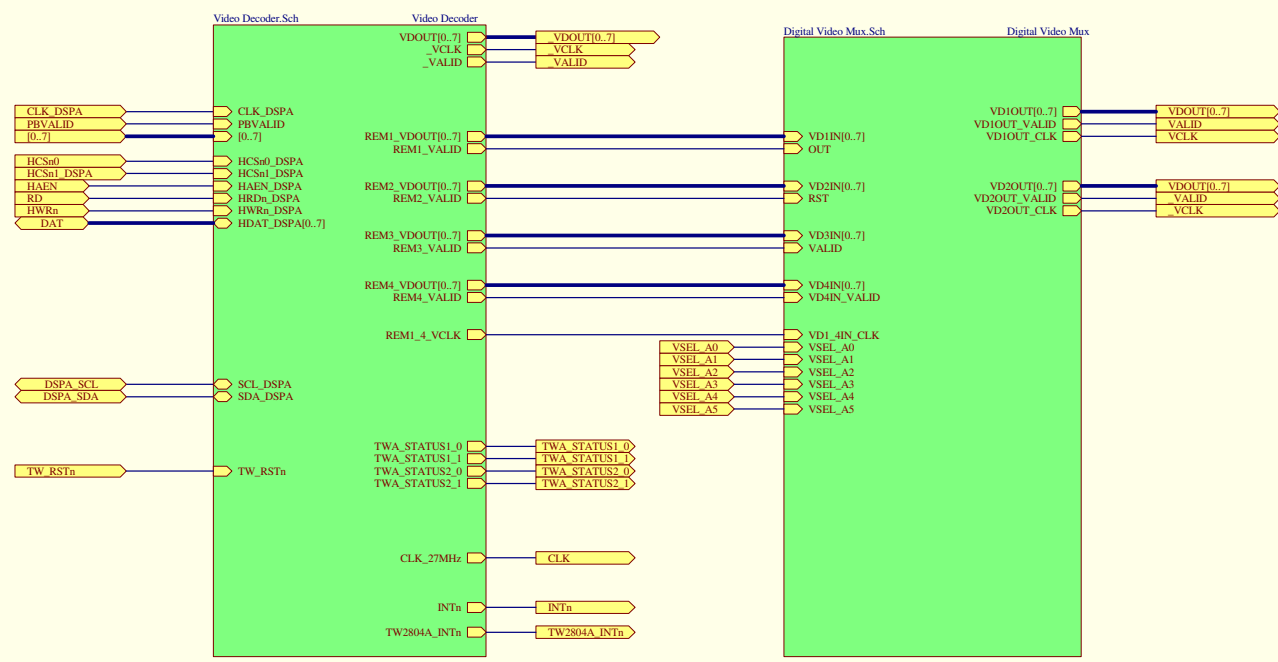


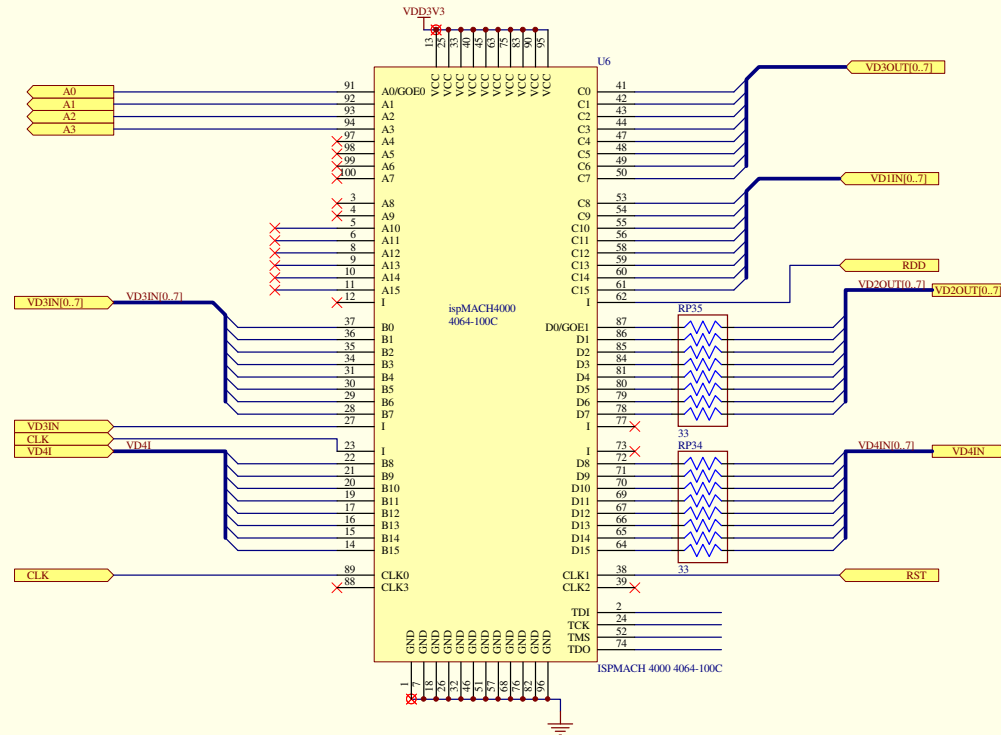


[illegible]

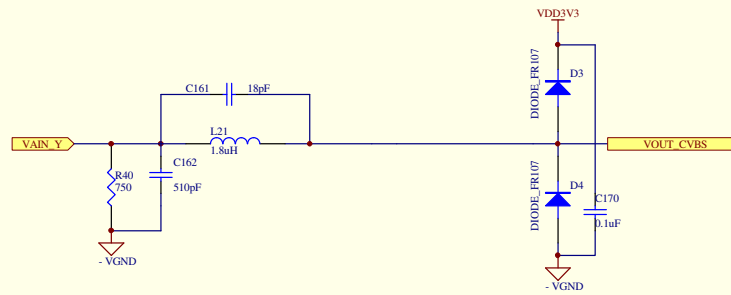
Digital Video System



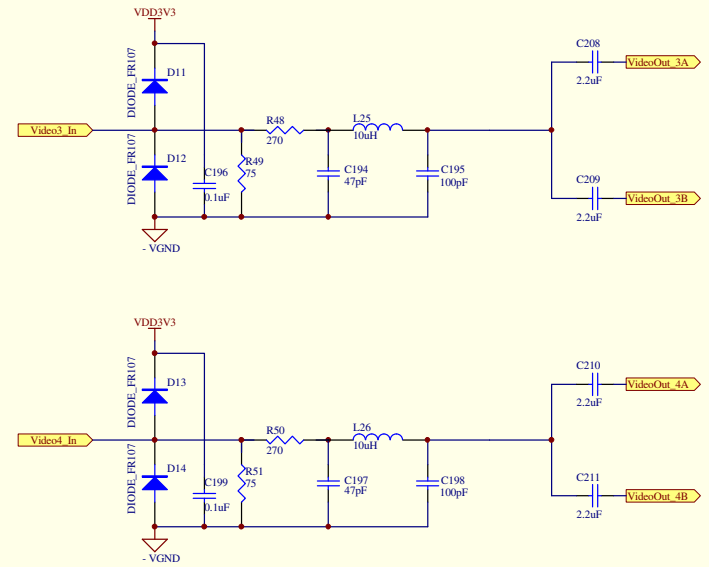
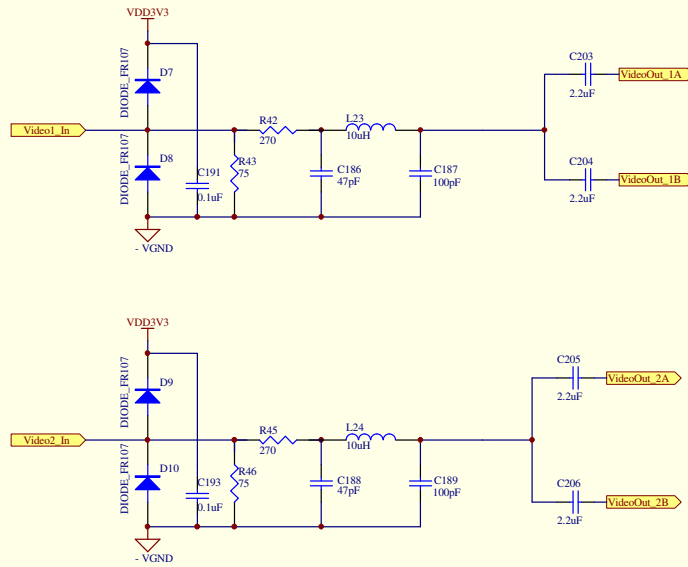
Digital Video MUX CPLD

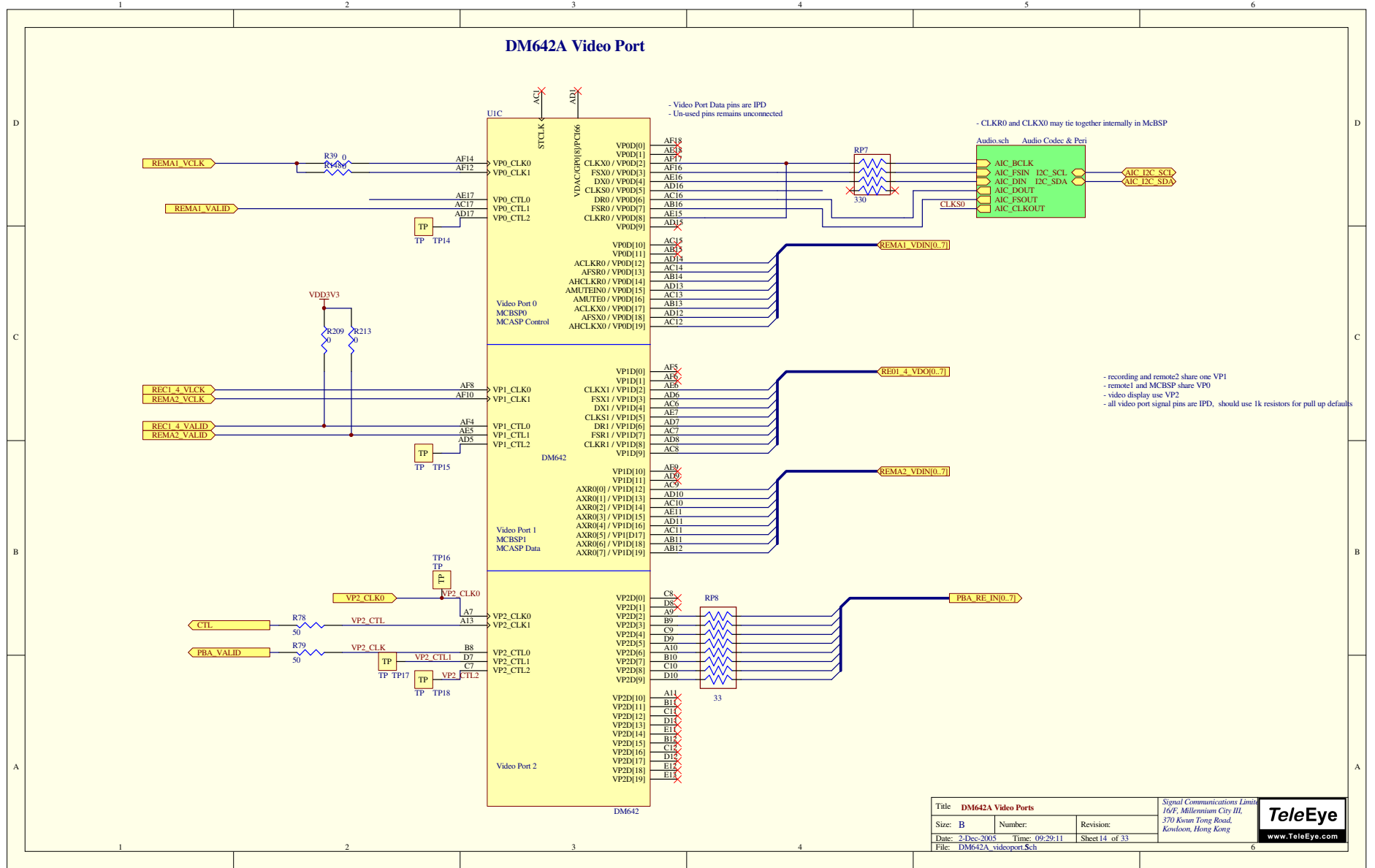


Analog Video Output

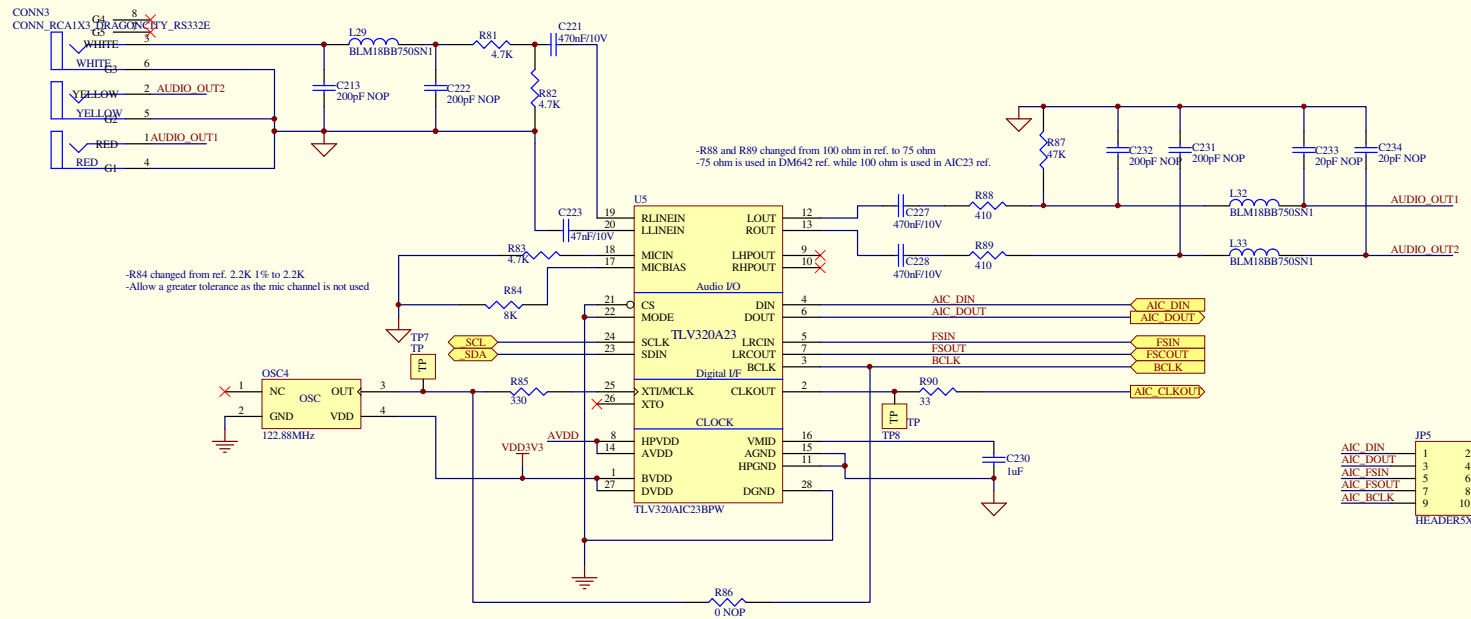


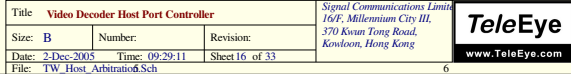
Analog Video Input





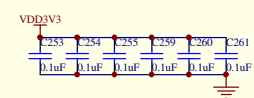
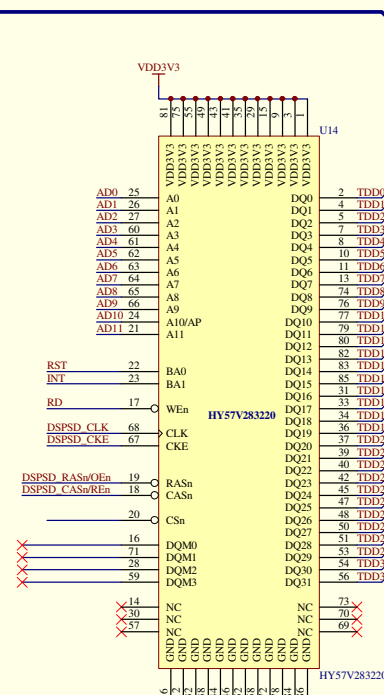
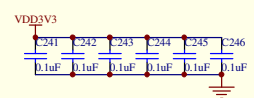
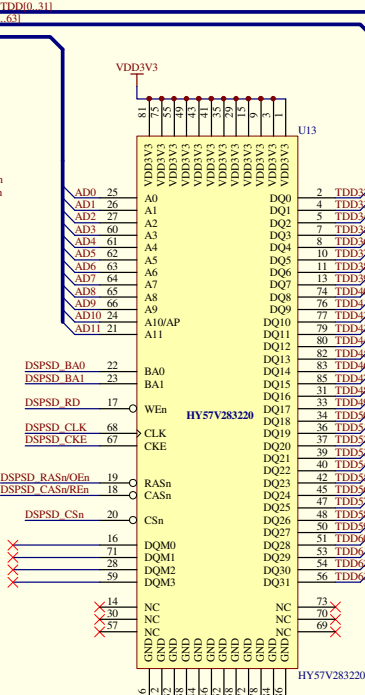
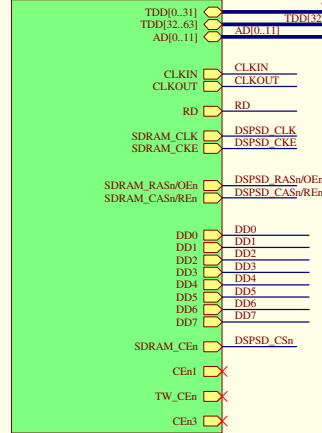
Audio System

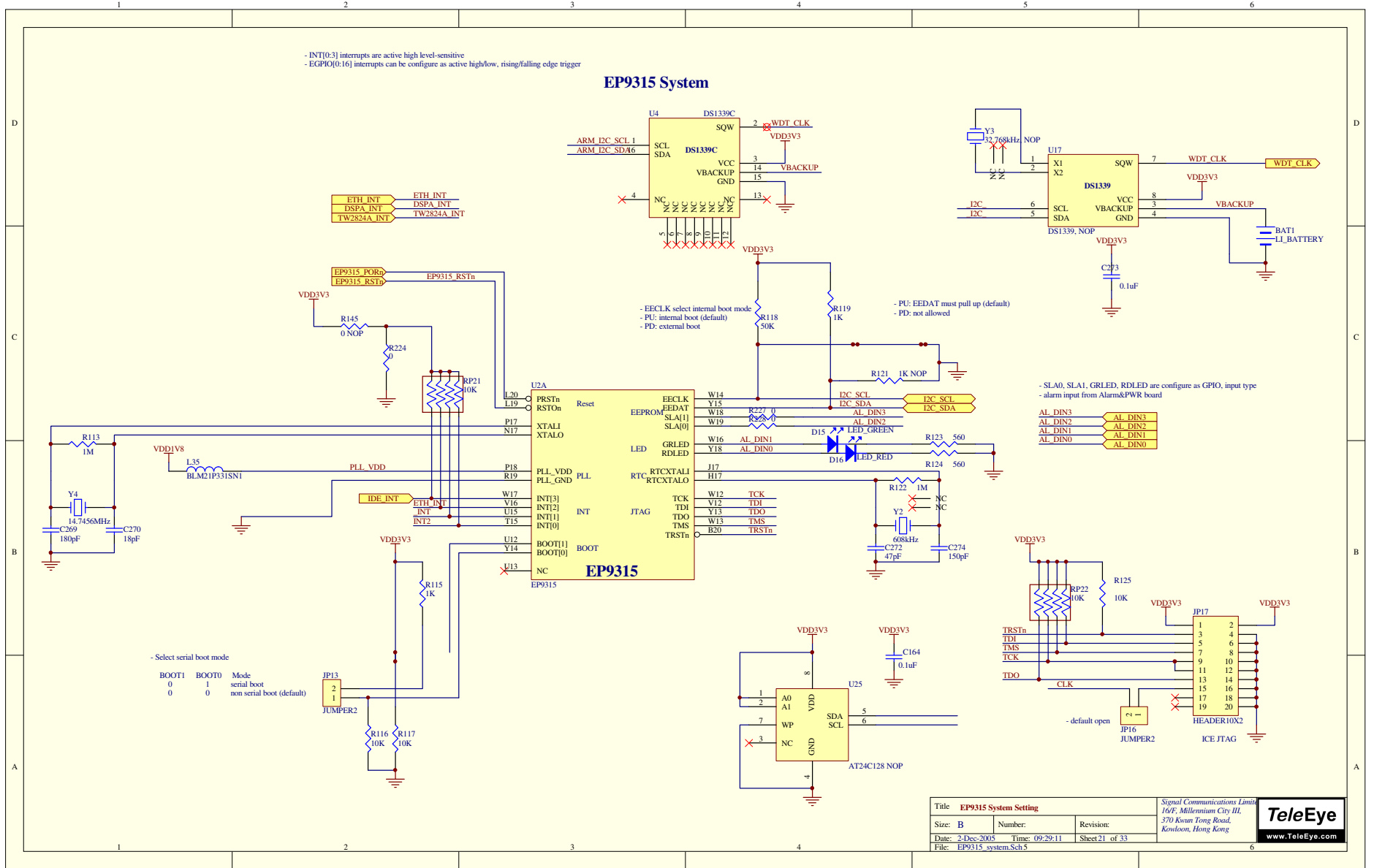




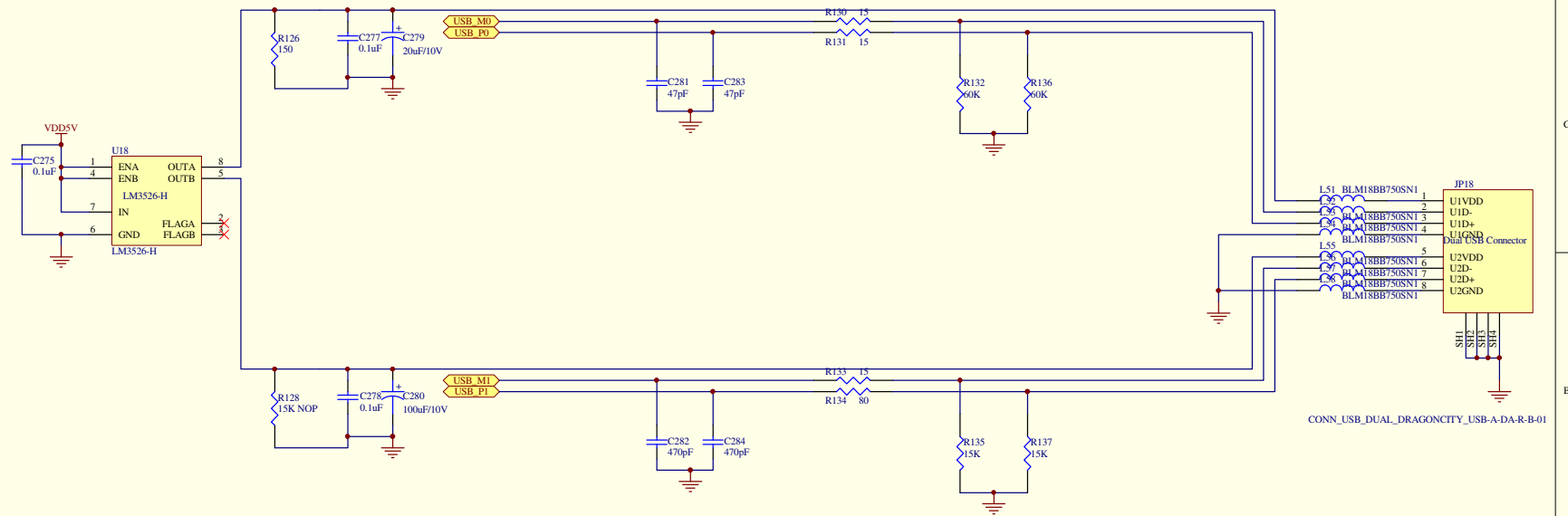
DM642A SDRam

DM642A_Mem_n_JTAG
DM642A_Mem_n_JTAG.Sch





EP9315 USB Interface



Title EP9315 USB			Signal Communications Limited 16/F, Millennium City III, 370 Kwan Tong Road, Kowloon, Hong Kong	
Size: B	Number:	Revision:	 www.TeleEye.com	
Date: 2-Dec-2005	Time: 09:29:12	Sheet 22 of 33		
File: EP9315_USB.Sch	5			

The diagram illustrates the peripheral connections for the EP9315 microcontroller. The central component is the EP9315, with various pins connected to external devices:

- USB:** USB P1, P0, M1, M0, and M10 are connected to the USB pins of the EP9315.
- Ethernet:** ETH_TXD0, ETH_TXD1, ETH_TXD2, ETH_TXD3, ETH_RXD0, ETH_RXD1, ETH_RXD2, ETH_RXD3, ETH_CRS, ETH_CLD, and ETH_TXCR are connected to the Ethernet pins of the EP9315.
- UART:** UART_TXD0, UART_RXD0, UART_CTRLD, UART_DTRn, UART_RTSn, and CAM_TX are connected to the UART pins of the EP9315.
- Touch Screen:** The Touch Scr. is connected to the Touch Scr. pins of the EP9315.
- Other:** Various other pins like SCLK, XM, YP, SXP, SXM, SYP, SYM, ADC_VDD, ADC_GND, PWM, PWMOUT, COL, ROW, KEY, and KEY_ROW are connected to their respective pins on the EP9315.

The diagram also shows the connection of the EP9315 to the EP9315 Peripheral, which is a separate component that provides the necessary peripheral functions.

[illegible]

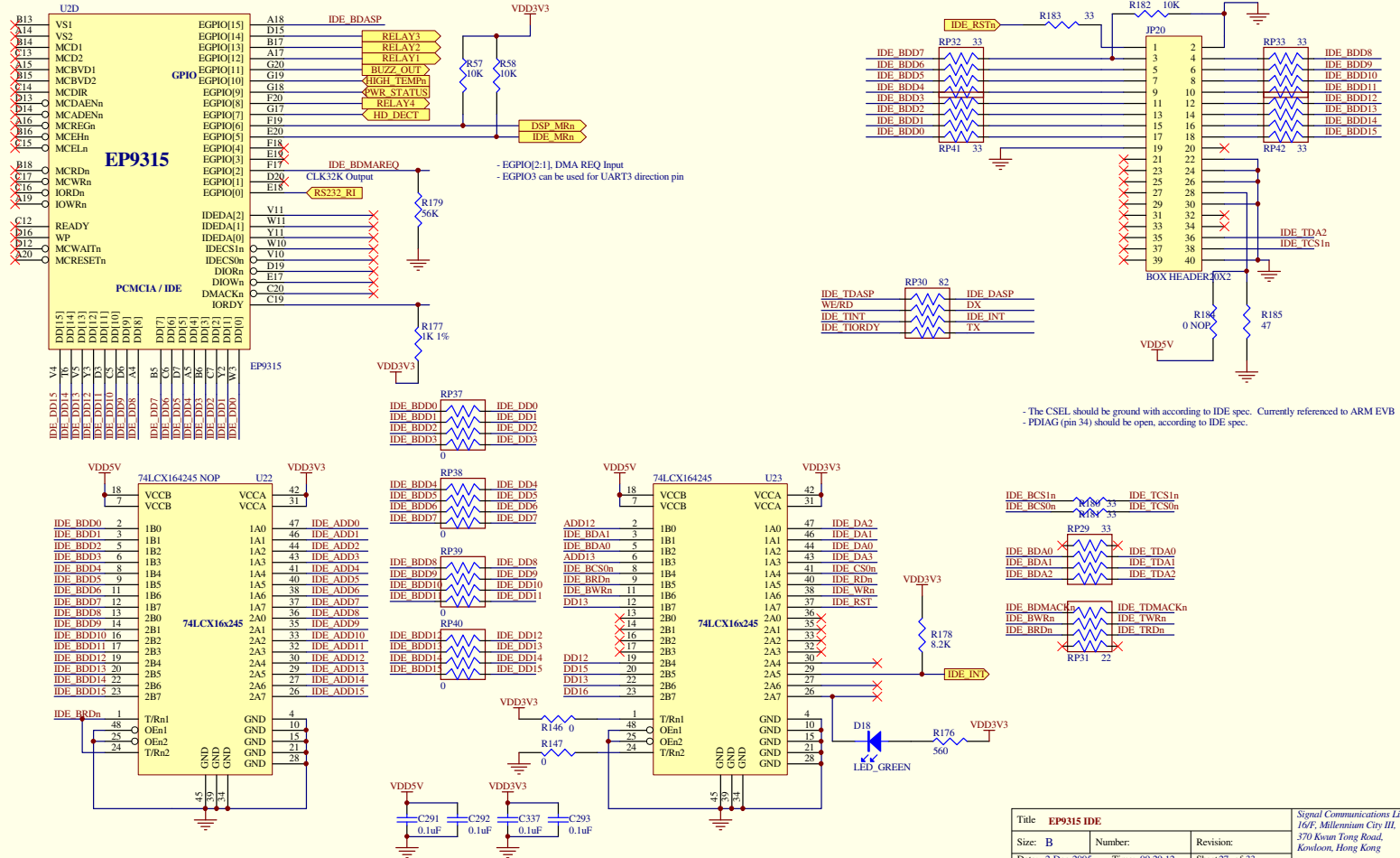
EP9315 IDE Interface & GPIO

- GPIO is configured as default mode which include:

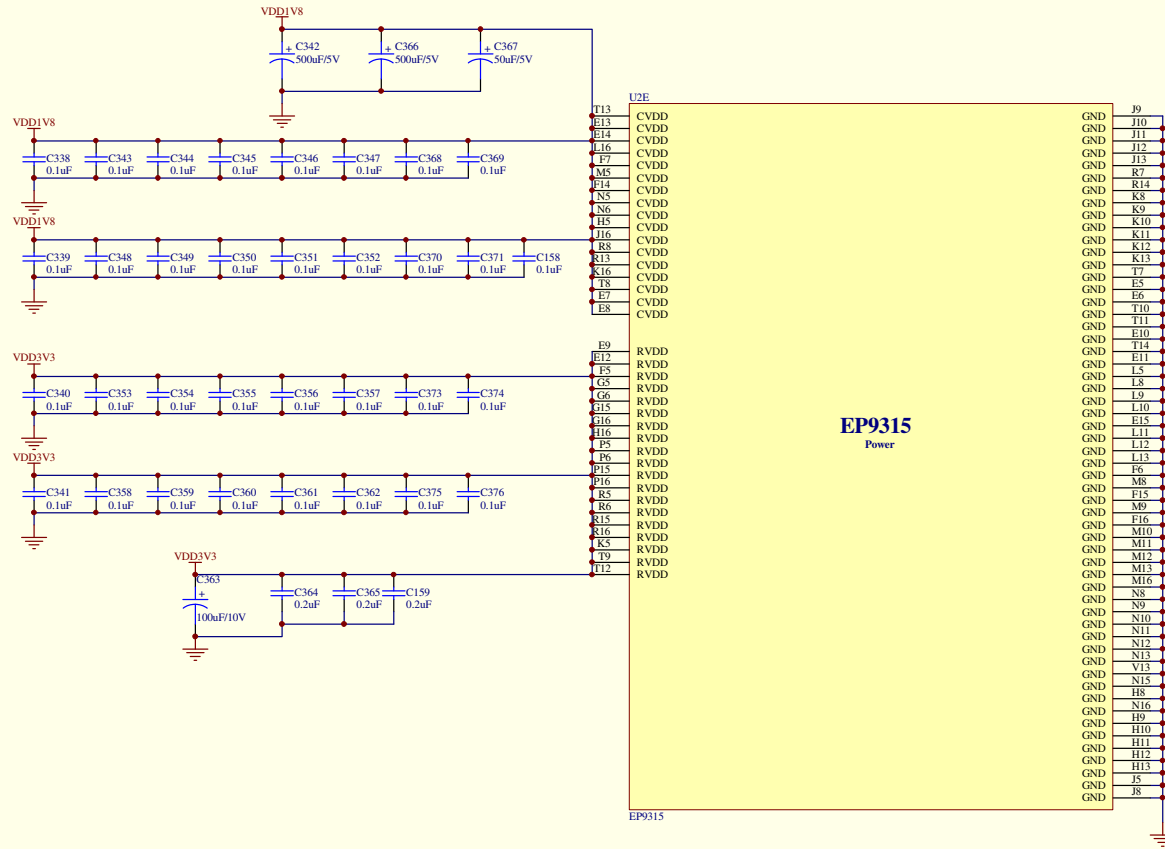
- EGPI0[7:0] : Port A
- EGPI0[15:8] : Port B
- GRLED, RDLED : Port E[0:1]
- PCMCIA : Port F
- IECLK, EEDAT : Port G[0:1]
- Key Scanning : no map, remain key scanning function
- IDE : no map, remain IDE function

- A, B, F are EGPI0 ports, can be configured as interrupt inputs

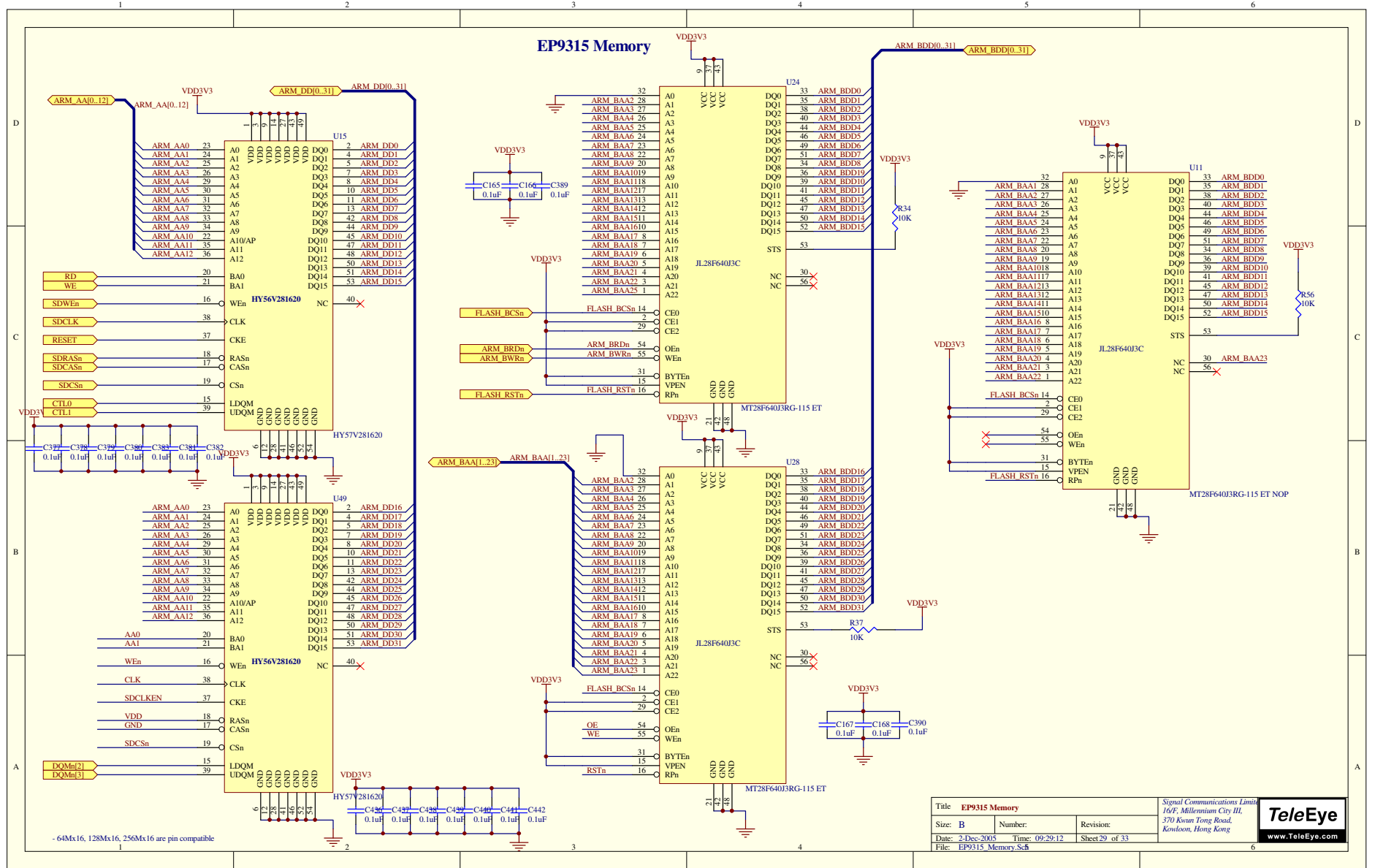
- GPIO[14] should be enabled w/ falling edge type interrupt for connection w/ LM75 O.S.
- The interrupt can be cleared by reading LM75 via I2C



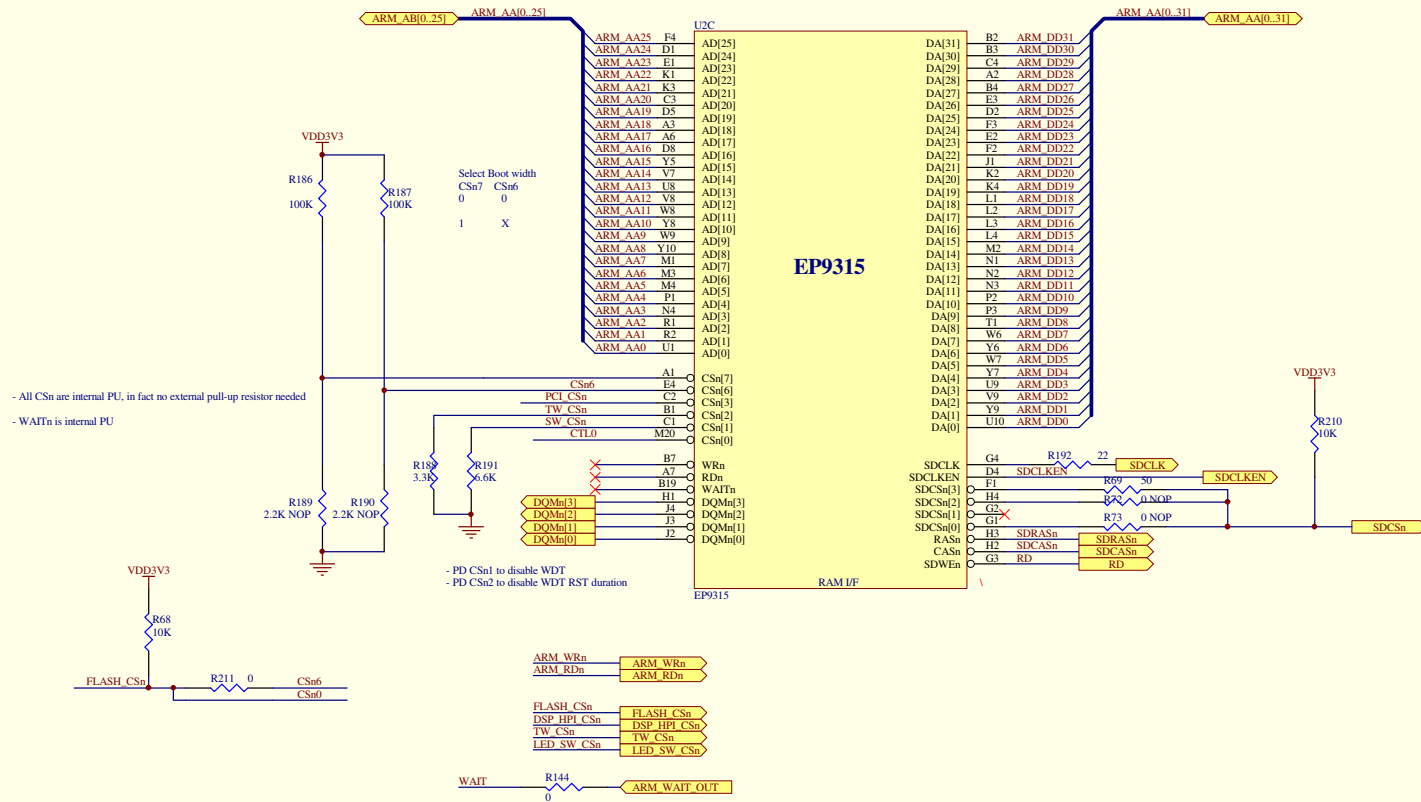
EP9315 Power



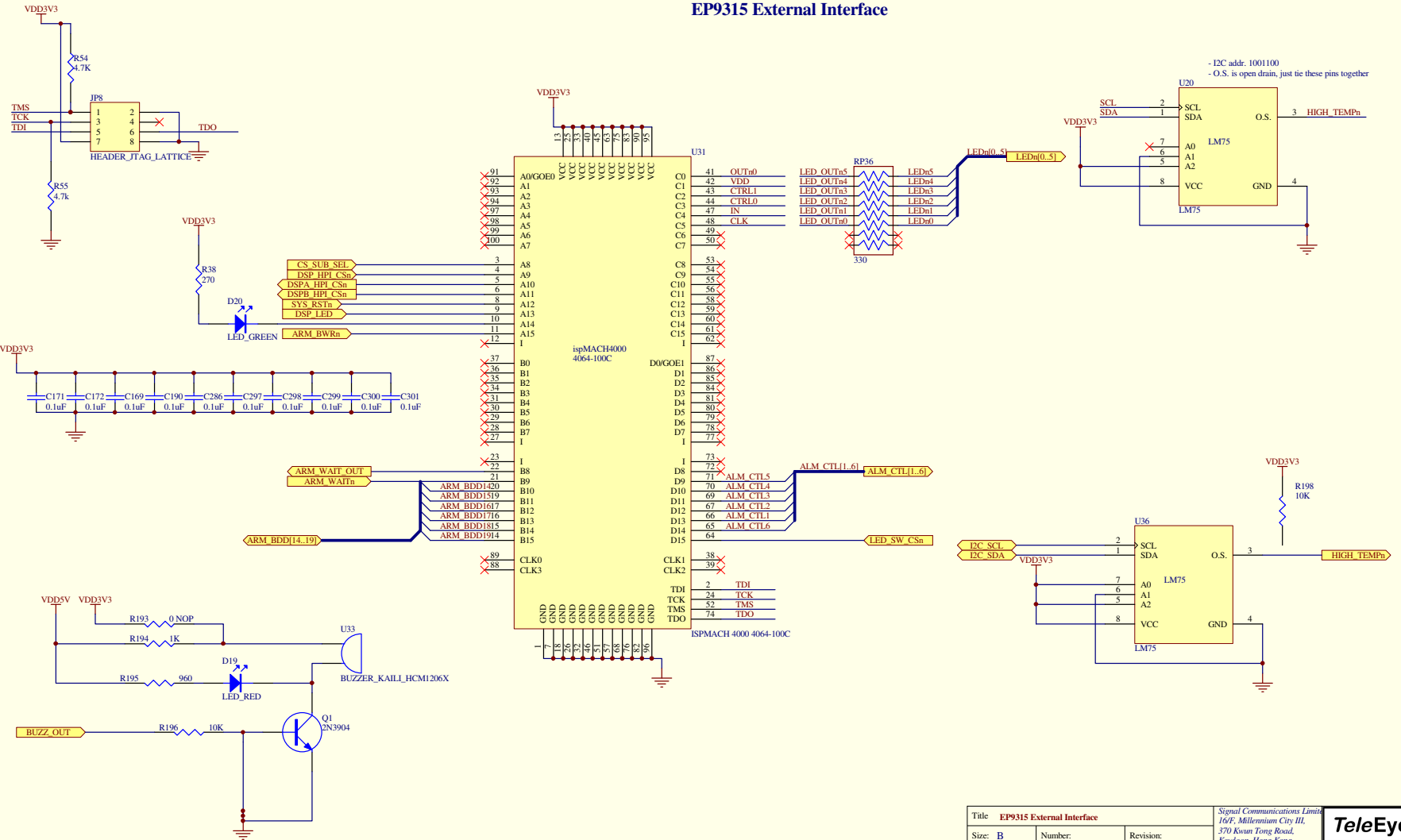
EP9315
Power



EP9315 Memory Interface



EP9315 External Interface



EP9315 Memory Buffer

