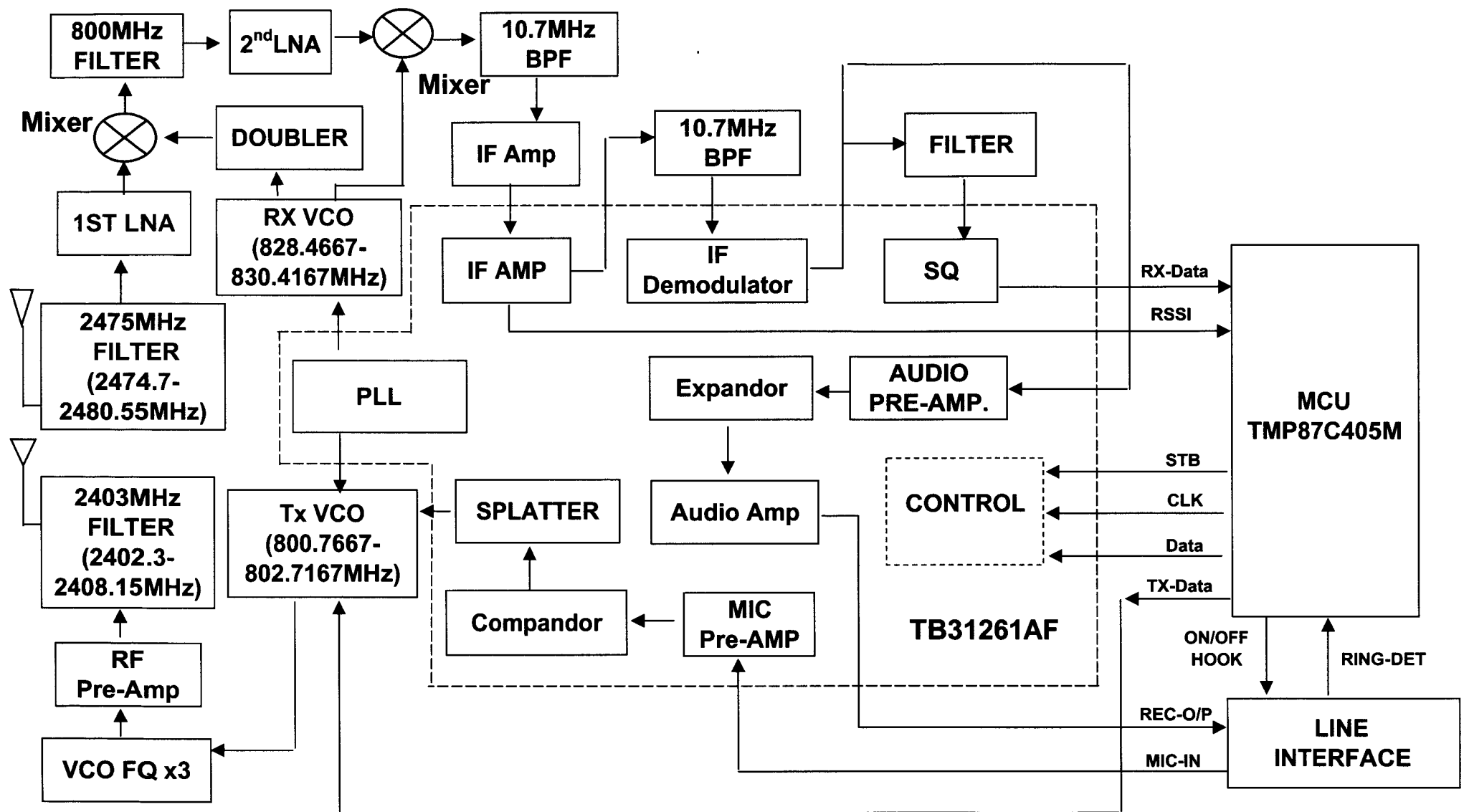
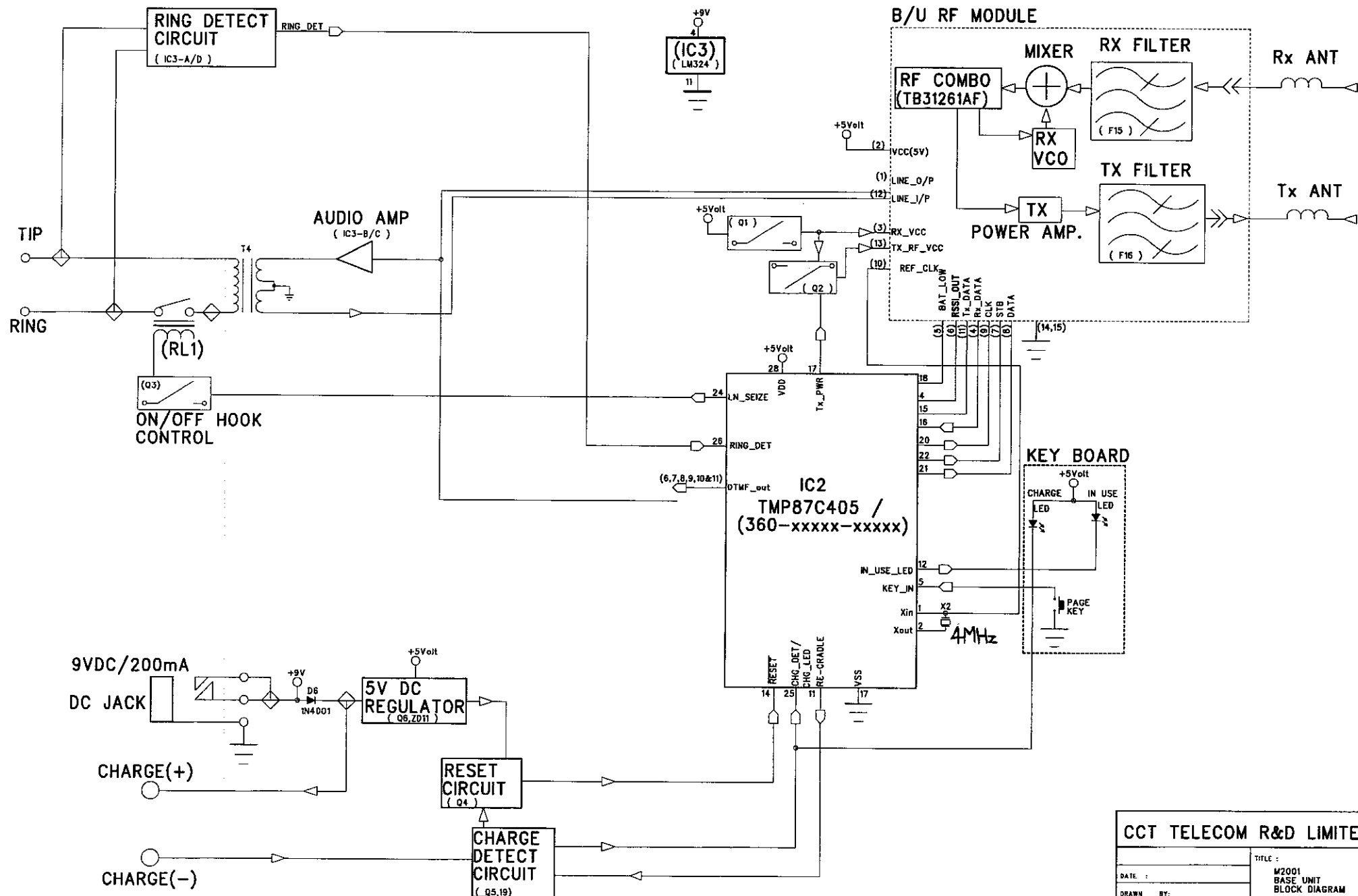


CCT TECH MA300 BASIC BLOCK DIAGRAM OF B/U SECTION



CCT TELECOM R&D LTD		
Date : 15 JULY,2002	Model: MA300 Analog Basic Phone	
Prepared by: CCT R&D	Title: 2.4GHz cordless phone	
Checked by:	(Base RF board)	
Approved by:	Sheet: 1 of 3	Rev 0A

Base Unit Functional Block Diagram of MA30X Series



CCT TELECOM R&D LIMITED

DATE :	TITLE :
DRAWN BY:	W2001 BASE UNIT BLOCK DIAGRAM
CHECKED BY:	FILE NAME: BD2001BK.SCH
APPROVED BY:	SHEET : 3 OF 3

The diagram illustrates the internal architecture of the TB31261F, a receiver/transmitter IC. It shows the flow of signals between various functional blocks:

- Transmitter Path (TX):**
 - VCO FQ x3** provides a reference frequency to the **TX VCO (824.9-826.25MHz)**.
 - TX Data** is input to the **TX VCO**.
 - The **TX VCO** output passes through an **RF Pre-Amp** and a **2475MHz FILTER (2474.7-2480.55MHz)**.
 - The signal then goes through a **Comandor** and a **SPLATTER** block before being output to the **Speaker** (pin 50).
- Receiver Path (RX):**
 - The **2403MHz FILTER (2402.3-2408.15MHz)** receives the incoming signal.
 - The signal passes through a **1ST LNA** and a **Mixer** (pin 44).
 - The **Mixer** output goes through a **DOUBLER** and another **Mixer** (pin 37).
 - The signal then passes through an **IF Amp** and an **IF AMP** (pin 40).
 - The **IF AMP** output goes through a **10.7MHz BPF** and an **IF Demodulator** (pin 33).
 - The **IF Demodulator** output goes through a **FILTER** and an **SQ** block (pin 26).
 - The **SQ** block output goes through an **AUDIO PRE-AMP.** and an **Expander** before being output to the **Speaker** (pin 50).
- Control and Interface:**
 - The **MCU TMP87C405M** is connected to the **TX VCO** (pin 3) and the **IF Demodulator** (pin 28).
 - The **MCU** is also connected to the **CONTROL** block (pins 18, 19, 20, 21, 24, 51).
 - The **MCU** is connected to the **KEY BOARD** and **IN-USE LED**.
 - The **MCU** is connected to the **RING SELECT** and **Buzzer**.
 - The **MCU** is connected to the **Speaker** (pin 50).

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