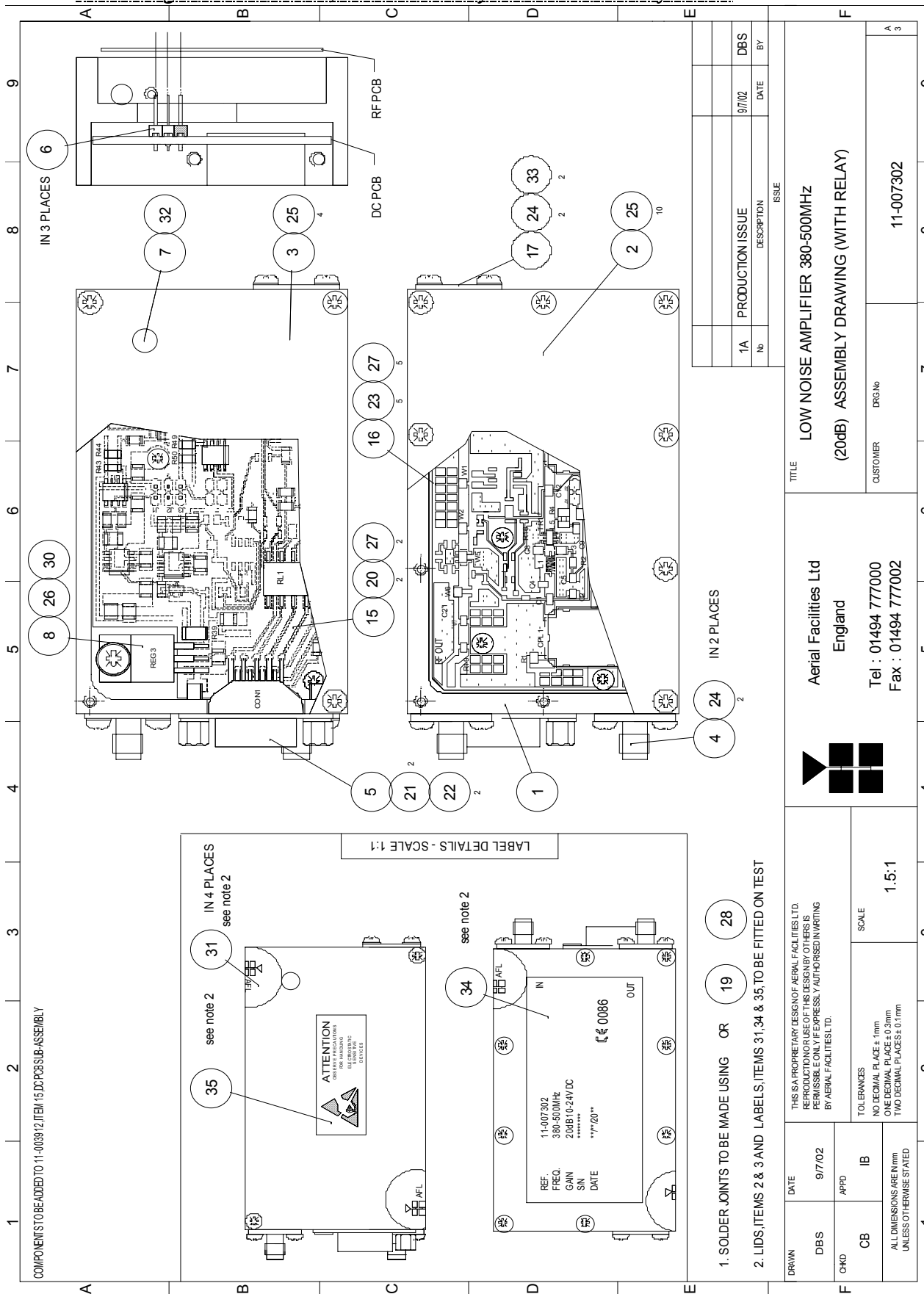




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Technical Literature

Maintenance Handbook

H/book Number:-

Issue No:-

Date:-

Page:-

1	2	3	4	5	6	7	8	9
The diagram shows a two-stage LNA circuit. The RF input is connected through a matching network (C1, C2, C3, C4) to the gate of a MOSFET (M1). The drain of M1 is connected to a matching network (C5, C6, C7, C8) and the gate of a second MOSFET (M2). The drain of M2 is connected to a matching network (C9, C10, C11, C12) and the RF output. The circuit includes various passive components: resistors (R1-R10), capacitors (C1-C12), and inductors (L1-L4). A header pin (J1) is used for the RF output. An attenuator group is shown in a dashed box, consisting of resistors R9 and R10. The circuit is powered by a 1.5V battery.								
RF OUTPUT								
RF INPUT								
ATTENUATOR GROUP (Selected values on Test)								
LOW NOISE AMPLIFIER 380-500MHZ (20dB) RF PCB CIRCUIT DIAGRAM								
Aerial Facilities Limited England								
Tel : 01494 777000 Fax : 01494 777002								
11-007370								



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Page:-

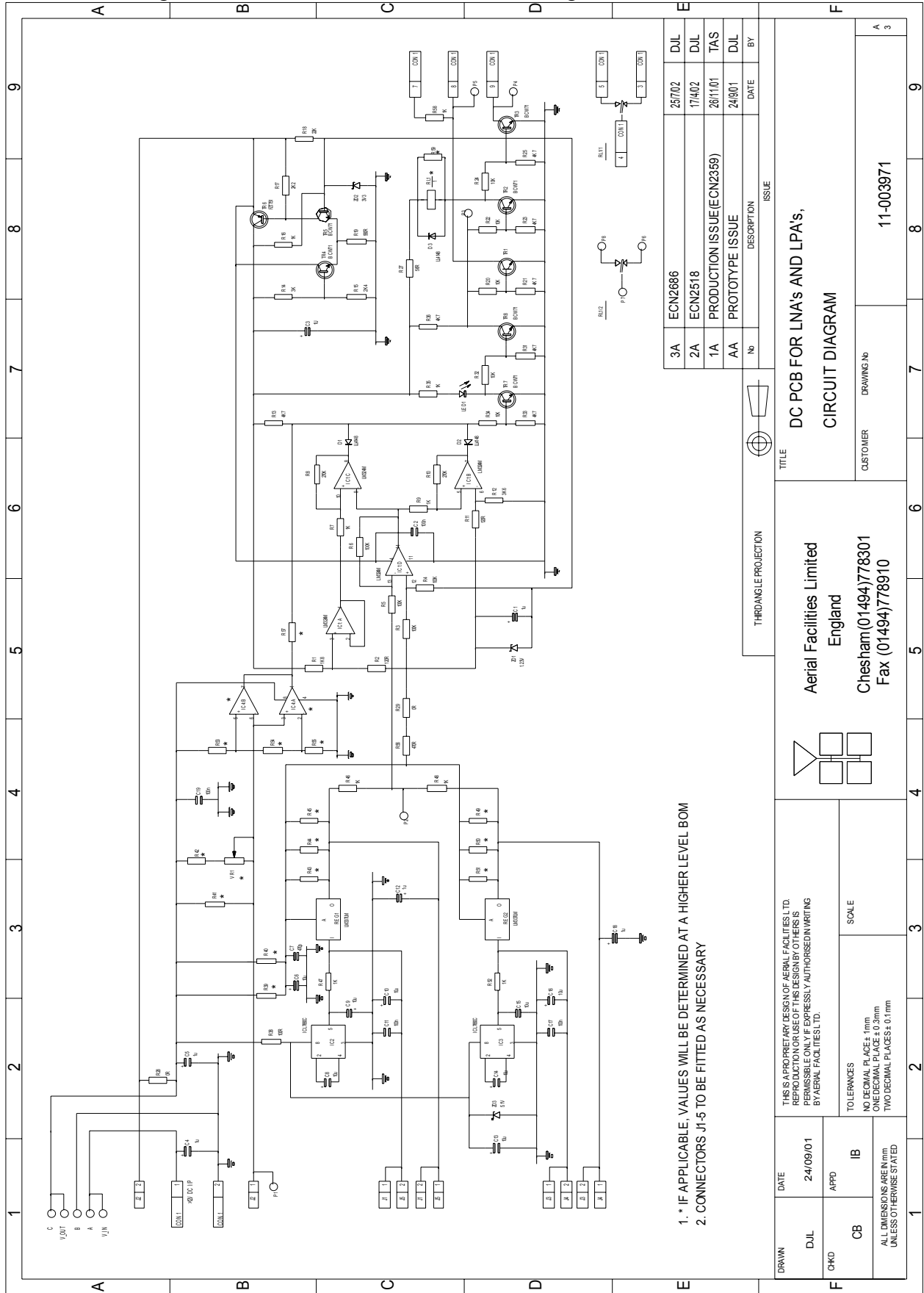
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Issue No:-

Page:



Drg. Nō. 11-003971, LNA DC Schematic Diagram



TITLE DC PCB FOR LNA's AND LPA's, CIRCUIT DIAGRAM		CUSTOMER Aerial Facilities Limited England Chesham(01494)778301 Fax (01494)778910		DRAWING No 11-003971	
DATE 24/09/01		SCALE NO DECIMAL PLACES, 1mm ONE DECIMAL PLACE, 0.1mm TWO DECIMAL PLACES, 0.01mm UNLESS OTHERWISE STATED		ISSUE No. 1 DESCRIPTION DATE BY	