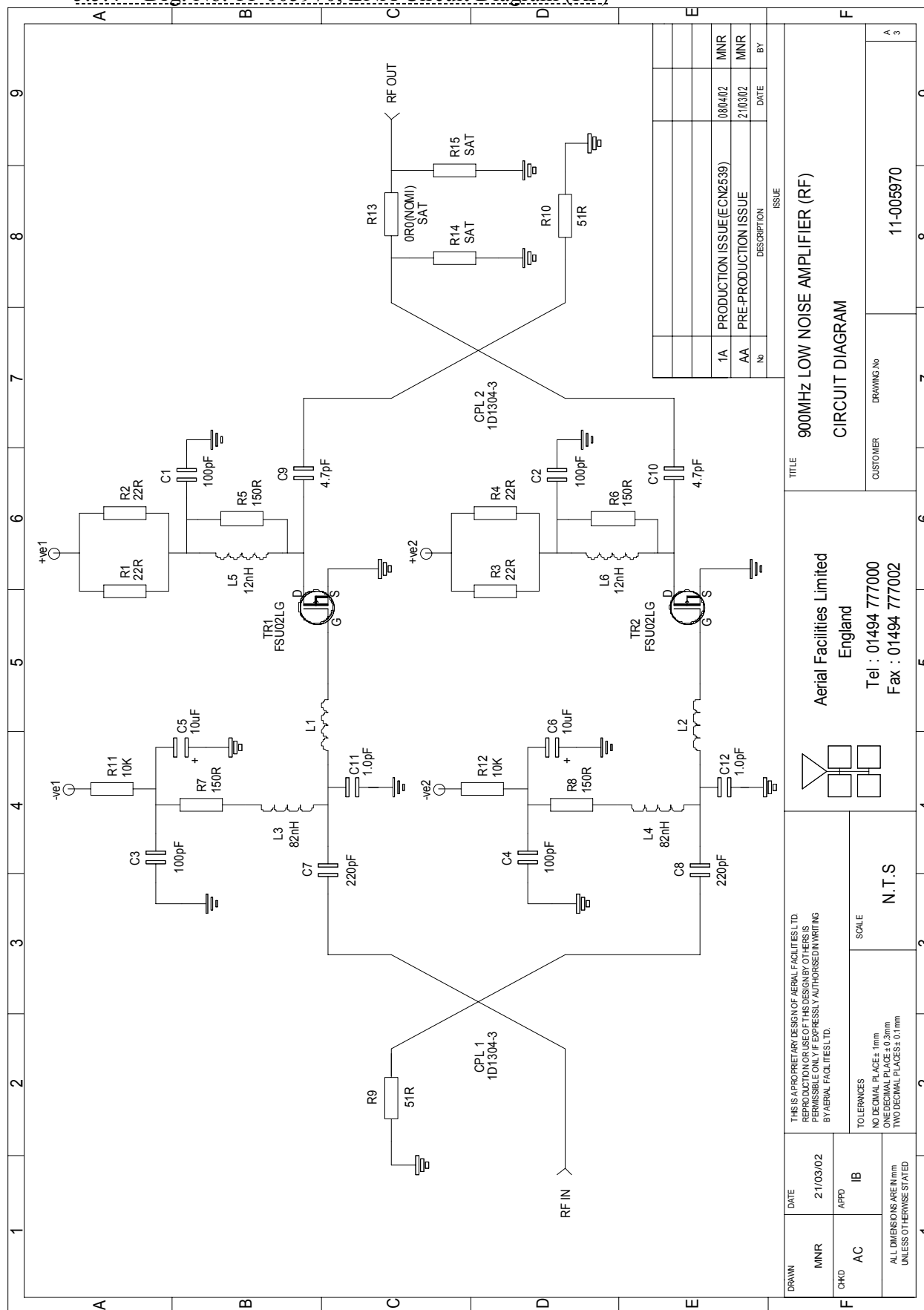


5.5.4 Drg. Nō. 11-005970, LNA Circuit Diagram (RF)



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Technical Literature

800MHz BDA (Pasadena Figuero Tunnel)
Maintenance Handbook

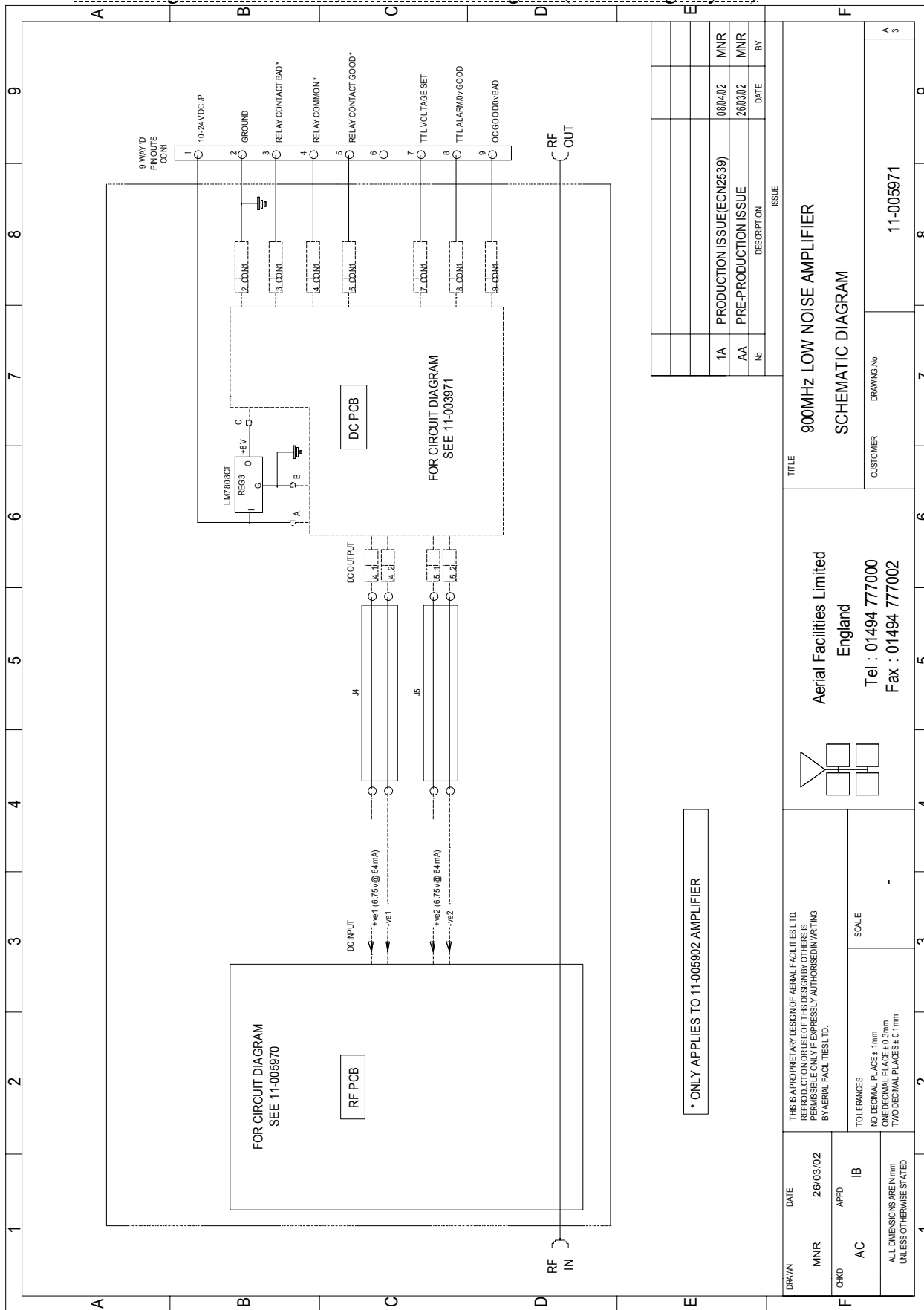
H/book Number:-60-056100HBKM

Issue No:-1

Date:-15/10/2003

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5.5.5 Drg. Nō. 11-005971, LNA Circuit Diagram (DC Wiring Layout).



800MHz BDA (Pasadena Figuero Tunnel)

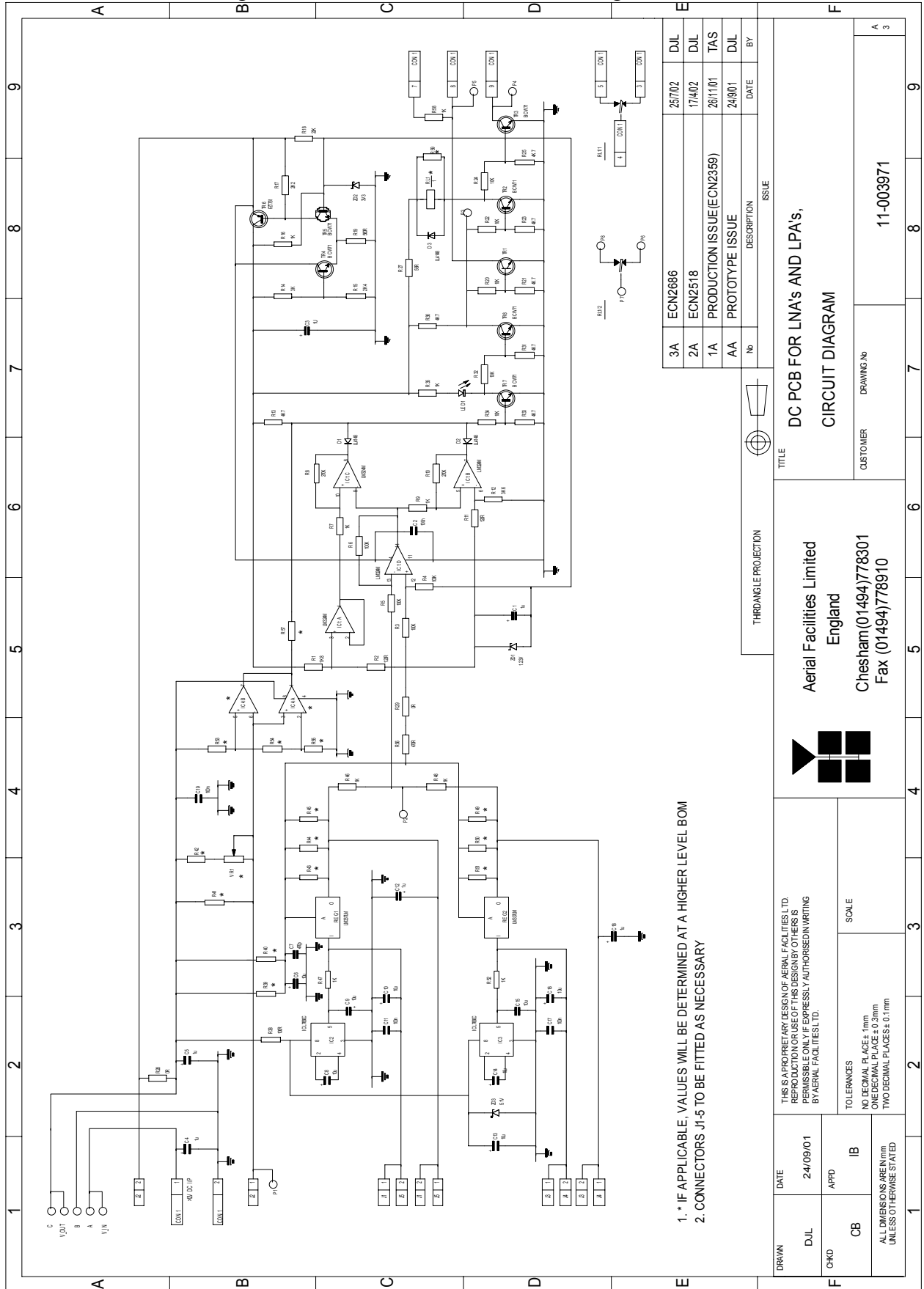
H/book Number:-60-056100HBKM

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5.1.5.6 Drg. No. 11-003971, LNA DC Circuit Diagram



1. * IF APPLICABLE, VALUES WILL BE DETERMINED AT A HIGHER LEVEL BOM
2. CONNECTORS J1-5 TO BE FITTED AS NECESSARY

DRAWN		DATE		DUL		ECN2686		25702		DUL	
CHKD		APPD		IB		ECN2518		17402		DUL	
CB		TAS		AA		PRODUCTION ISSUE (ECN2359)		261101		DUL	
ALL DIMENSIONS ARE IN mm UNLESS OTHERWISE STATED		SCALE		TOLERANCES		NO DECIMAL PLACES ± 1mm ONE DECIMAL PLACE ± 0.1mm TWO DECIMAL PLACES ± 0.01mm		DESCRIPTION		DATE	
TITLE		THIRD ANGLE PROJECTION		DC PCB FOR LNA's AND LPA's, CIRCUIT DIAGRAM		No		ISSUE		BY	
CUSTOMER		DRAWING No		11-003971		A		3		9	