

ENPC TECHNOLOGY CORP.

ENPC-100 Ver. 1.00

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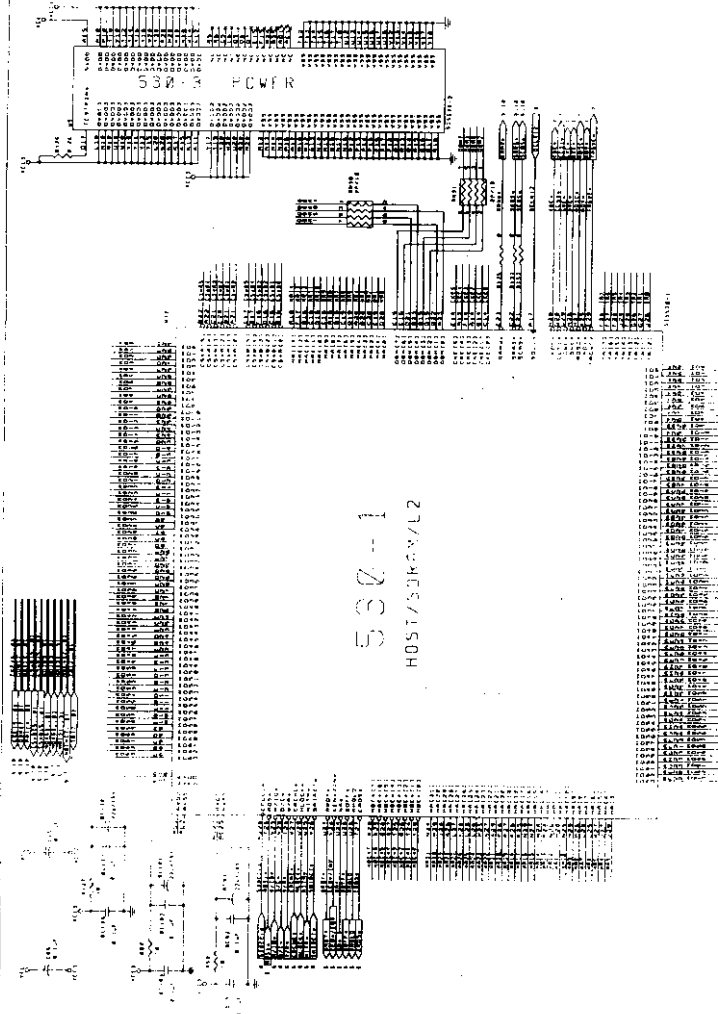
ENPC-100

文件管理

禁止印刷

禁止印刷

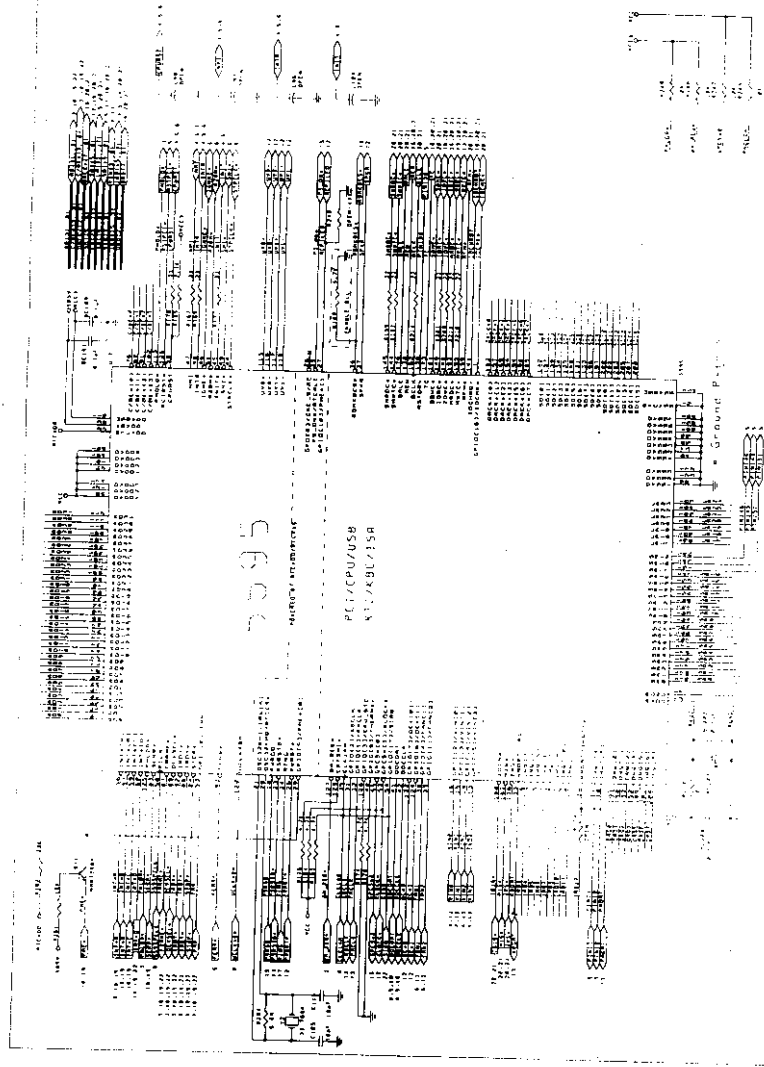
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REV.	1.1000000000000000



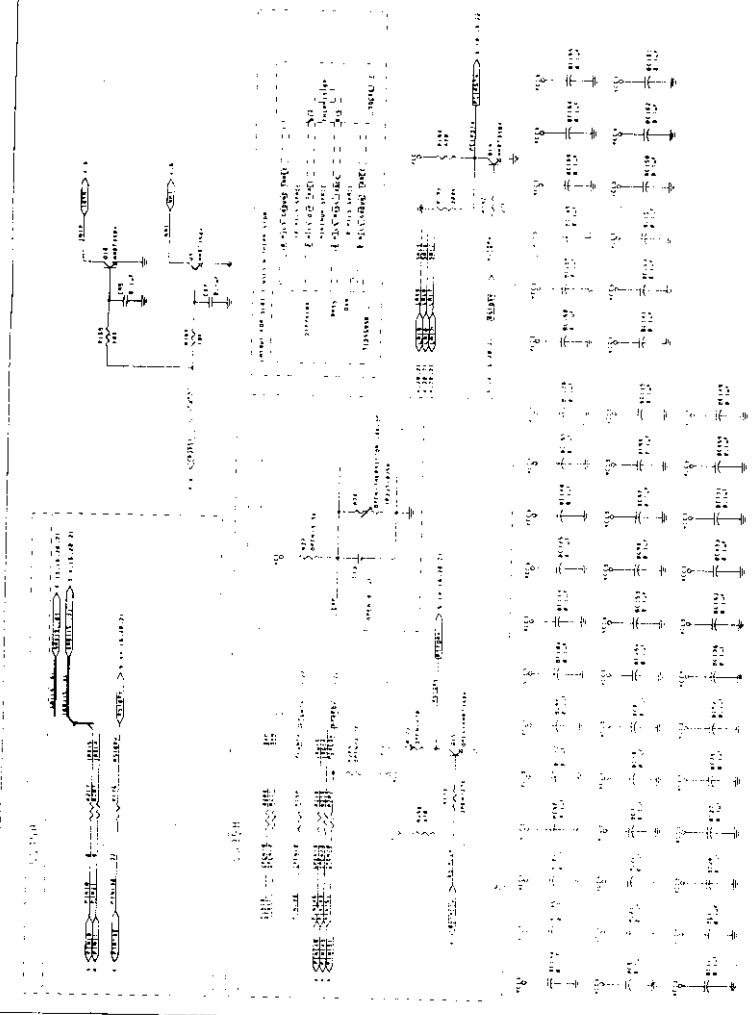
530-1

作發行
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FILE	3335	
BY	CHEN, H. H.	
CHKD	CHEN, H. H.	

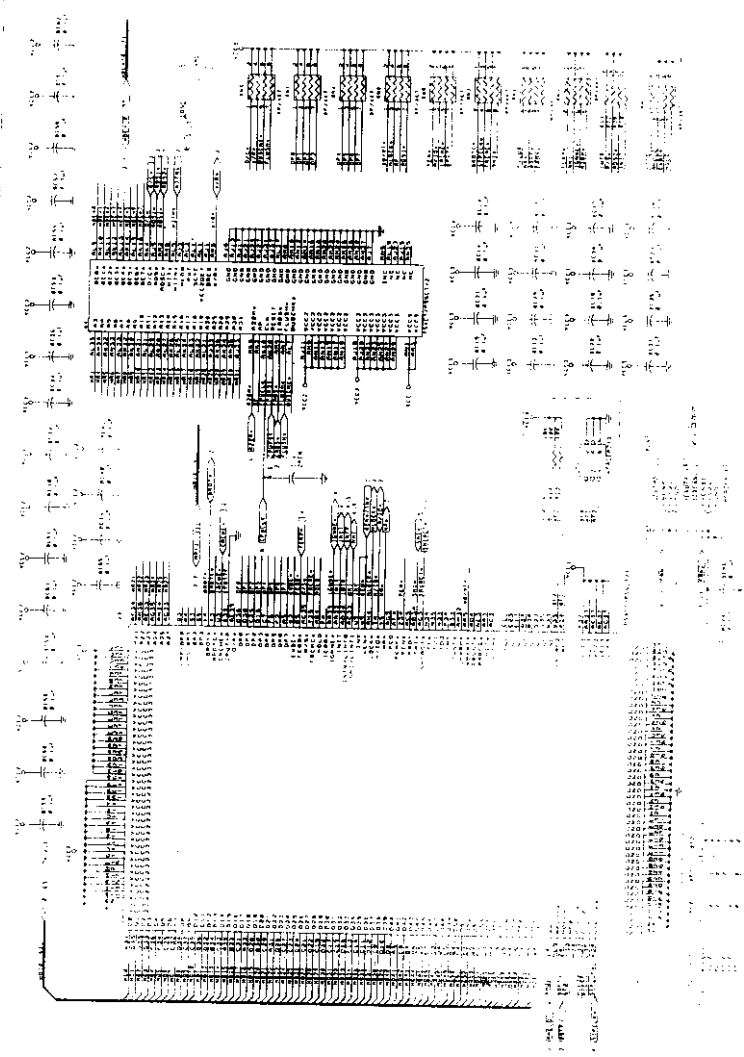


100-100-100
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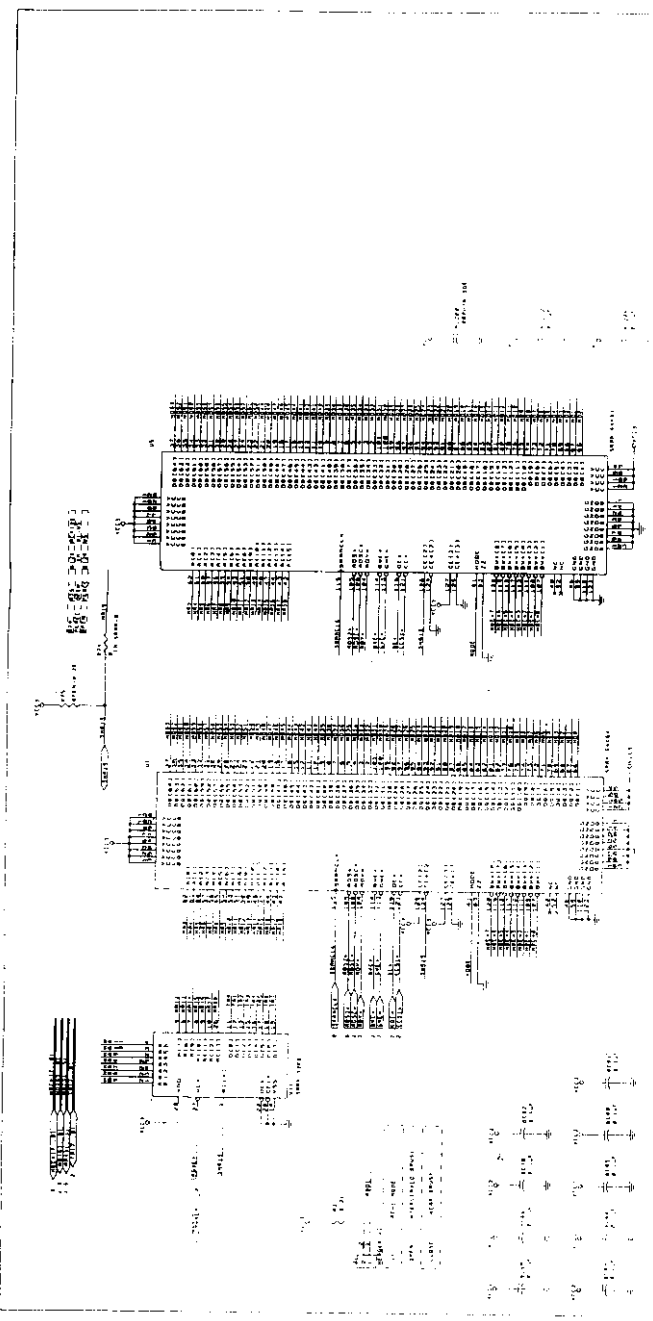
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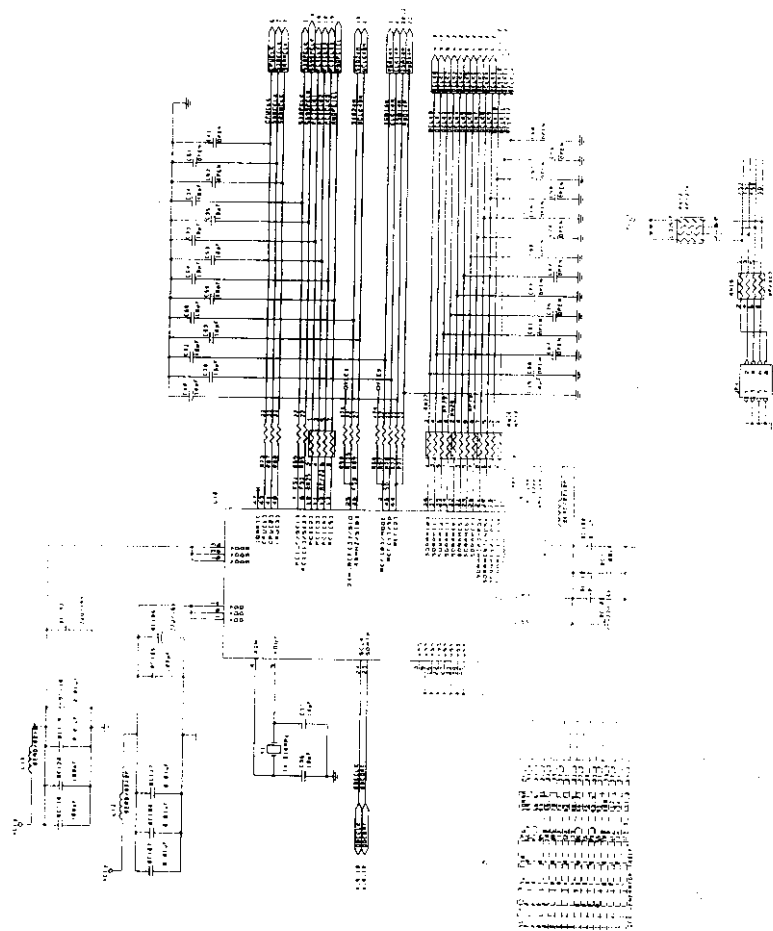
EPC (GENERAL) CAP	
PART 1/2	
DATE: 1999.01.12	
DRAWN BY: 1111111111	
CHECKED BY: 1111111111	
APPROVED BY: 1111111111	



件發行章
禁止膠印

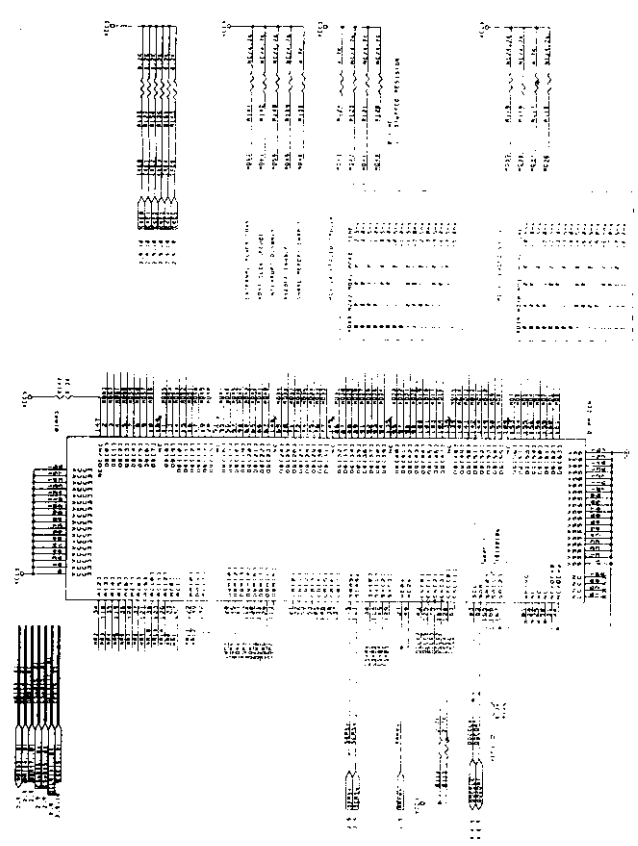
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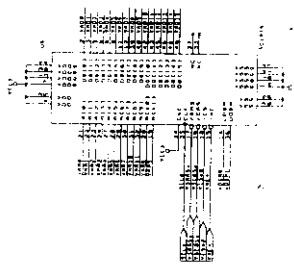
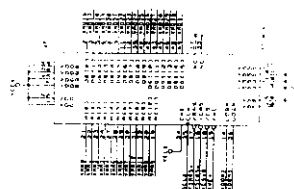
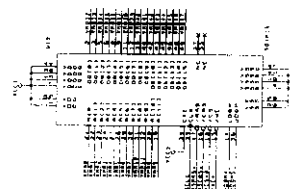
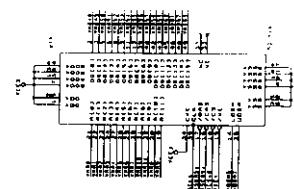
文件设计
禁止

DATE	1970-06-10
TIME	10:00
BY	W. J. B. J. B. J. B.
NO.	100



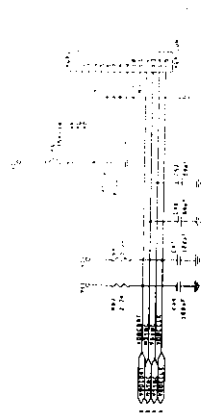
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禁止影印

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BY	100-100000-100
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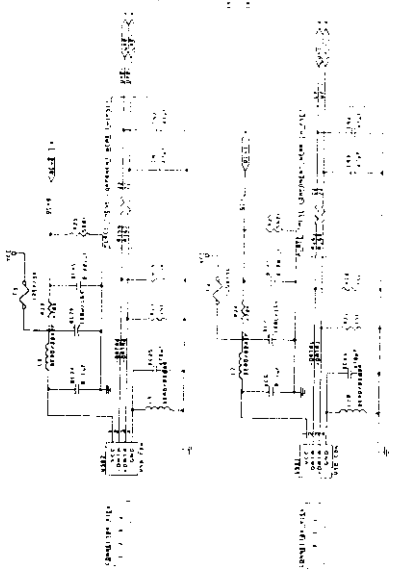
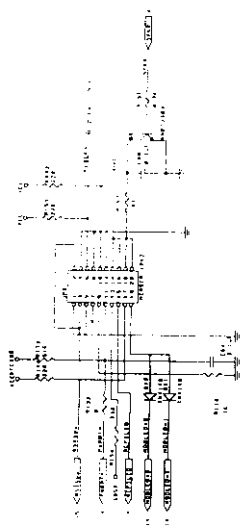
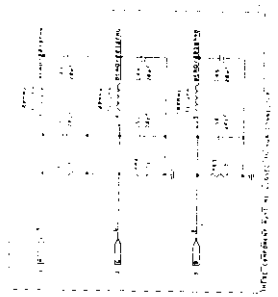


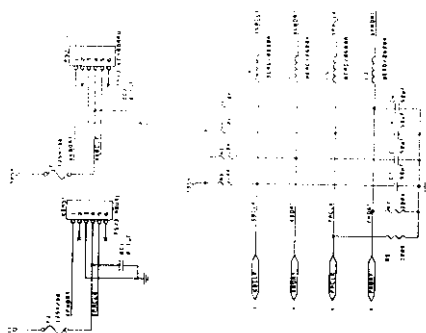
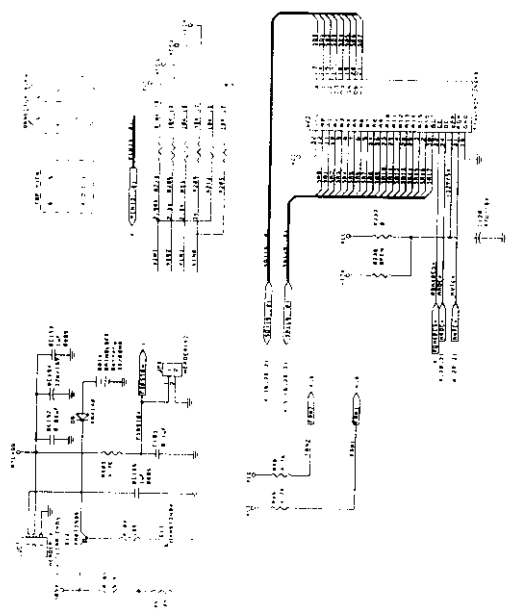
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BY	W. J. B. (11/11/64)
CHKD	W. J. B. (11/11/64)
APP'D	W. J. B. (11/11/64)
REV	

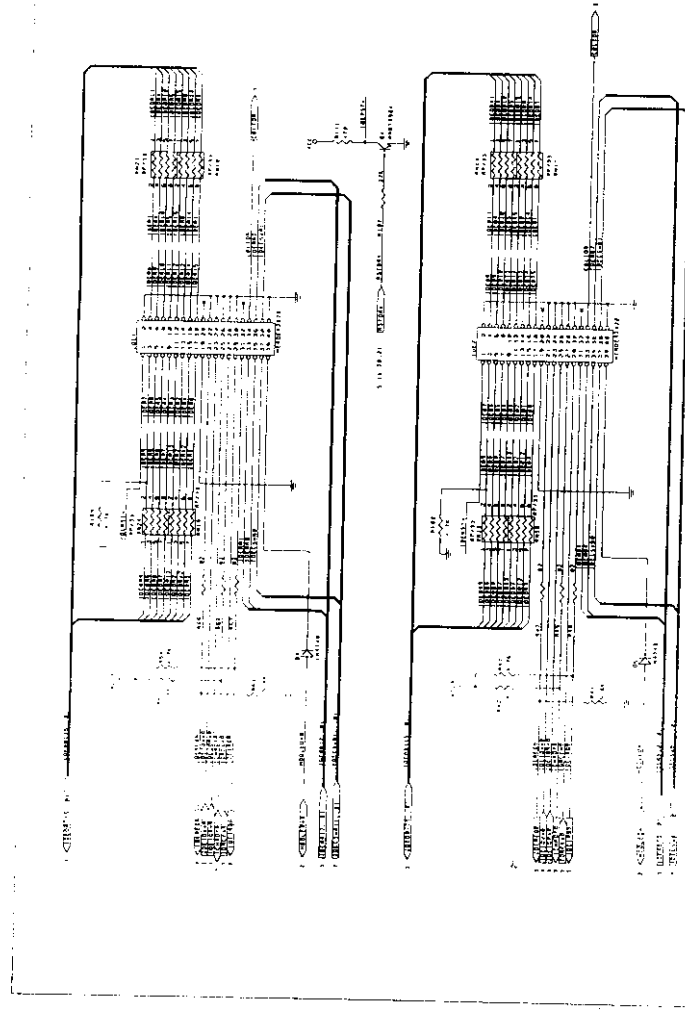
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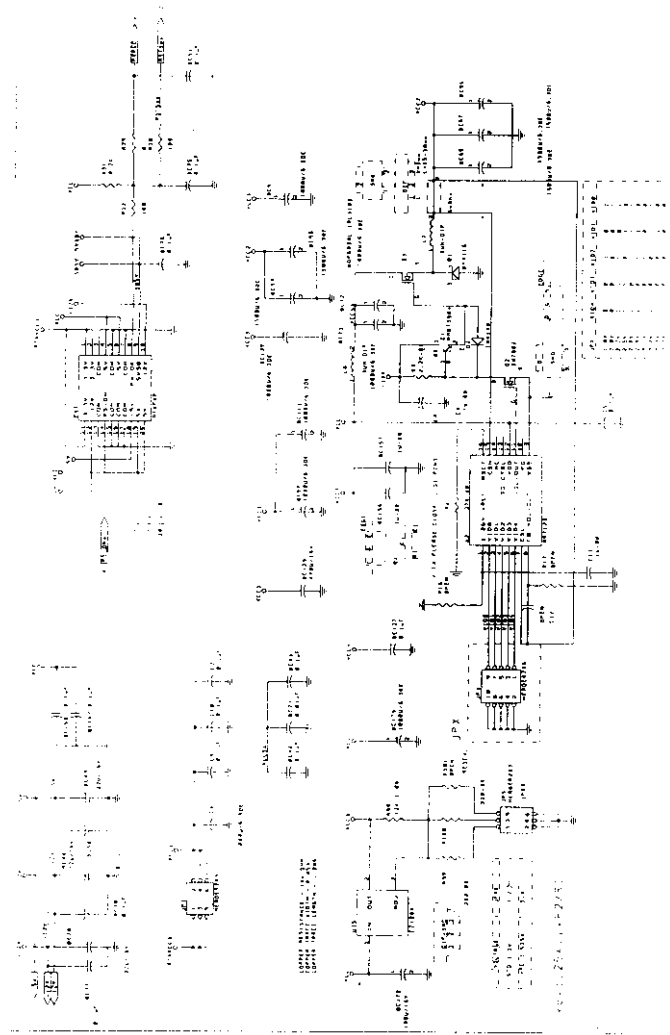
1. This circuit is designed to provide a regulated output voltage of 1.5V at 100mA. The output voltage is adjustable by varying the potentiometer. The circuit is powered by a 100V AC source.







图号	1000-100000-1000
比例	1:1
材料	1000-100000-1000
备注	

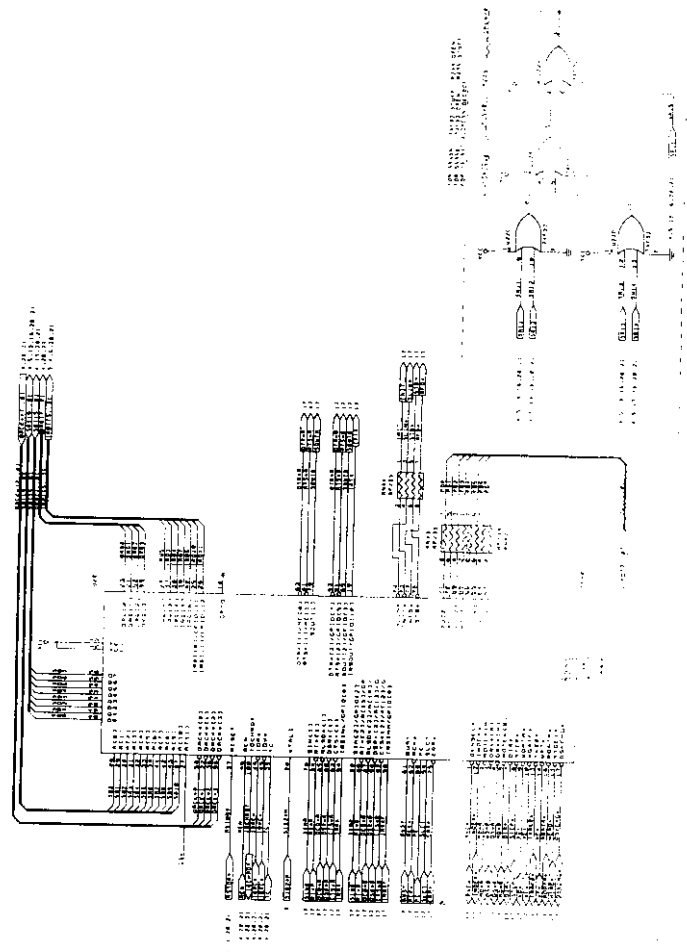


1000-100000-1000

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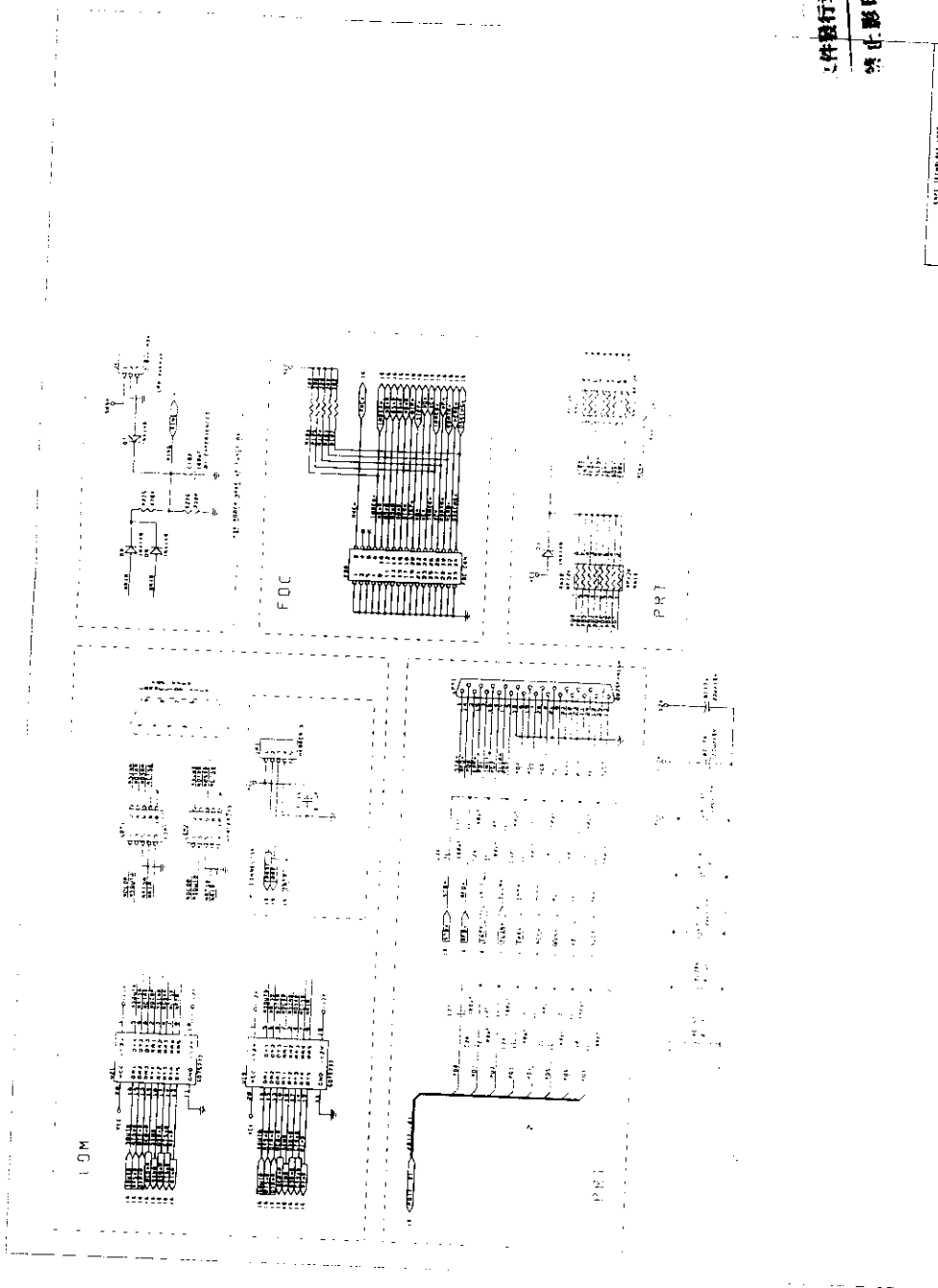
1000-100000-1000

DATE	TIME	NO.
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RECEIVED		

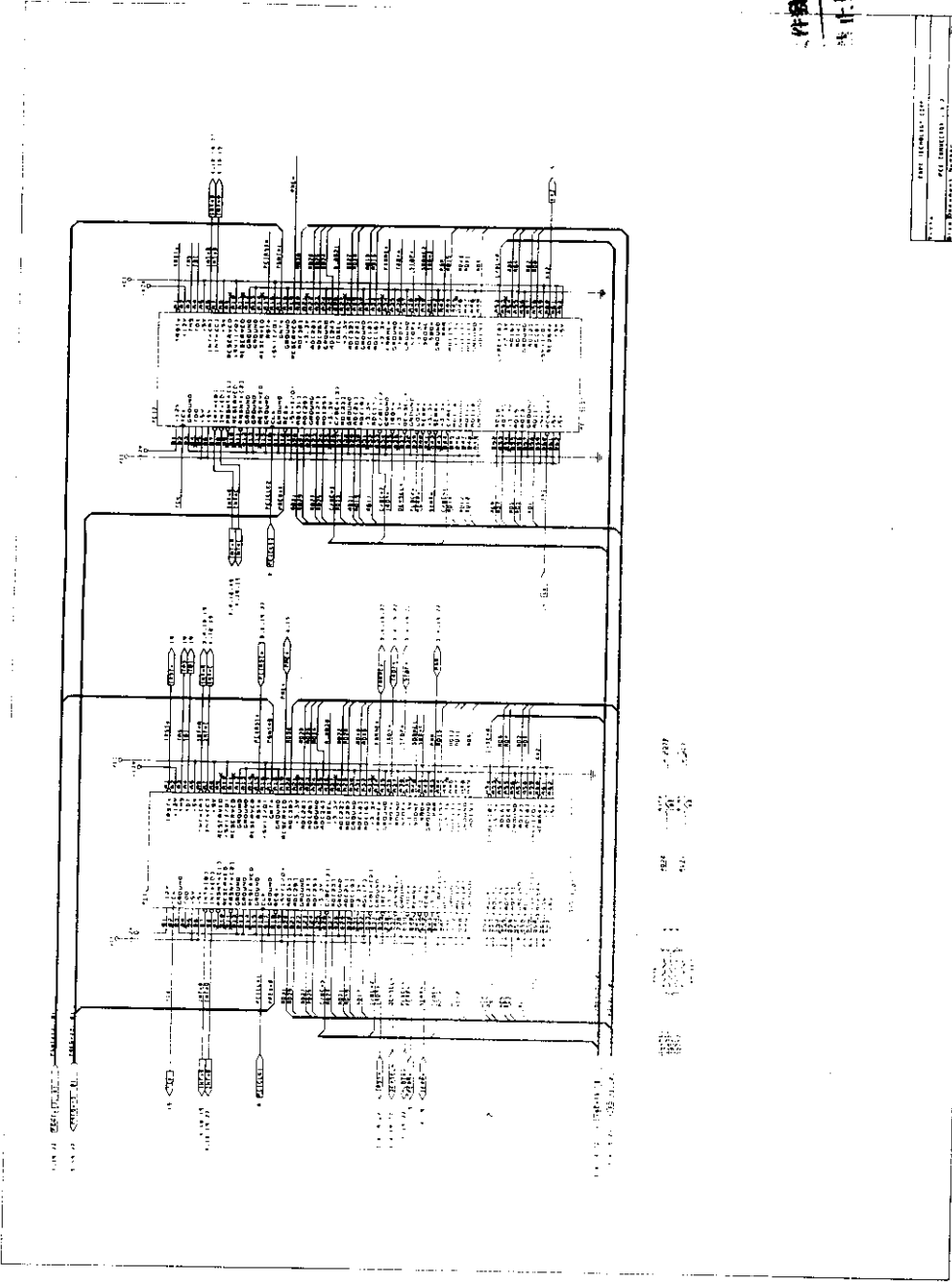


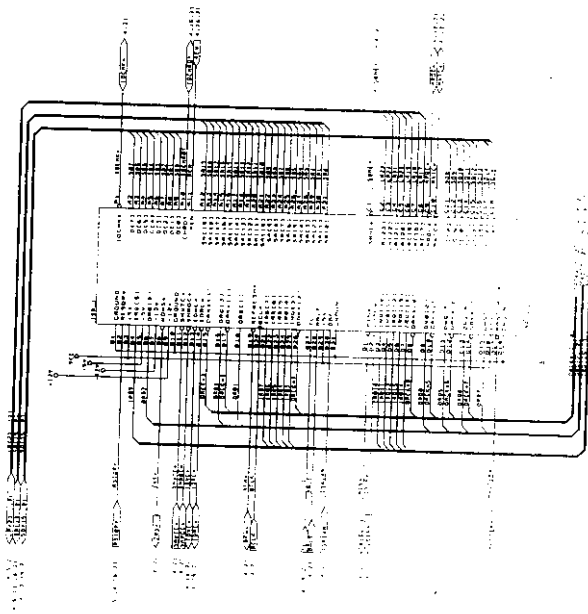
体發行章
禁止影印

NAME	DATE
ADDRESS	TELEPHONE
CITY	STATE
COUNTRY	POSTAL CODE

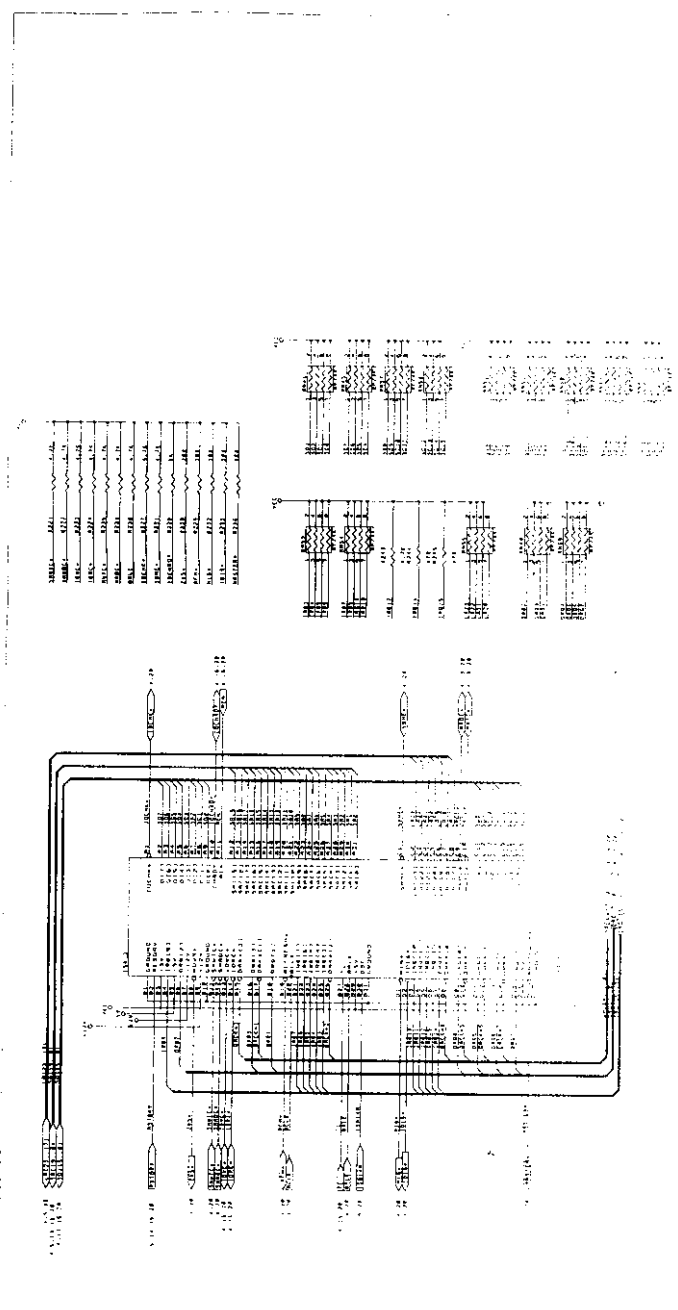


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图名	图号	比例	日期
图例	图例	图例	图例
图例	图例	图例	图例
图例	图例	图例	图例
图例	图例	图例	图例



设计
审核
校对
制图

图号	1000-1000-1000
比例	1:1
日期	1980.10.10
设计	张明
审核	李华
校对	王强
制图	赵刚

