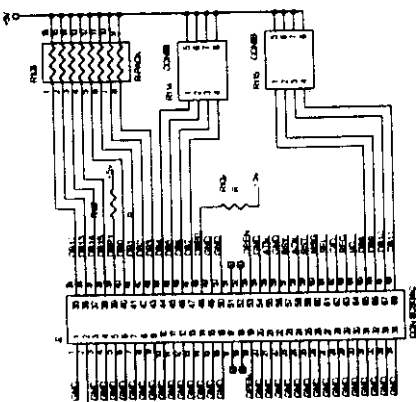
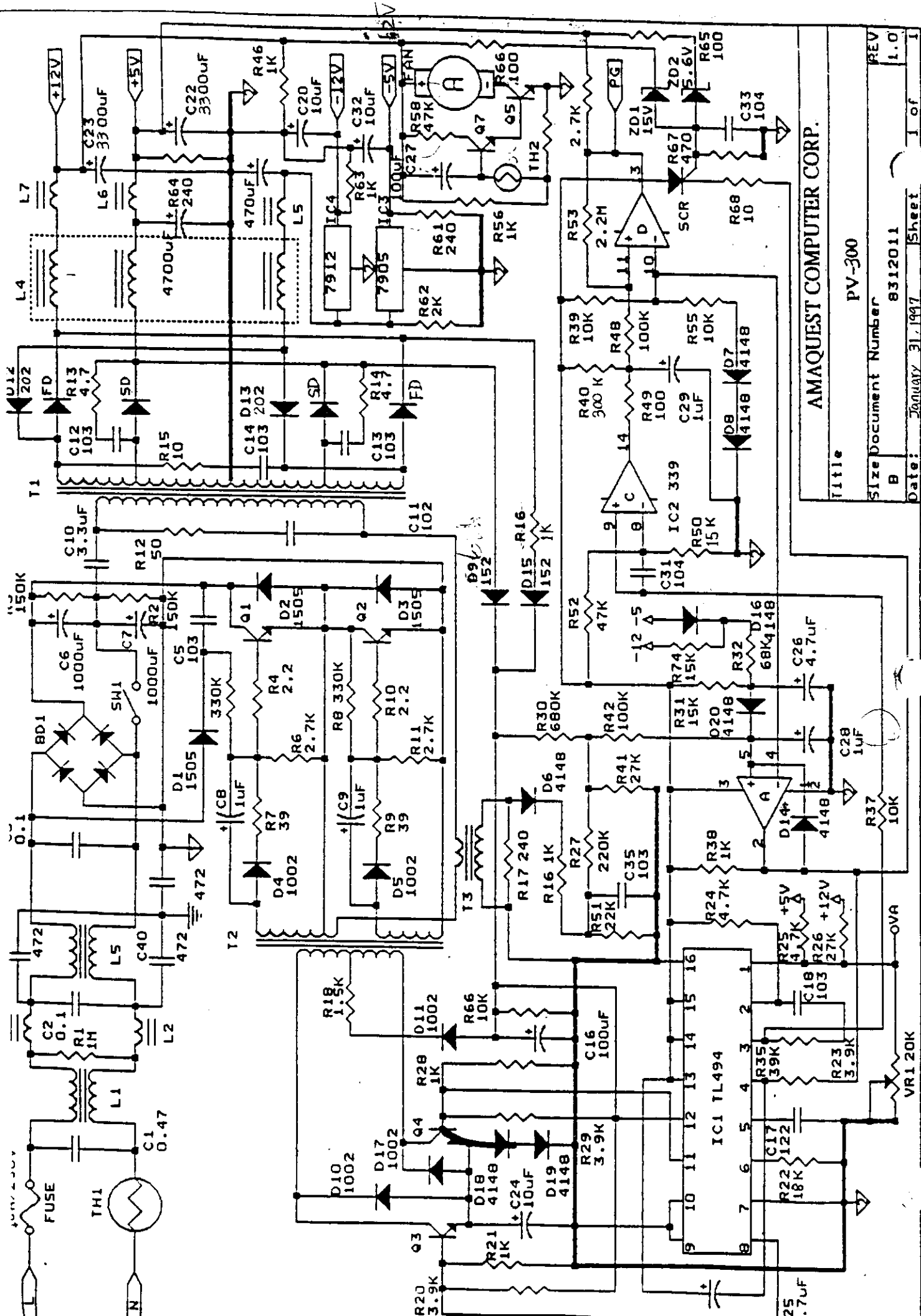


EXHIBIT D

Circuit Diagram





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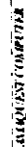
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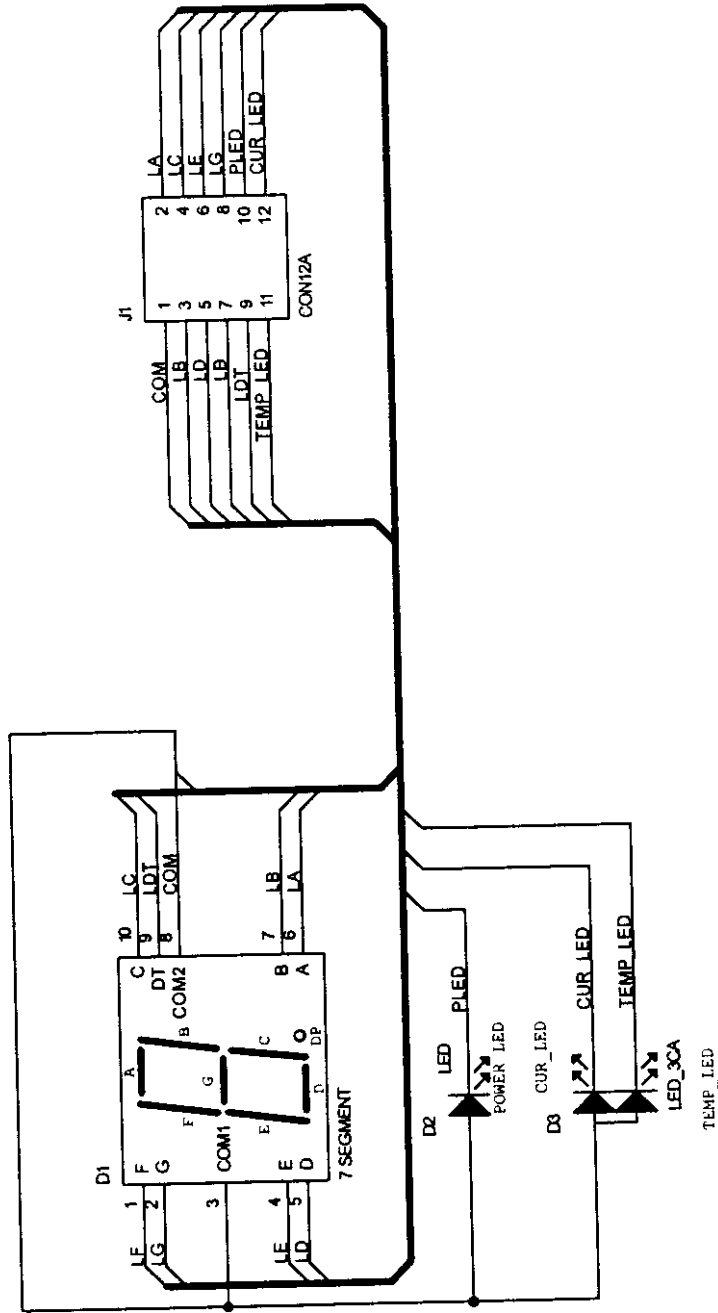


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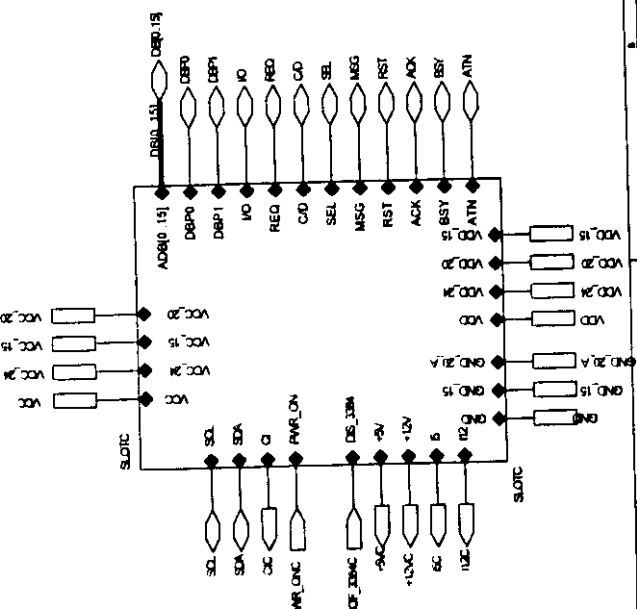
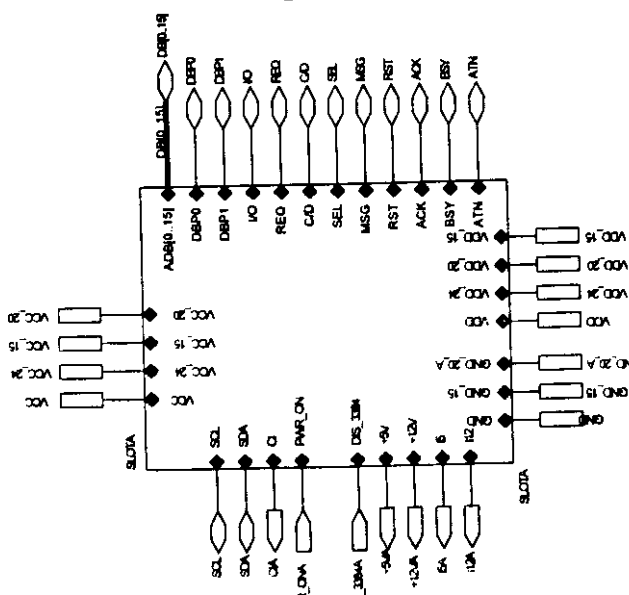
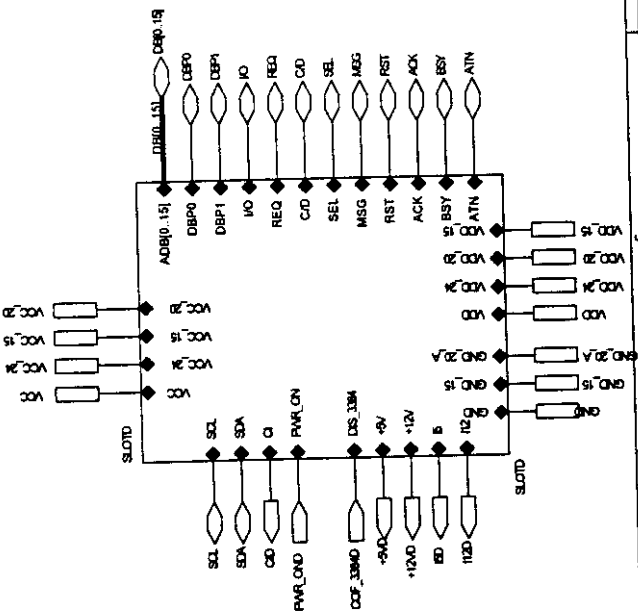
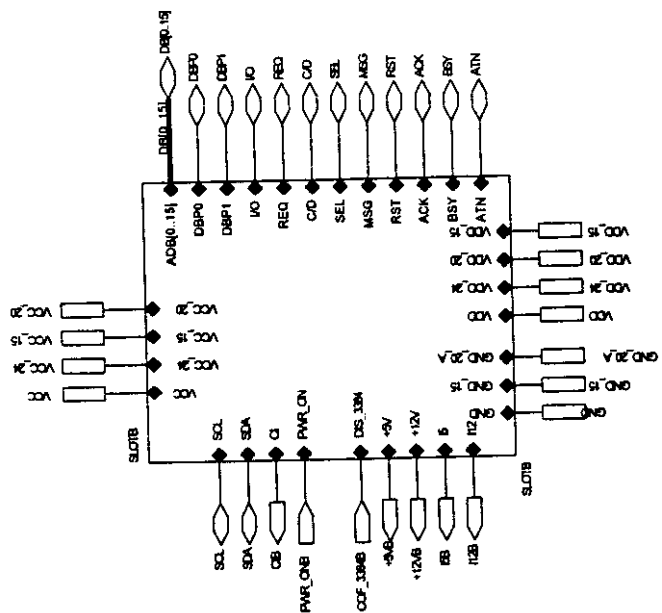
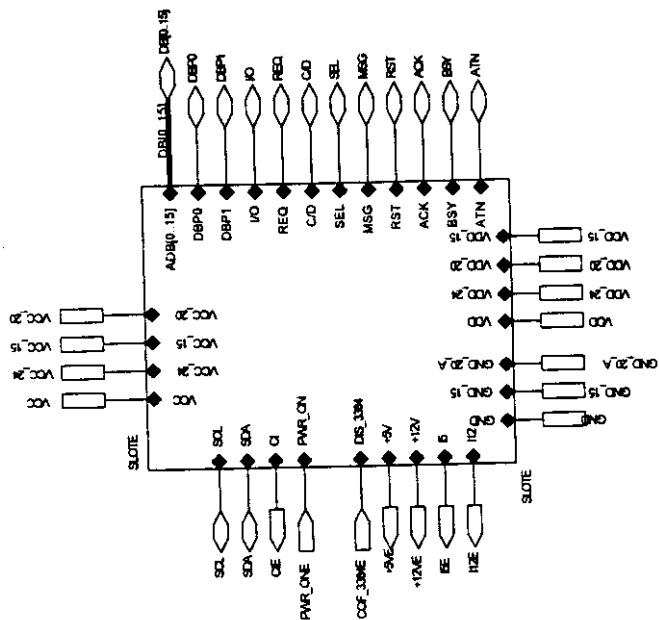
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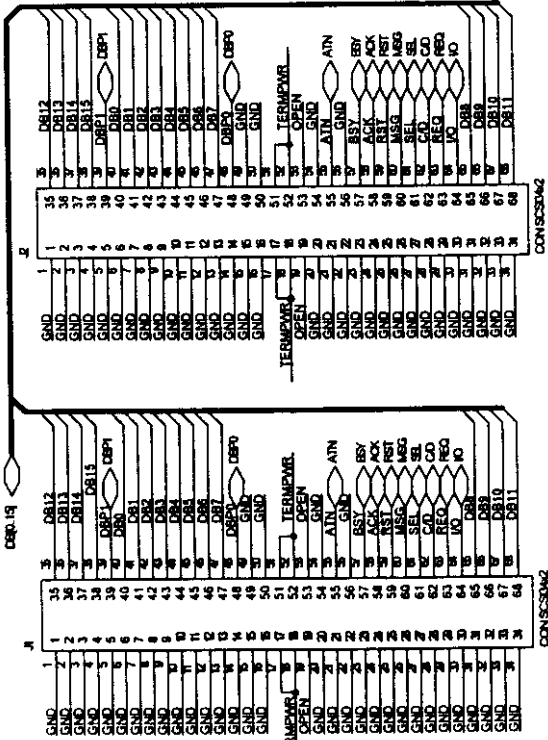
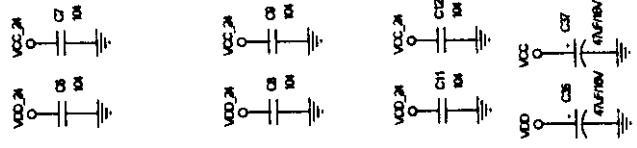
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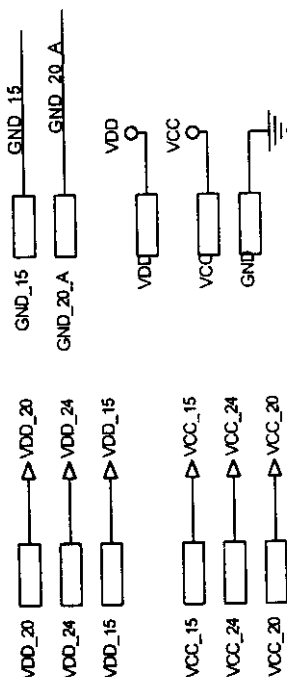
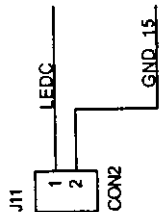
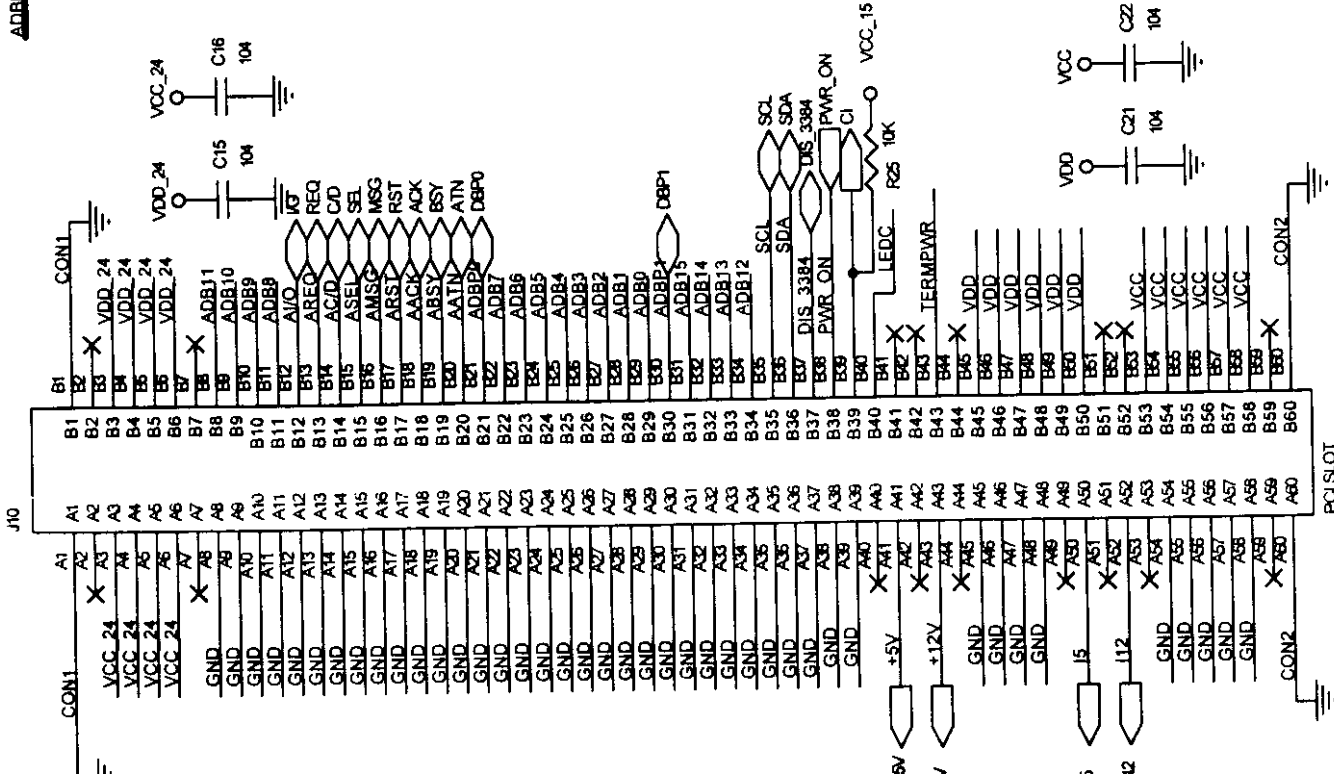


ANALOGUE COMPUTER CO., LTD.
RAID BACKPLANE 5

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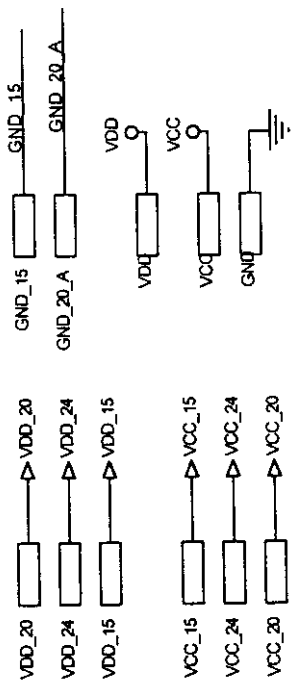
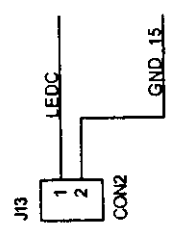
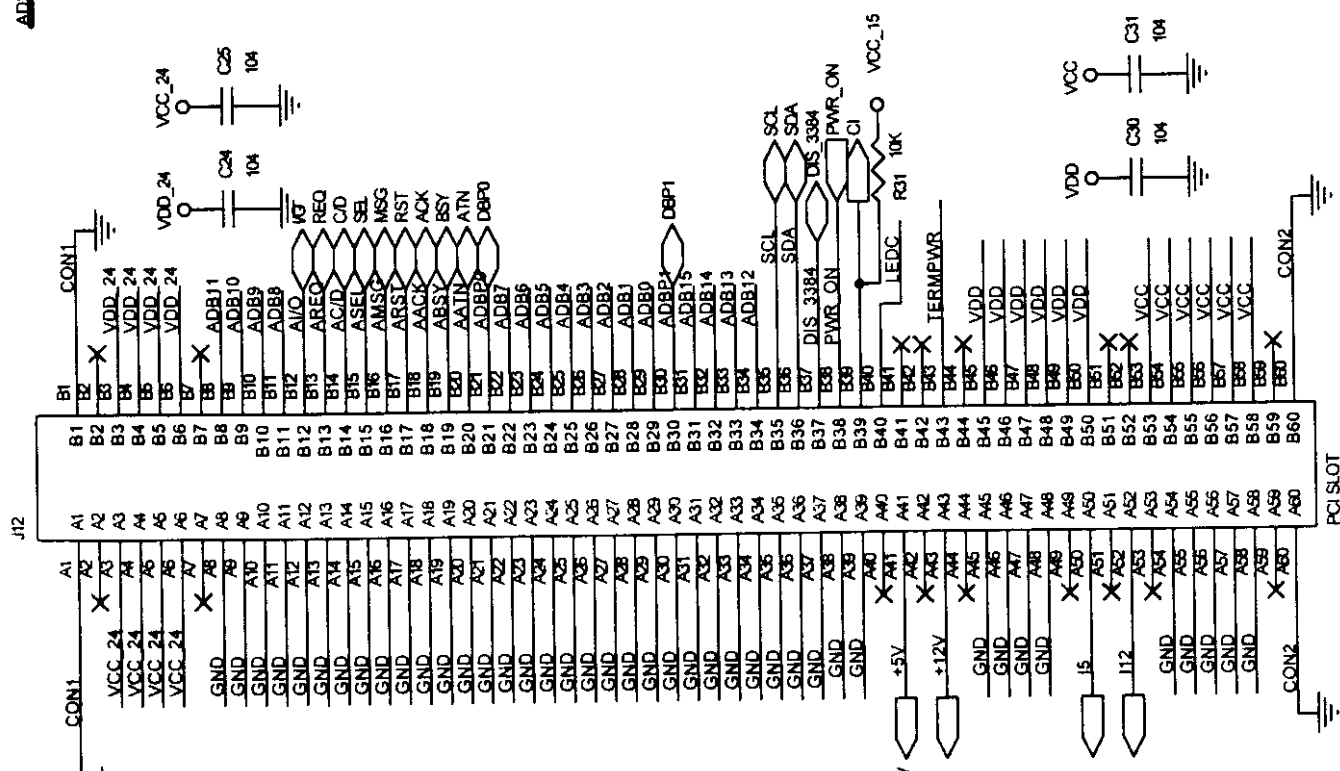
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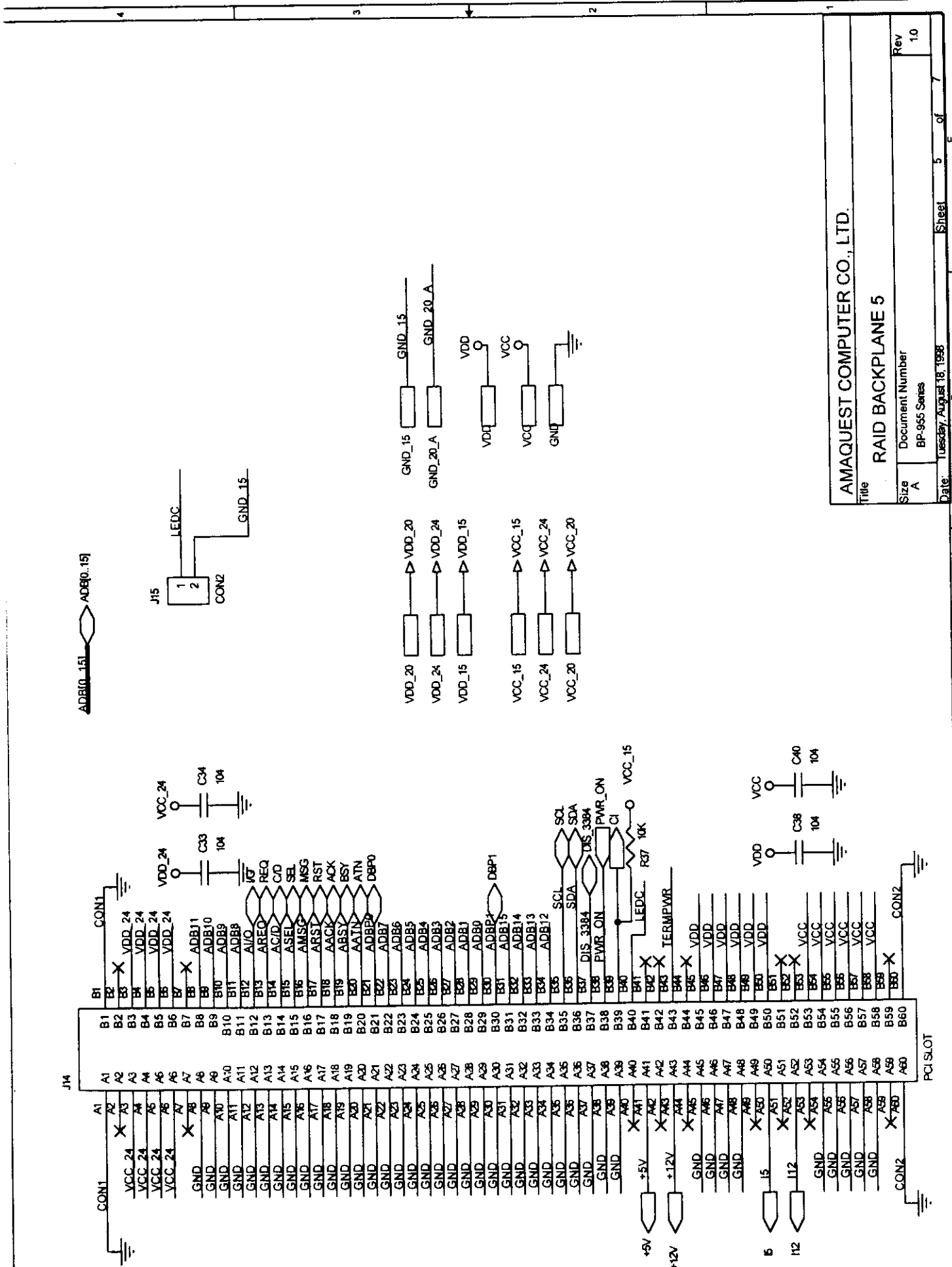
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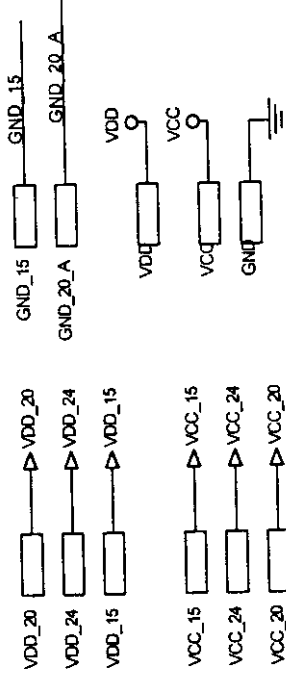
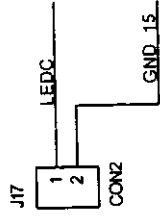
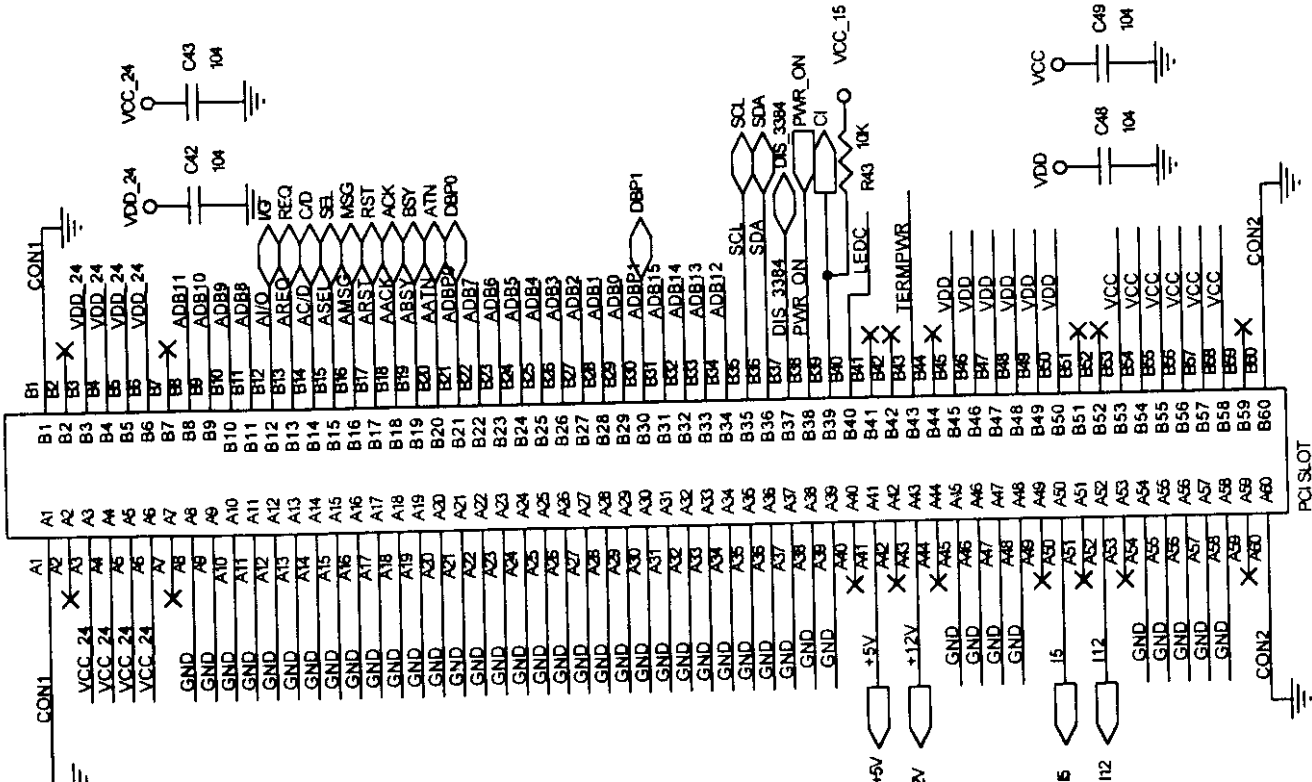
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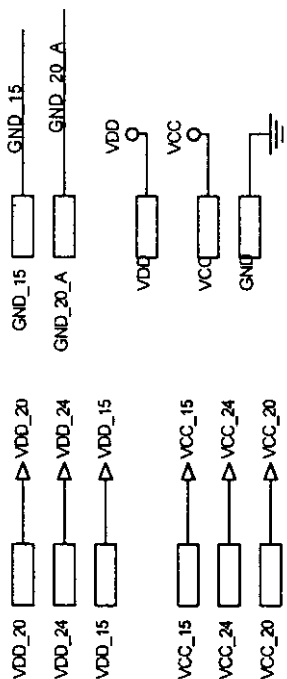
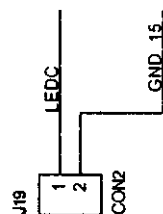
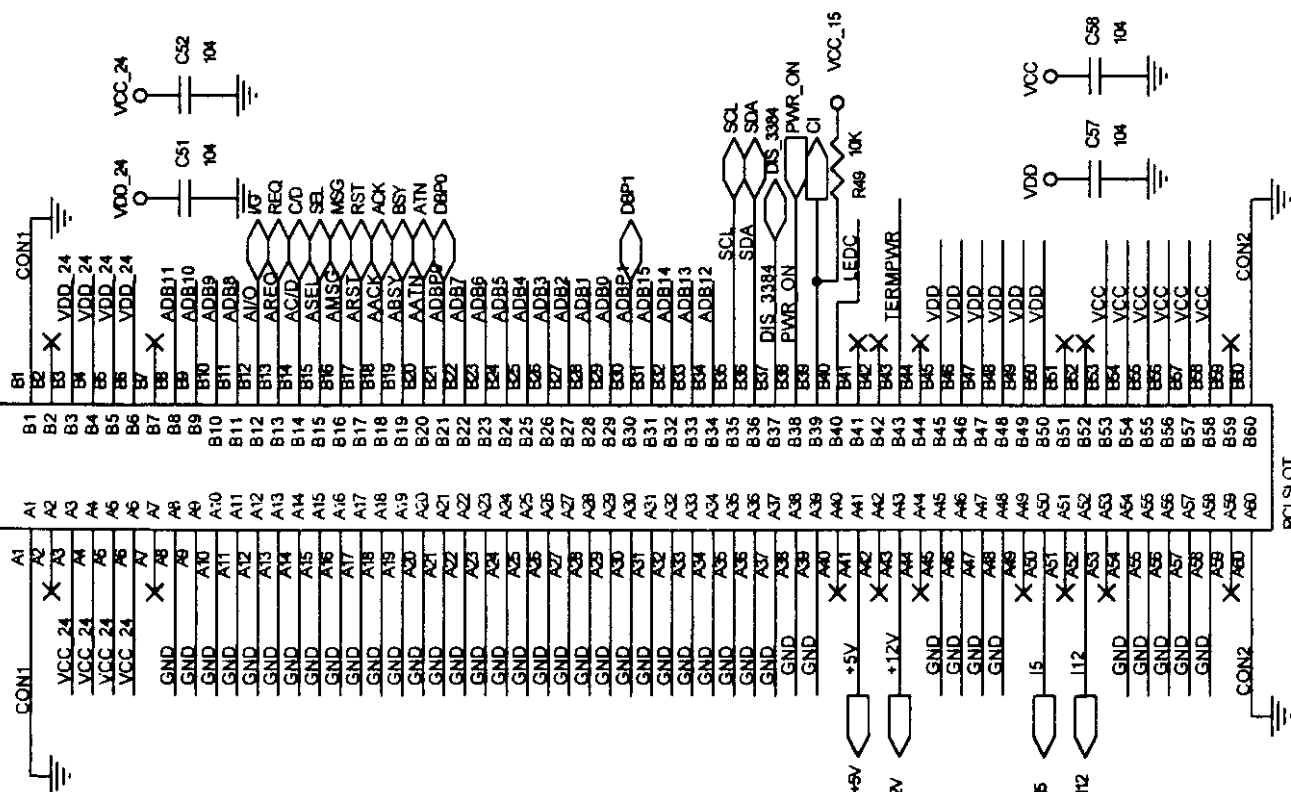


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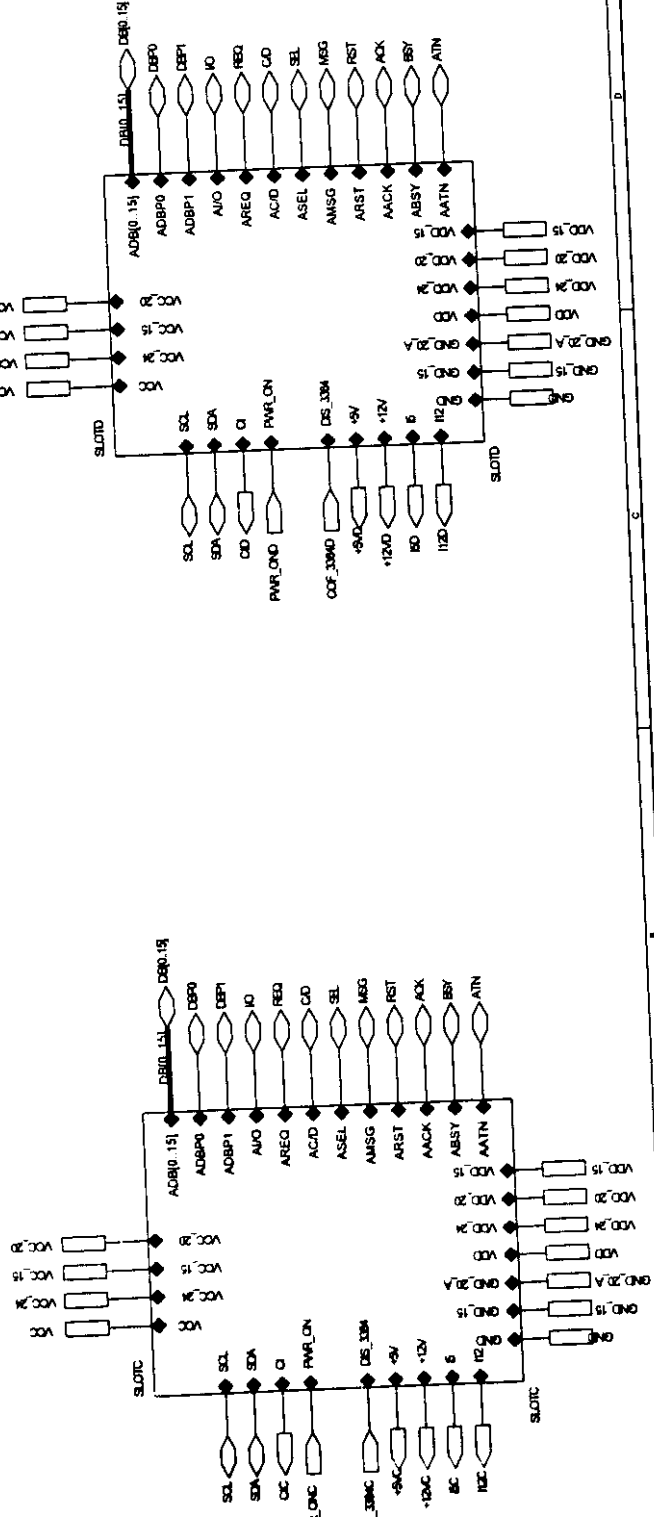
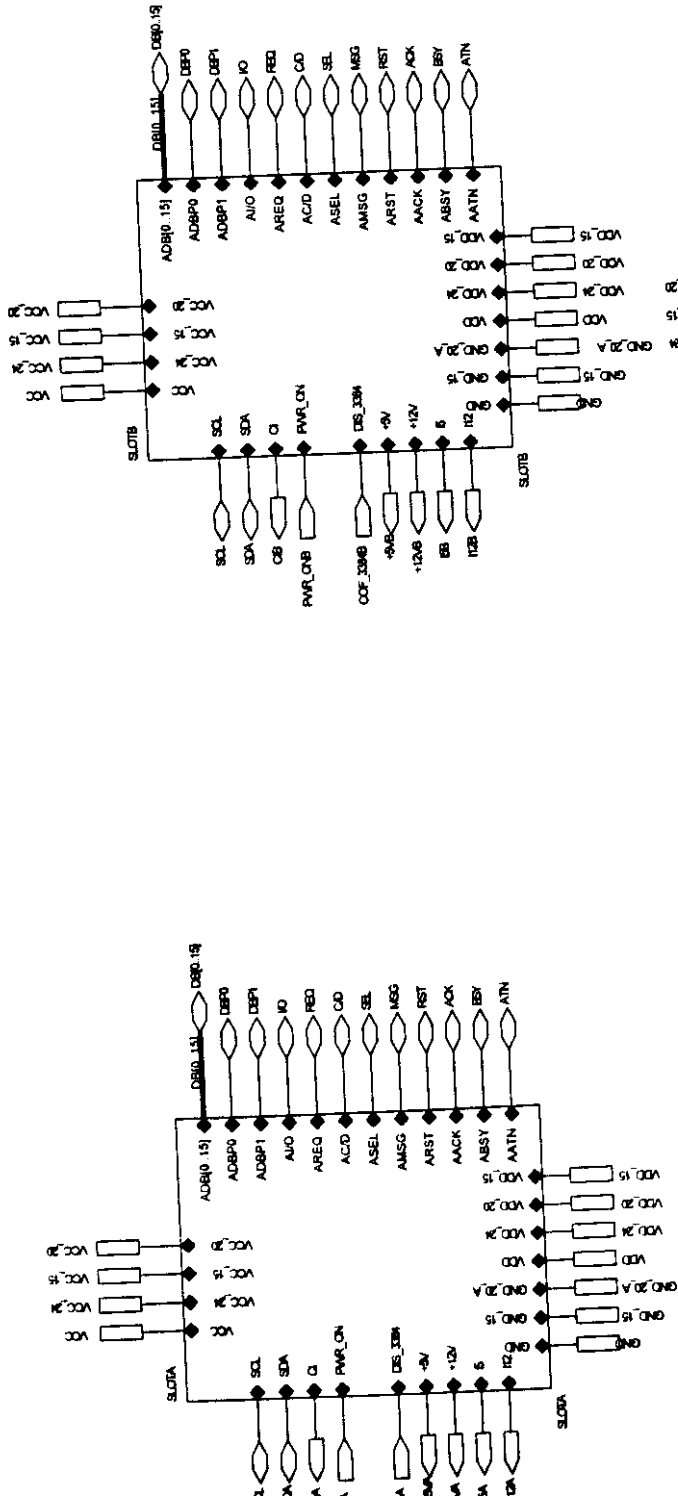
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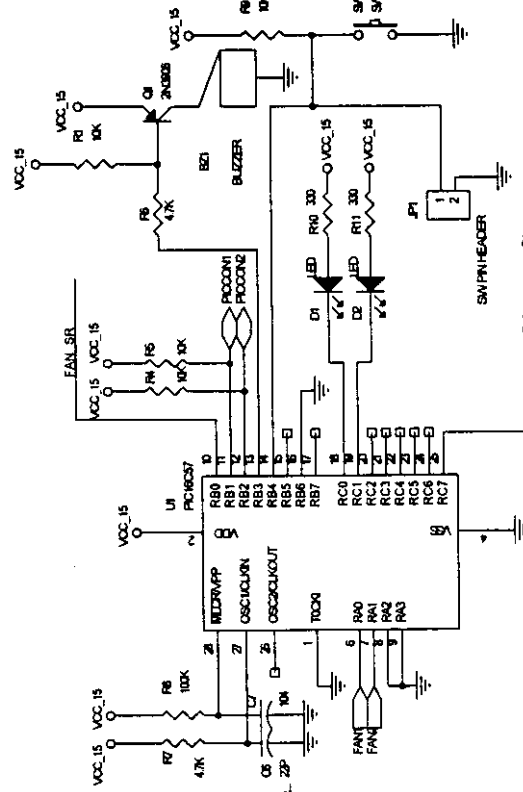
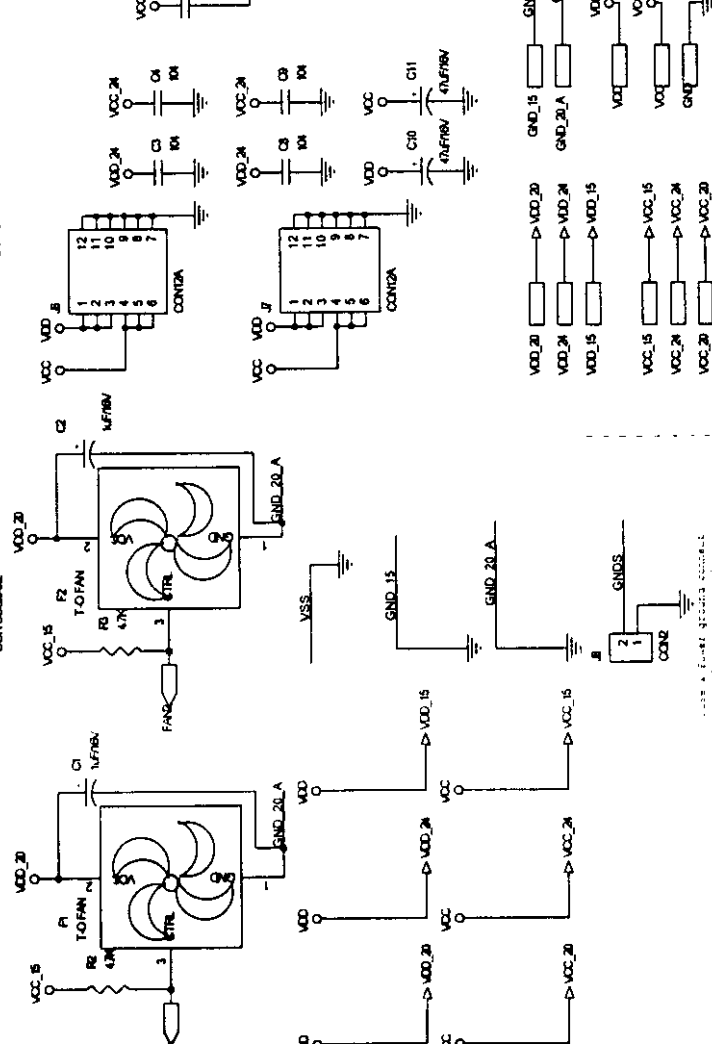
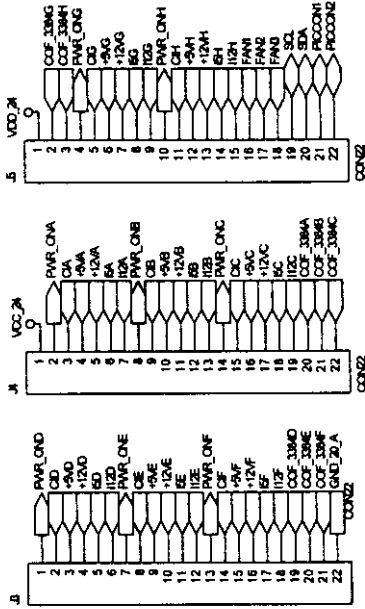
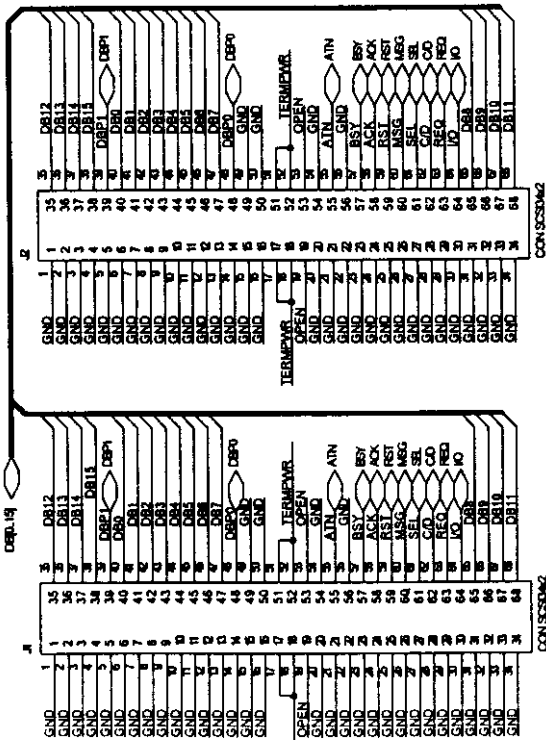
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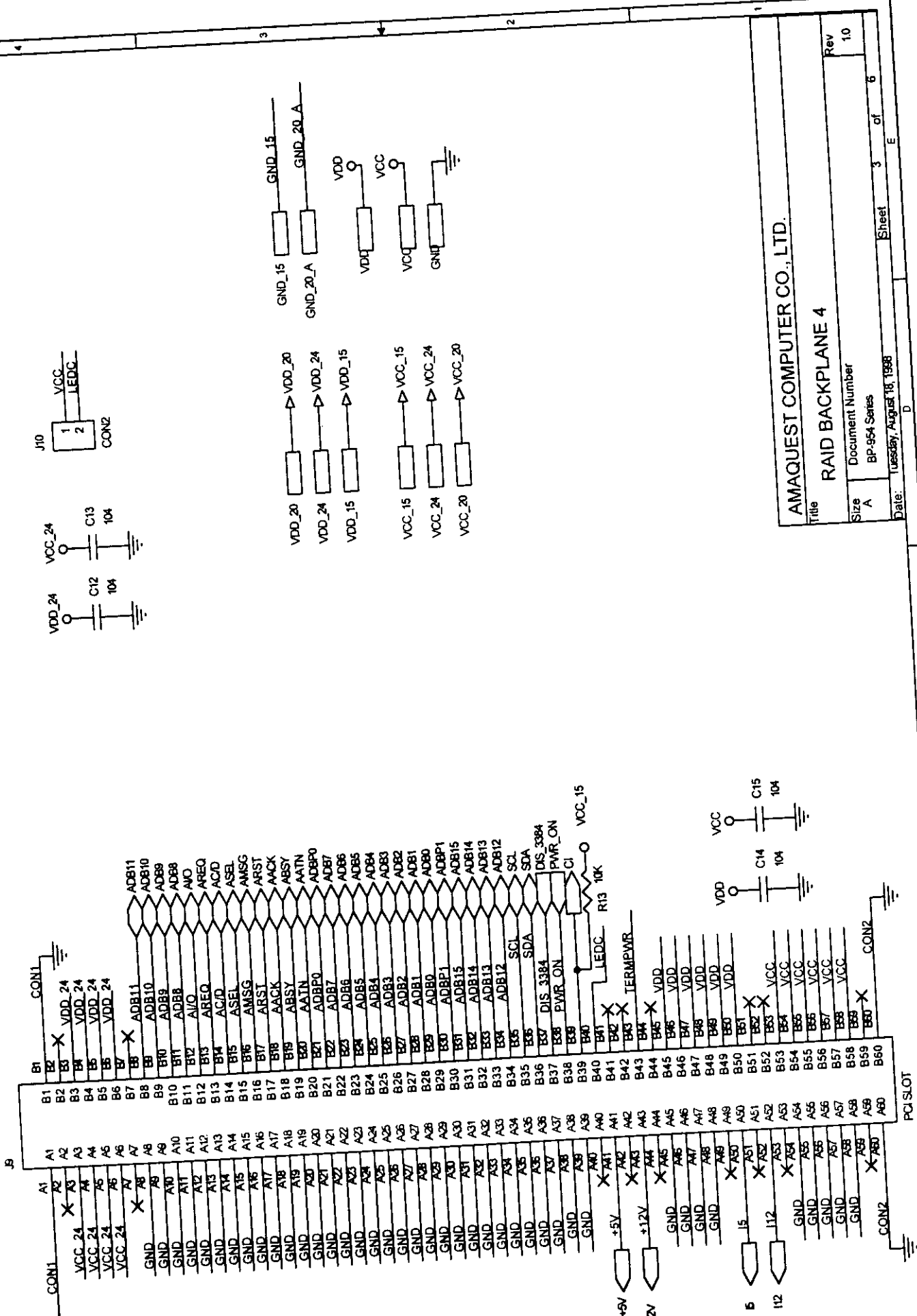
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RAID BACKPLANE 4

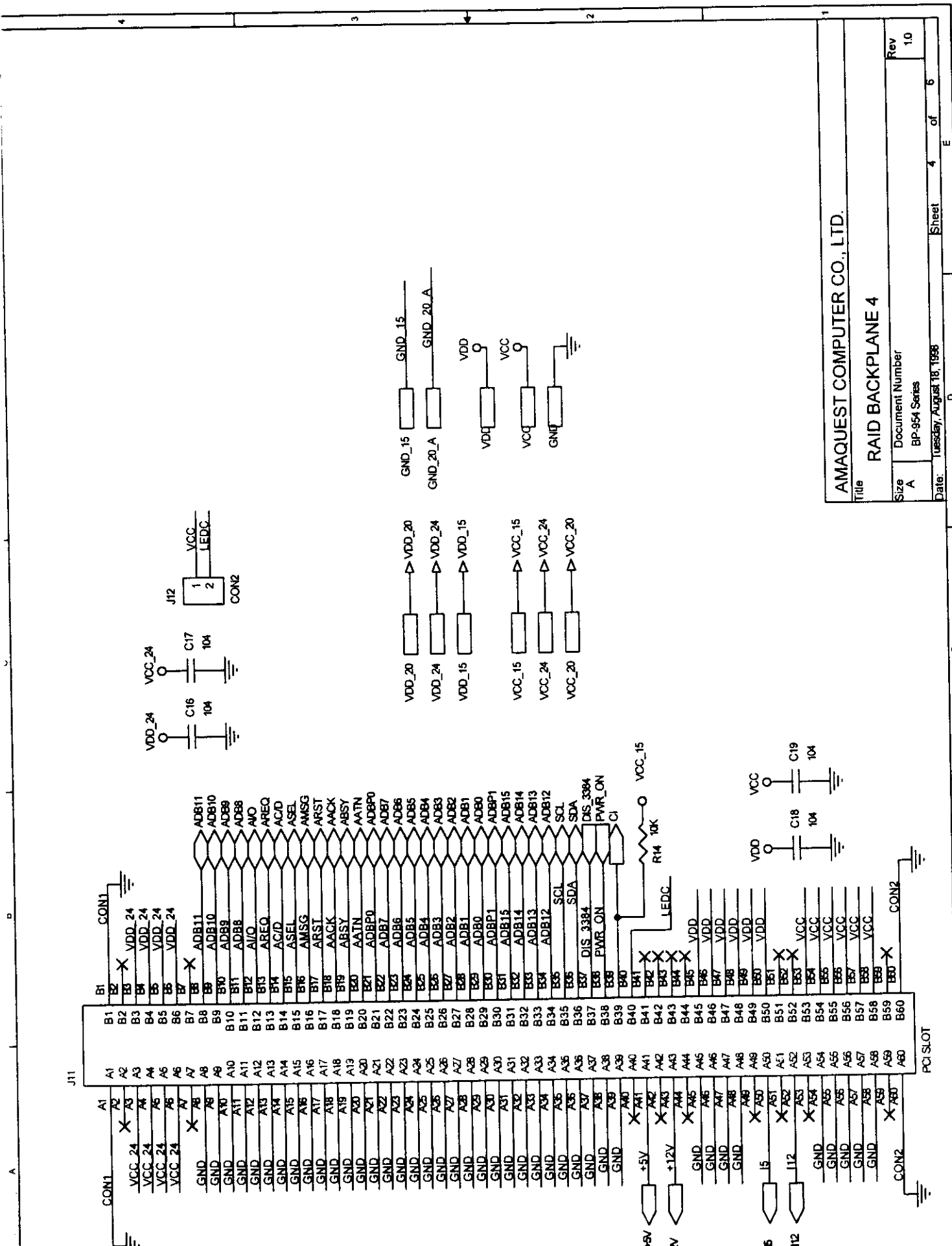
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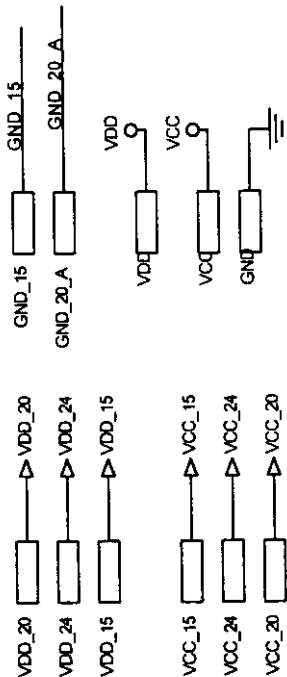
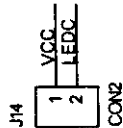
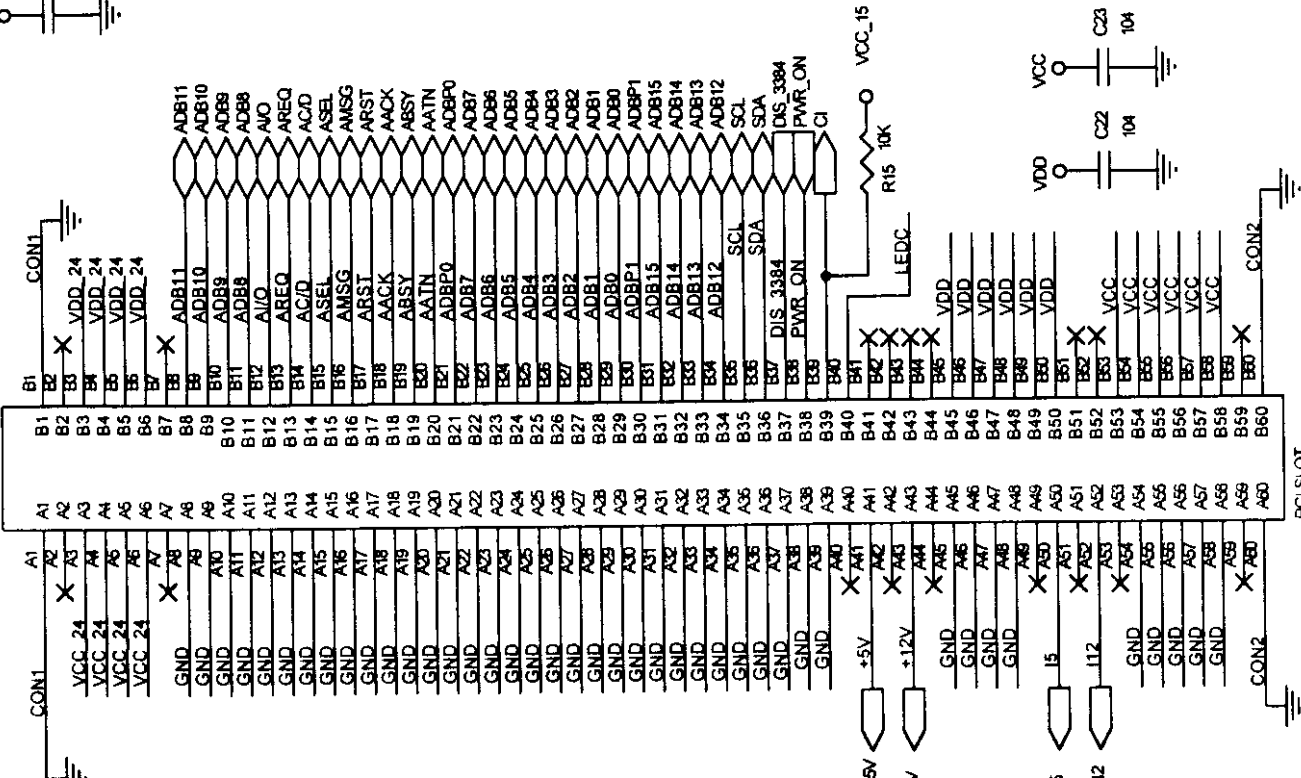


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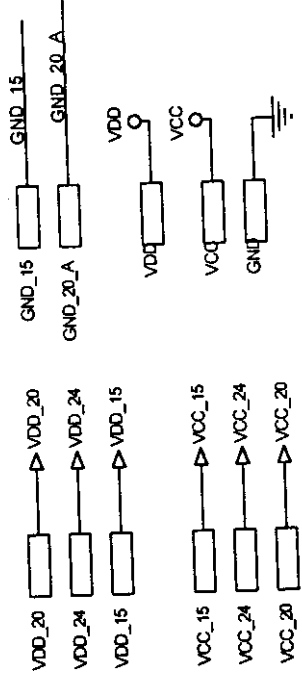
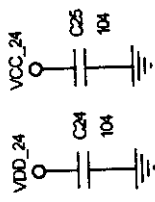
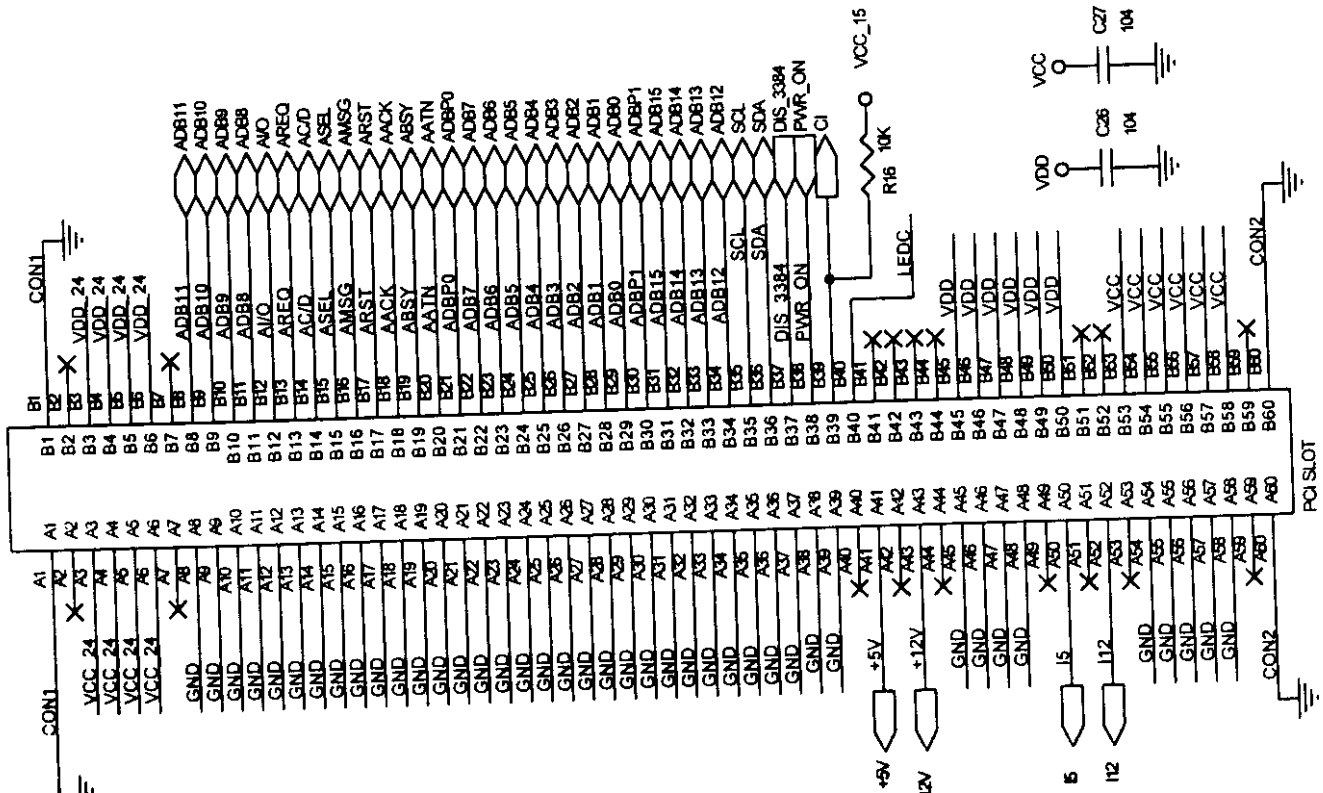


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RAID BACKPLANE 4

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