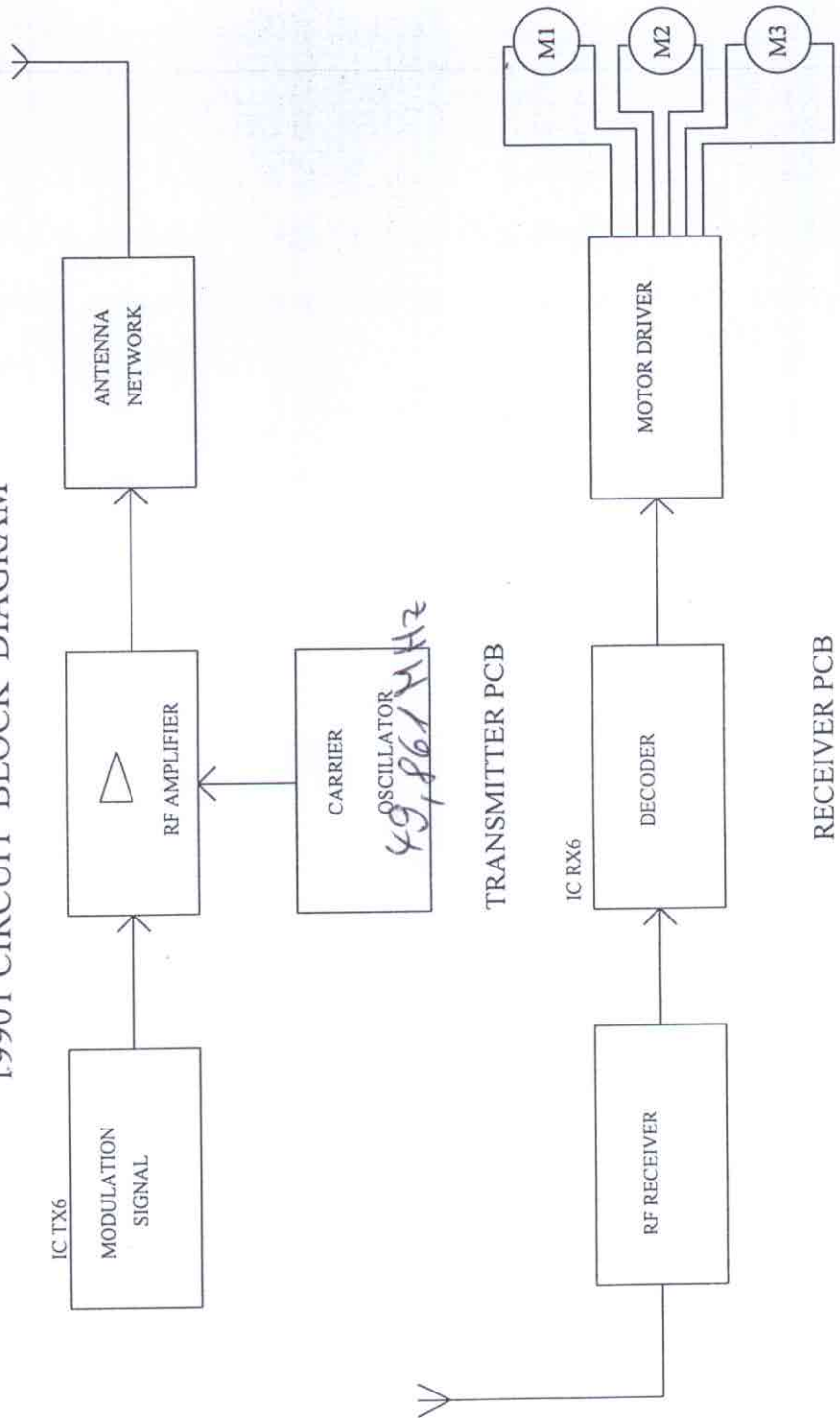


49MHz FCC conformity

Model 49001

19901 CIRCUIT BLOCK DIAGRAM



Title		Revision	
Size	Number		
A4			
Date:	7-Jul-2003	Sheet of	
File:	G:\98data\trivtr.Ddb	Drawn By:	
		TANG YUAN SONG	